

## MAINTENANCE MANUAL

#### SERVICE SECTION

FOR

#### CMX-8630

#### TWO-WAY MOBILE RADIOS

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#### DESCRIPTION

The Service Section contains the information necessary for aligning and troubleshooting the radio. In addition, information is provided for removing and replacing chip components, disassembly procedures and module replacement procedures.

Test point voltages and receiver bias voltages with audio waveforms are also provided.

#### INITIAL ADJUSTMENT

After the radio has been installed as described in the Installation Manual, the following adjustments should be made by a certified electronics technician.

#### TRANSMITTER ADJUSTMENT

The transmitter has been adjusted at the factory and should require no

readjustment. However, the antenna length should be adjusted for optimum VSWR, and the frequency and modulation measured and recorded for future reference. For the complete transmitter adjustment, refer to the Alignment Procedure (See Table of Contents).

#### RECEIVER ADJUSTMENT

No initial adjustments to the receiver are required. Refer to the Table of Contents for the complete receiver alignment.

#### RE-INSTALLATION

The CMX-8630 series mobile radios will operate in  $\frac{12-\text{Volt}}{\text{If}}$  the mobile radio is moved to a different vehicle, always check the battery polarity of the new vehicle system.



#### MAINTENANCE

#### PREVENTIVE MAINTENANCE

To ensure high operating efficiency and to prevent mechanical and electrical failures from interrupting system operations, routine checks should be made of all mechanical and electrical parts at regular intervals. Preventive maintenance should include the following checks:

#### Connections

Ground connections to the voltage source should be periodically checked for tightness. Loose or poor connections to the power source will cause excessive voltage drops and faulty operation. When ground connections are not made directly to the battery, the connection from the battery to vehicle chassis must be checked for low impedance. A high impedance may cause excessive voltage drops and alternator noise problems.

#### Electrical System

Check the voltage regulator and alternator or generator periodically to keep the electrical system within safe and economical operating limits. Overvoltage is indicated when the battery loses water rapidly. Usage of 1 or 2 ounces of water per cell per week is acceptable for batteries in continuous operation. A weak battery will often cause excessive noise or faulty operation.

#### Mechanical Inspection

Since mobile units are subject to constant shock and vibration, check for loose plugs, nuts, screws and parts to make sure that nothing is working loose.

#### Antenna

The antenna, antenna base and all contacts should be kept clean and free from dirt or corrosion. If the antennas or its base should become coated or poorly grounded, loss of radiation and a weak signal will result.

#### Alignment

The transmitter and receiver meter readings should be checked periodically, and the alignment "touched up" when necessary. Refer to applicable Alignment Procedure and Troubleshooting sheet for typical voltage readings.

#### Frequency Check

Check transmitter frequency and deviation. Normally, these checks are made when the unit is first put into

operation, after the first six months and once a year thereafter.

#### DISASSEMBLY

To gain access to the transmitter/receiver/synthesizer (TRS) or logic board for servicing, loosen the two screws (A) securing the top cover at the rear of the radio. Then pull the cover out from under the edge of the front panel and lift the cover off (See Figure 1).

#### To Remove Logic Board:

- 1. Remove the top cover.
- 2. Remove the four screws B securing the logic board to the system frame, and carefully lift up the board with the interconnecting cables attached.

#### To Remove the TRS Board:

- Remove the top cover and the logic board. Then unscrew the two standoffs supporting the logic board.
- 2. Remove the 11 screws C, D and E securing the RF shield, and remove the shield.
- 3. Remove the four screws F holding the front panel to the "U" frame. Disconnect speaker jack J24, and J25, and remove the front panel.
- 4. Remove the two screws G securing the antenna jack to the rear of the radio.
- 5. Remove the screws (H) in regulator A1, L51 and transistors Q7 and Q15.
- 6. Remove the two screws J in exciter module A9, and PA transistor Q12.
- 7. Remove the three screws (K) securing the TRS board, and carefully lift the board up off the bottom of the "U" frame and interconnection pins.

#### 30-Watt Driver Replacement

#### --- NOTE ---

Always check out the associated circuitry carefully before replacing the driver module. The module is a very reliable device and will not normally need replacing.

- To remove Driver All:
- 1. Remove the two screws securing All to the printed wire board.

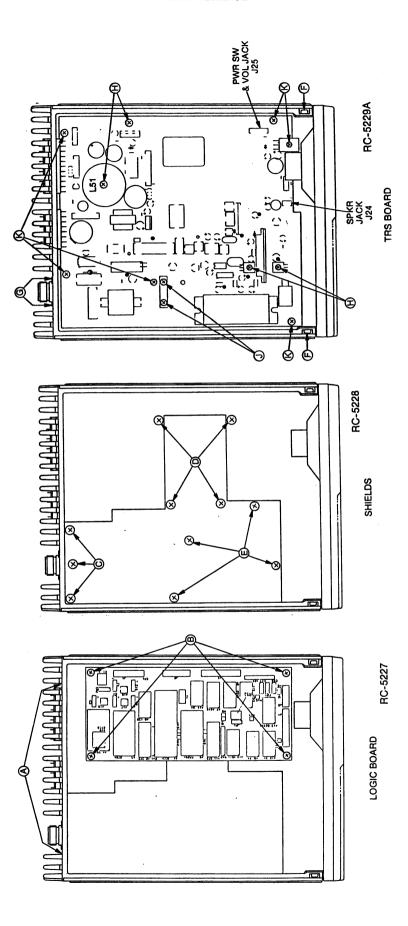
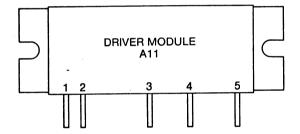


Figure 1 - Disassembly Procedure

 Unsolder the five leads bridging All to the printed board while lifting each lead as they are unsoldered.

Service Note: These leads are soft and can be bent very easily.

- 3. Lift out the module, taking care not to injure the spacer under the module. The module may also stick to the printed board.
- To replace Driver A11:
- Position the module properly, aligning the screw holes and leads with the printed board. Trim leads if necessary (see Figure 2).
- Replace the two screws securing the driver to the printed board, using a moderate torque of 0.5 ±0.1 Newton meter (5 inchpounds).
- Solder the five leads of driver All to the printed board and ground strap.



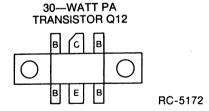


Figure 2 - Driver & PA Lead Identification

#### PA Transistor Replacement

#### - WARNING -

The PA transistor contains Beryllium Oxide, a TOXIC substance. If the ceramic or other encapsulation is opened, crushed, broken or abraded, the dust may be hazardous if inhaled. Use care when replacing the module.

- 1. Remove the two retaining screws securing PA transistor Q12 to chassis assembly.
- 2. Unsolder and remove capacitors C87, C88, C89 and C90. Use a desoldering tool as necessary while lifting the transistor leads with a small screwdriver or pick. Discard old capacitors.
- 3. Unsolder the emitter, base and collector leads of the transistor, and remove it from the printed board.
- 4. Remove all excess solder from the board, and clean the holes to allow the new transistor to be positioned properly and the capacitors to fit into proper locations. Refer to Figure 1 and trim leads of Q12 as shown.
- 5. Apply silicon grease to back of the replacement transistor and place the transistor into the mounting slot.
- 6. Replace the transistor mounting screws using a moderate torque of 0.5 Newton meter (4.5 inchpounds).
- 7. Tack solder the four base leads to the printed board, using minimum solder. Then solder the emitter and collector leads.
- 8. Install C87, C88, C89, C90 into their proper mounting areas, flush to the board.
- 9. Solder the capacitor bodies to the printed board by first soldering the outside edge. Then, holding the iron to the outside edge, touch the solder to the inside edge of the capacitor. Be careful not to create solder bridges at the front and back edges of the capacitors.
- 10. Remove any flux left on board.

#### REPLACING CHIP COMPONENTS

Replacement of chip capacitors should always be done with a temperature-controlled soldering iron, using a controlled temperature of 500°F (260°C). However, do NOT touch black metal film of the resistors or the ceramic body of capacitors with the soldering iron.

#### - NOTE -

The metalized end terminations of the parts may be touched with the soldering iron without causing damage.

#### To Remove Chip Components:

- Grip the component with tweezers or needle nose pliers.
- 2. Alternately heat each end of the chip in rapid succession until solder flows, and then remove and discard the chip.
- Remove excess solder with a vacuum de-soldering tool.
- 4. Carefully remove the epoxy adhesive and excess flux to prevent damage to the printed board.

#### To Replace Chip Components:

- Using as little solder as possible, "tin" one end of the component and one of the pads on the printed wiring board.
- 2. Place the "tinned" end of the component on the "tinned" pad on the board and simultaneously touch the component and the pad with a well "tinned" soldering iron while pressing the component down on the board.
- 3. Place the "tinned" soldering iron on the other end of the component and the pad simultaneously. Apply solder to the top of the end of the component until the solder starts to flow. Use as little solder as possible while getting a good joint.
- 4. After the component has cooled, remove all flux from the component and printed wiring board area with alcohol.

#### REMOVING IC'S

Removing IC's (and all other soldered-in components) can be easily accomplished by using a vacuum desoldering tool. To remove an IC, heat each lead separately on the solder side and remove the old solder with the desoldering tool.

#### TEST AND TROUBLESHOOTING PROCEDURES

Maintenance of radio is facilitated by using the Troubleshooting Procedures and servicing techniques unique to this radio. The Troubleshooting Procedures are designed to lead the serviceman rapidly to the defective component or circuit.

Troubleshooting Procedures are provided for most major problems that might arise in the Transmitter, Receiver, Logic and Synthesizer section of the radio.

#### SERVICING TECHNIQUES

The radio lends itself well to rapid isolation of malfunctions in the voltage and signal paths. A majority of the signals and voltages pass through the connectors on the TRS and Logic boards.

To isolate a signal or voltage path to determine loading effects, locate short circuits, etc. carefully insert an insulator (plastic wand, toothpick) between the appropriate pins of the related molex connector to create an open circuit. Signals paths that may be isolated include: MIC HI, DISC OUT, RX AUDIO, TX AUDIO, PTT, RX MUTE and SPKR HI, D-PTT, PTT, and Supply Voltages.

#### TEST POINTS

The TRS board contains 11 test points for monitoring the operation of the radio. The test points provide DC voltage readings required for aligning and troubleshooting the radio see Table of Contents).

#### RECEIVER VOLTAGE READINGS

Bias voltages for the receiver transistors and integrated circuits are shown on the Receiver Bias Voltage section listed in the Table of Contents.

#### --- CAUTION -



The CMOS Integrated Circuit devices used in this equipment can be destroyed by static discharges. Before handling one of these devices, the serviceman should discharge himself by touching the case of a bench test instrument that has a 3-prong power cord connected to an outlet with a known good earth ground. When soldering or desoldering a CMOS device, the soldering iron should also have a 3-prong power cord connected to an outlet with a known good earth ground. A battery-operated soldering iron may be used in place of the regular soldering iron.

GENERAL ELECTRIC COMPANY» MOBILE COMMUNICATIONS DIVISION WORLD HEADQUARTERS » LYNCHBURG, VIRGINIA 24502 U.S.A.

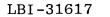


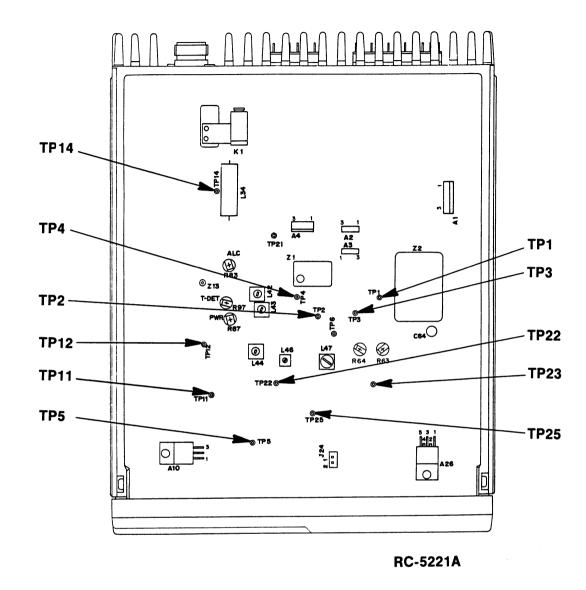
#### TEST POINTS

Test points are provided to aid in troubleshooting the radio and to facilitate alignment of the transmitter and receiver. The test points are located on the TRS board, and are identified in Table I. Test Point DC readings are typical readings measured with a 20,000-ohm-per-volt meter.

TABLE I - TYPICAL TEST POINT READINGS

Test Point	Location	Function	Typical Reading
TP1	Synthesizer	Synthesizer Control	2.0-6.0 V DC
TP2	Synthesizer and Receiver	+9 Volt regulator (A4)	+9 Volts
ТР3	Synthesizer	Collector bias of Q4	+8.1 Volts
TP4	Synthesizer	Collector bias of Q9	+8.1 Volts
TP5	Tx	Vcc of A9	+7.6 Volts
TP11	Tx	APC Voltage	+8.5 Volts
TP12	Tx	Vc3 of Pa module	+12.8 Volts
TP14	Tx	Collector bias of Q12	+12.5 Volts
TP22	Rx	2nd local frequency	44.545 MHz
TP23	Rx	Disc Out	300 mVrms
TP25	Rx	RSSI	0.3-2.4 Volts





# **TEST POINT DC VOLTAGE READINGS**

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# RECEIVER DC BIAS VOLTAGES (Receive Mode)

Receiver DC bias voltages are typical voltage readings measured with a high impedance analog DC voltmeter, and with the transmitter unkeyed.

	Collector	Base	Emitter	Gate	Source	Drain
Q21	6.9V DC	1.4V DC	0.85V DC			
Q22				GND	0.22V DC	4.9V DC
Q23	5.3V DC	0.6V DC	GND			
Q24	8.7V DC	8.1V DC	9.0V DC			

TA	40	•
10.	AZ	

A21-1	1.4V DC
A21-2	6.3V DC
A21-3	0.8V DC
A21-4	GND
A21-5	6.1V DC
A21-7	5.4V DC
A21-9	5.4V DC
A21-11	5.4V DC
A21-13	6.4V DC
A21-15	6.4V DC
A21-16	2.7V DC

#### IC A23

A23-1, 9	7.2V DC
A23-2, 8	3.6V DC
A23-3, 7	3.7V DC
A23-4, 6	3.6V DC
A23-5	GND

#### IC A25

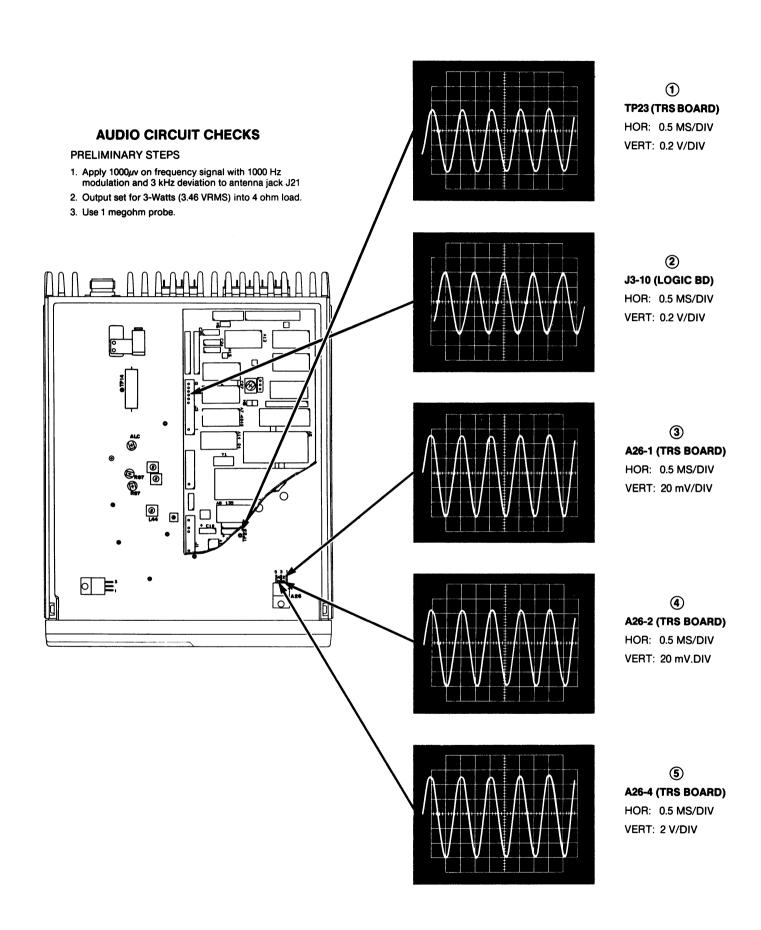
A25-1, 9	7.2V DC
A25-2, 8	3.6V DC
A25-3, 7	3.6V DC
A25-4, 6	3.6V DC
A25-5	GND

#### IC A26

A26-1	0.7V DC
A26-2	0.7V DC
A26-3	GND
A26-4	6.2V DC
A26-5	13.4V DC

# **RECEIVER VOLTAGE READINGS**

DC Bias Readings & Audio Waveforms



#### TRANSMITTER ALIGNMENT

As the transmitter uses a broadband exciter and PA, and the VCO receiver LO injection frequency is the transmit frequency, no transmitter alignment procedure is required. However, some adjustments are required to optimize operation. The adjustments include:

TXCO Frequency adjustment (FREQ ADJUST Z1)

Transmitter deviation (MOD ADJUST R64)

Channel Guard deviation (VR5 on the Logic Board)

Output power level (PWR LEVEL R87 and R107)

Temperature control adjustment (T-DET R97)

VSWR adjustment (ALC R83)

#### TEST EQUIPMENT

Audio Oscillator

Wattmeter (50-watts) with 50-ohm load

Deviation monitor

Directional coupler

DC Voltmeter (20,000 ohms-per-volt)

13.8-Volt regulated power supply

Frequency counter

AC Voltmeter

#### FREQUENCY ADJUSTMENT

- 1. Connect the wattmeter and frequency counter to antenna jack J21.
- 2. Key the transmitter and adjust FREQ control Z1 for the correct frequency.

#### MODULATION LEVEL ADJUSTMENT

- Disable the Channel Guard. If a non-Channel Guard channel is not available, connect the jumper at JP1 on the logic board from JP1-1 to JP1-2. Replace jumper in JP1-2 and JP1-3 after setting the modulation.
- Connect the audio oscillator and the AC voltmeter across audio input terminals J22-4 (Hi) and J22-5 (Lo) on the Transmitter/Receiver board.
- 3. Adjust the audio oscillator for a 1V RMS at 1000 Hz.
- 4. Connect 50 ohm load to antenna jack.
- 5. Key the transmitter and adjust R64 on the Radio board for 3.5  $\ensuremath{\,kHz}$  deviation.

#### CHANNEL GUARD MODULATION ADJUSTMENT

- Select a Channel Guard channel.
- 2. Key the transmitter and adjust CG Mod adjust VR5 on the logic board for 0.75 kHz deviation.

#### AUDIO SENSITIVITY

- . Connect the audio oscillator output across J22-4 (MIC Hi) and J22-3 (MIC Lo). Adjust output for 1000 Hz at 1.0V RMS.
- Set to frequency
- 3. Reduce the oscillator output until deviation falls to 2.25 kHz. Voltage should be less than 120 millivolts.

#### POWER CONTROL ADJUSTMENT

The Power Control Adjustments have been set at the factory and will normally not require re-adjustment. The controls are labeled on the synthesizer shield, and include:  $\frac{1}{2} \left( \frac{1}{2} \right) \left( \frac$ 

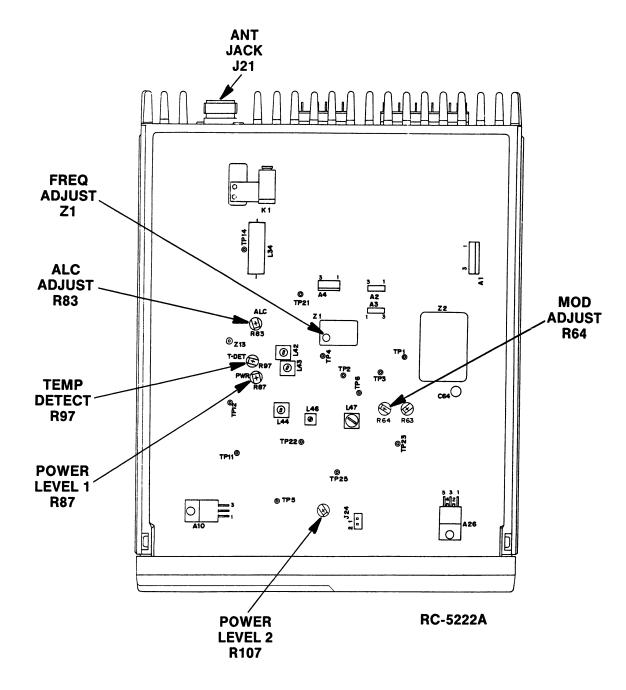
- 1. Output power level R87 (PWR1) and R107 (PWR2)
- 2. VSWR control R83 (ALC)
- 3. Temperature detect R97 (T-DET)

#### PRELIMINARY ADJUSTMENTS

- 1. Connect the thru-line wattmeter and 50-ohm load to antenna jack J21.
- 2. Set PWR and ALC controls fully counterclockwise (minimum setting).

#### PROCEDURE

Step	Test Point	Tuning Control	Indication	Procedure	
	Power Control				
1.	Wattmeter	R87 (PWR1) R107 (PWR2)	See Procedure	Key the transmitter and adjust R87/R107 for rated power output.	
2.	Wattmeter	R83 (ALC)	See Procedure	Disconnect the 50-ohm load from the thruline wattmeter. Key the transmitter and set R83 for a supply current of 6 amperes maximum. Perform this setting within 10 seconds.  Reconnect 50-ohm load to wattmeter.	
3.	Wattmeter	R97 (T-DET)	See Procedure	Connect the lead of 213 to ground with a short wire. Key the transmitter and set R97 for rated power output minus 2dB.	
	Final Frequency Adjustment				
4.	Frequency Counter	FREQ Adjust (ON TCXO)	Transmit Frequency and RF Injection Frequency	Key the transmitter and adjust FREQ control Z1 to the correct transmitter frequency.	



### TRANSMITTER ALIGNMENT

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#### RECEIVER ALIGNMENT

#### Equipment Required

- 1. RF Signal Generator (851 MHz 870 MHz)
- 2. Analog DC Voltmeter (with high input impedance and millivolt scale)
- 3. Frequency Counter (up to 1000 MHz with 0.05V sensitivity)
- 4. Audio Level Meter and Distortion Analyzer

#### Preliminary Adjustments

- 1. Connect the DC Voltmeter to TP25.
- 2. Connect RF Signal Generator to antenna jack J21.
- 3. Set the Signal Generator on the receive frequency with  $\pm 4$  kHz deviation and 1 kHz modulation.
- 4. Set top of tuning slug in L46 even with the top of the coil form.

#### - NOTE

- 1. Adjust the Signal Generator to keep the DC voltage readings between 1.0V to 1.5V (below saturation) during the alignment procedure.
- 2. Make sure that the transmitter is properly aligned before aligning the receiver.

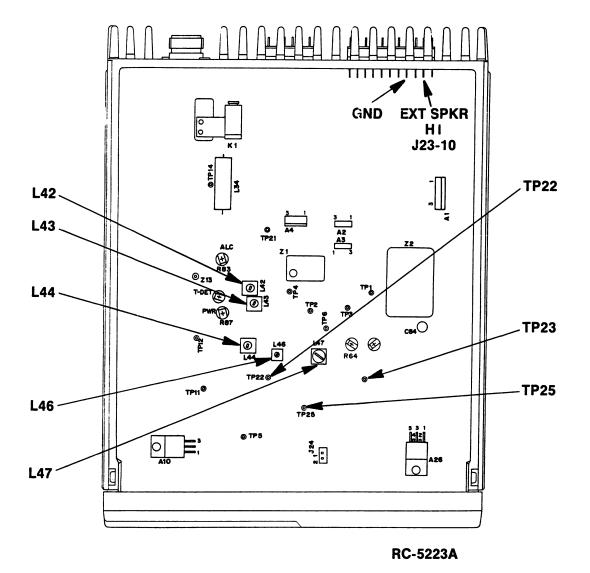
#### Alignment Procedure

Step	Test Point	Tuning Control	Meter Reading	Procedure
1.	TP22	L46	See Procedure	Connect RF signal probe from the frequency counter to TP22.  Check for a reading of 44.545 MHz ±1 Hz.
2.	TP25	L42	Maximum	Set the Signal Generator output for a meter reading of 1.0V DC.  Tune L42 for maximum meter reading.
3.	TP23	L43 L44	See Procedure	Connect the audio distortion meter to TP23 (use a high impedance meter).  Alternately tune L43, L44 for minimum meter reading on distortion, and maximum meter reading on DC voltage.

Step	Test Point	Tuning Control	Meter Reading	Procedure
4.	TP23	L47	Maximum	Set the RF signal level to 1 millivolt.  Tune L47 for maximum audio output level, and check for a meter reading of approximately 300 millivolts RMS.
5.	TP25	L43 L44	See Procedure	Set the Signal Generator for ± 3 kHz deviation with 1 kHz modulation and repeat Step 3.
6.	J23-10		See Procedure	Disconnect P24 and connect the audio level meter to J23-10. Adjust VOLUME control for 3 watts (3.46V RMS across 4-ohm load).
7.				Measure audio distortion (see Receiver Test Procedure). Distortion should be less than 5%. Disconnect all test equipment. Re-connect Speaker Plug P24 to J24.

# **RECEIVER ALIGNMENT**

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#### RECEIVER TEST PROCEDURES

#### Test Procedures

These Test Procedures are designed to help you to service a receiver that is operating --- but not properly. The problems encountered could be low power, poor sensitivity, distortion, and low gain. By following the sequence of test steps starting with Step 1, the defect can be quickly localized. Once the defective stage is pin-pointed, refer to the "Service Check" listed to correct the problem. Additional corrective measures are included in the Troubleshooting Procedure. Before starting with the Receiver Test Procedures, be sure the receiver is tuned and aligned to the proper operating frequency and the transmitter is operating properly.

Test Equipment Required

- 1. Distortion Analyzer
- 2. Signal Generator
- 3. 6-dB Attenuation Pad, and 4.0-ohm, 5-Watt Resistor

#### - NOTE -

The receive frequency is always 45 MHz above the transmit frequency except for talk-around, where the transmit frequency is the same as the receive frequency.

#### TEST PROCEDURE

STEP 1: Audio Output and Distortion

Measure Audio Power Output as follows:

- Apply a 1000 microvolt, on-frequency test signal modulated by 1,000 Hz with +3.0 kHz deviation to antenna jack J21.
- в. With 3 Watt Speaker

Disconnect speaker plug if present.

Connect a 4.0-ohm, 5-Watt load resistor across the external speaker

Connect the Distortion Analyzer input across the resistor.

- C. Adjust the VOLUME control for rated power output of 3 Watts (3.46 VRMS across the 4-ohm load) using the Distortion Analyzer as a voltmeter.
- Make distortion measurements according to manufacturer's instructions. Reading should be less than 5%. If the receiver sensitivity is to be measured, leave all controls and equipment as they are.

#### SERVICE CHECK

If the distortion is more than 5%, or maximum audio output is less than 3 Watts, make the following checks: Battery and regulator voltage --- low voltage will cause distortion.

Troubleshooting Procedure).

FM Detector Alignment (Refer to Receiver Alignment).

STEP 2: Usable Sensitivity (12 dB SINAD)

If Step 1 checks out properly, measure the receiver sensitivity as follows:

- A. Apply a 1000-microvolt, on-frequency signal modulated by 1000 Hz with 3 kHz deviation to J21.
- B. Place the RANGE switch on the Distortion Analyzer in the 200 to 2000 Hz distortion range position (1000-Hz filter in the circuit). Tune the filter for minimum reading or null on the lowest possible scale (100%, 30%, etc.)
- C. Place the RANGE switch to the SET LEVEL position (filter out of the circuit) and adjust the input LEVEL control for a +2 dB reading on a mid range (30%).
- D. Set signal generator output to 0.3 V. Switch the RANGE control from SET LEVEL to the distortion range. Readjust Distortion Analyzer SET LEVEL as required until a 12 dB difference (+2 dB to -10 dB) is obtained between the SET LEVEL and distortion range positions (filter out and filter in).
- E. The 12 dB difference (Signal plus Noise and Distortion to noise plus distortion ratio) is the "usable" sensitivity level. The sensitivity should be less than rated 12 dB SINAD specifications with an audio output of at least 1.5 Watts (0.56V RMS across the 4-ohm receiver load using the Distortion Analyzer as a Voltmeter).

Audio Gain (Refer to Receiver F. Leave all controls as they are and all equipment connected if the Modulation Acceptance Bandwidth test is to be performed.

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#### SERVICE CHECK

If the sensitivity level is more than rated 12 dB SINAD, check the alignment of the RF stages as directed in the Alignment Procedure.

STEP 3: Modulation Acceptance (Bandwidth or IF Bandwidth)

If Steps 1 and 2 check out properly, measure the bandwidth as follows:

- Set the Signal Generator output for twice the microvolt reading obtained in the 12 dB SINAD measurement.
- B. Set the Range control on the Distortion Analyzer in the SET LEVEL position (1000 Hz filter out of the circuit), and adjust the input LEVEL control for a +2 dB reading on the 30% range.
- While increasing the deviation of the Signal Generator, switch the RANGE control from SET LEVEL to distortion range until a 12 dB difference is obtained between the SET LEVEL and distortion range readings (from +2 dB to -10 dB).
- The deviation control reading for the 12 dB difference is the Modulation Acceptance Bandwidth of the receiver. It should be more than +6.5 kHz.

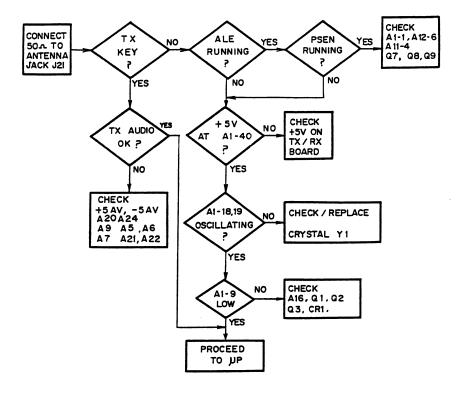
#### SERVICE CHECK

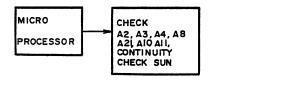
If the Modulation Acceptance Bandwidth test does not indicate the proper width, refer to the Receiver Troubleshooting Procedure.

#### RECEIVER TEST PROCEDURES

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#### TROUBLESHOOTING PROCEDURE LOGIC FLOW CHART





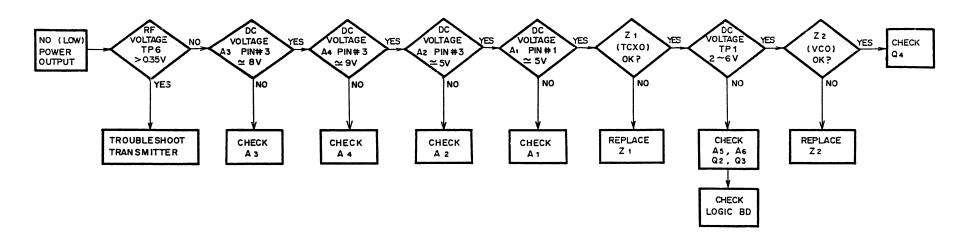
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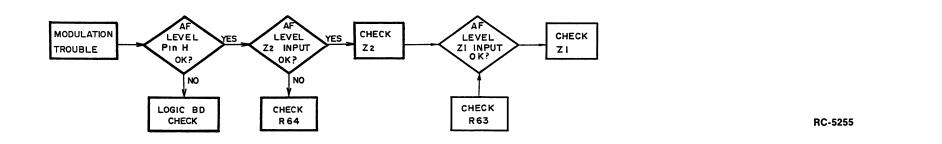
# TROUBLESHOOTING PROCEDURE

Logic Board & Synthesizer

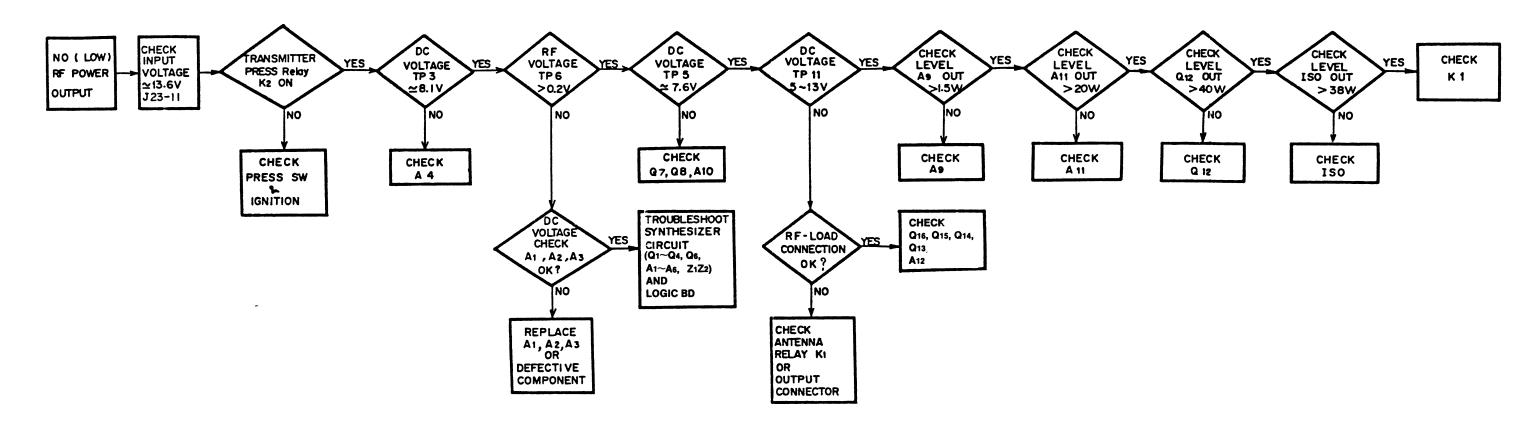
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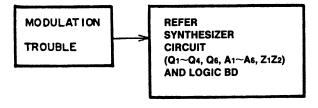
# TROUBLESHOOTING PROCEDURE SYNTHESIZER FLOW CHART





# TROUBLESHOOTING PROCEDURE TRANSMITTER FLOW CHART





#### TEST PROCEDURE

- 1. CONNECT AUDIO OSCILLATOR ACROSS J 22-4 (MIC HI) AND J22-3 (MIC LO).
- 2. SET OSCILLATOR FOR 1000 HZ AT 1.0 V RMS AND DEVIATION TO 3.5 kHz.

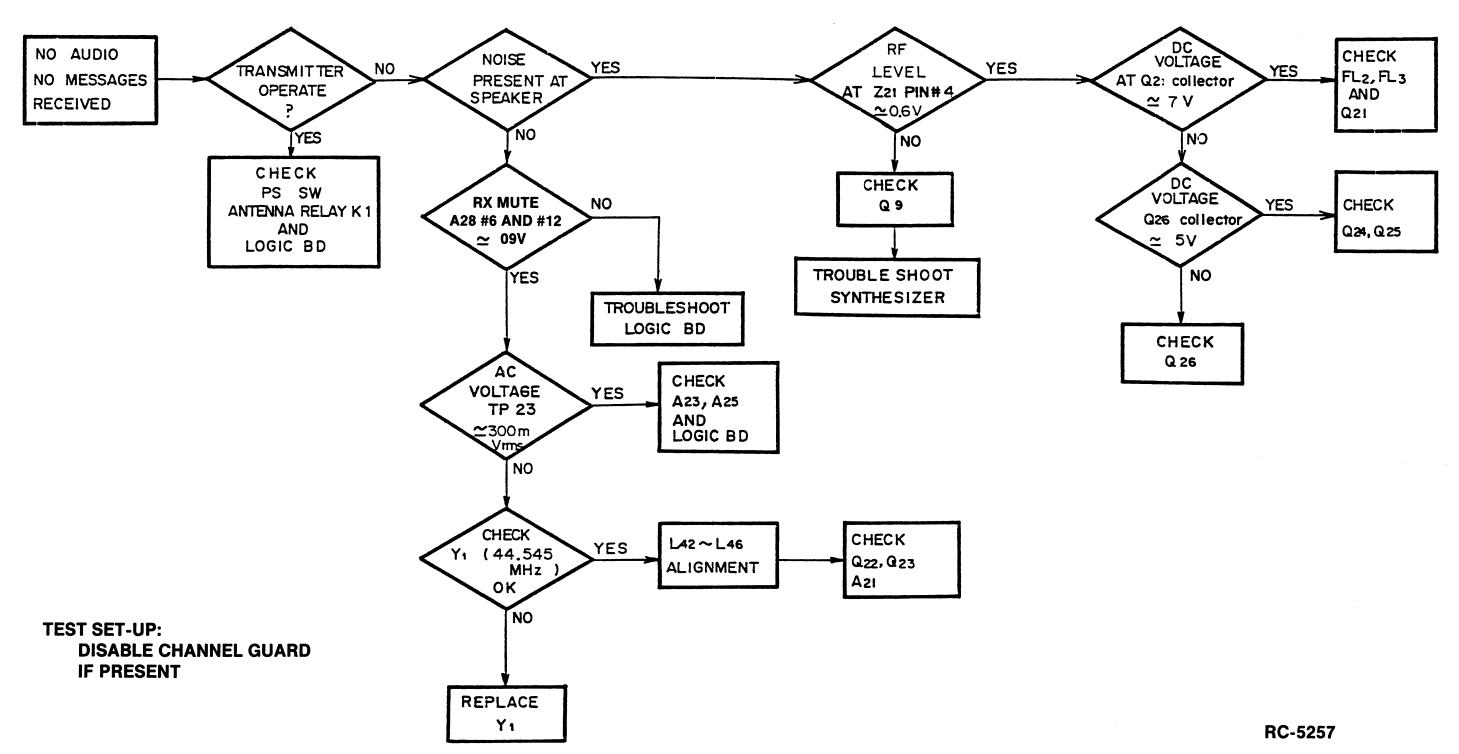
AN RMS OR PEAK READING VOLTMETER WILL
READ 1/2 TO 1/3 OF PEAK-TO-PEAK READING

## TROUBLESHOOTING PROCEDURE

Transmitter

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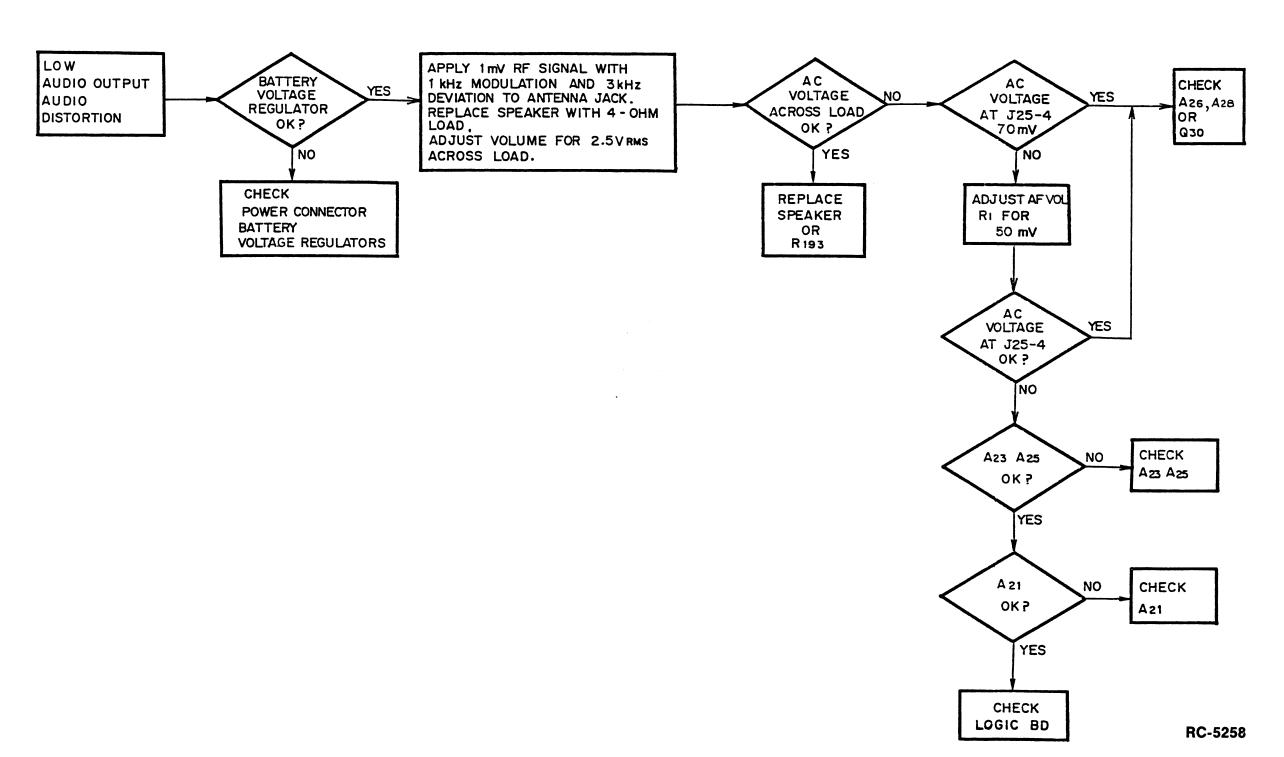
# TROUBLESHOOTING PROCEDURE RECEIVER (1/3) FLOW CHART



### TROUBLESHOOTING PROCEDURE

Receiver (Sheet 1 of 3)

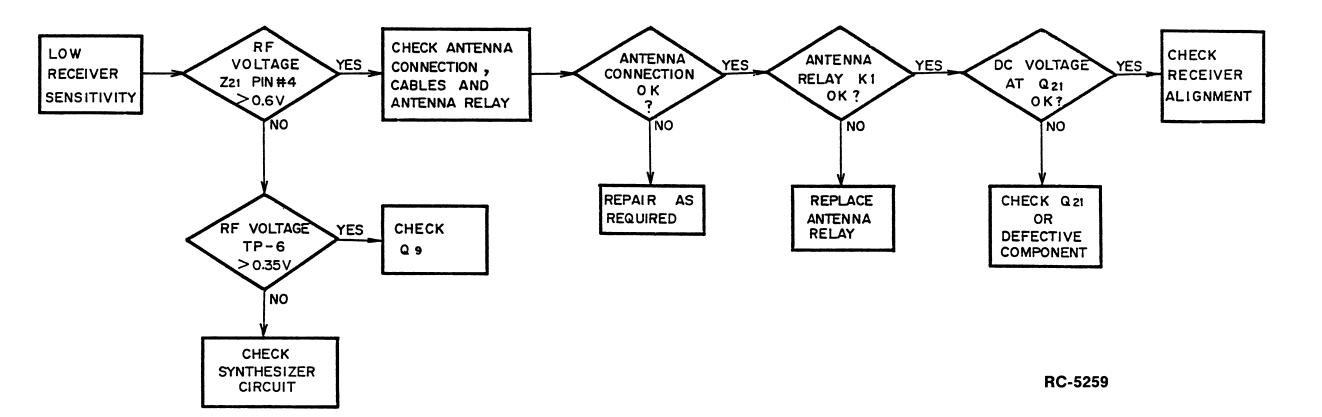
# TROUBLESHOOTING PROCEDURE RECEIVER (2/3) FLOW CHART



## TROUBLESHOOTING PROCEDURE

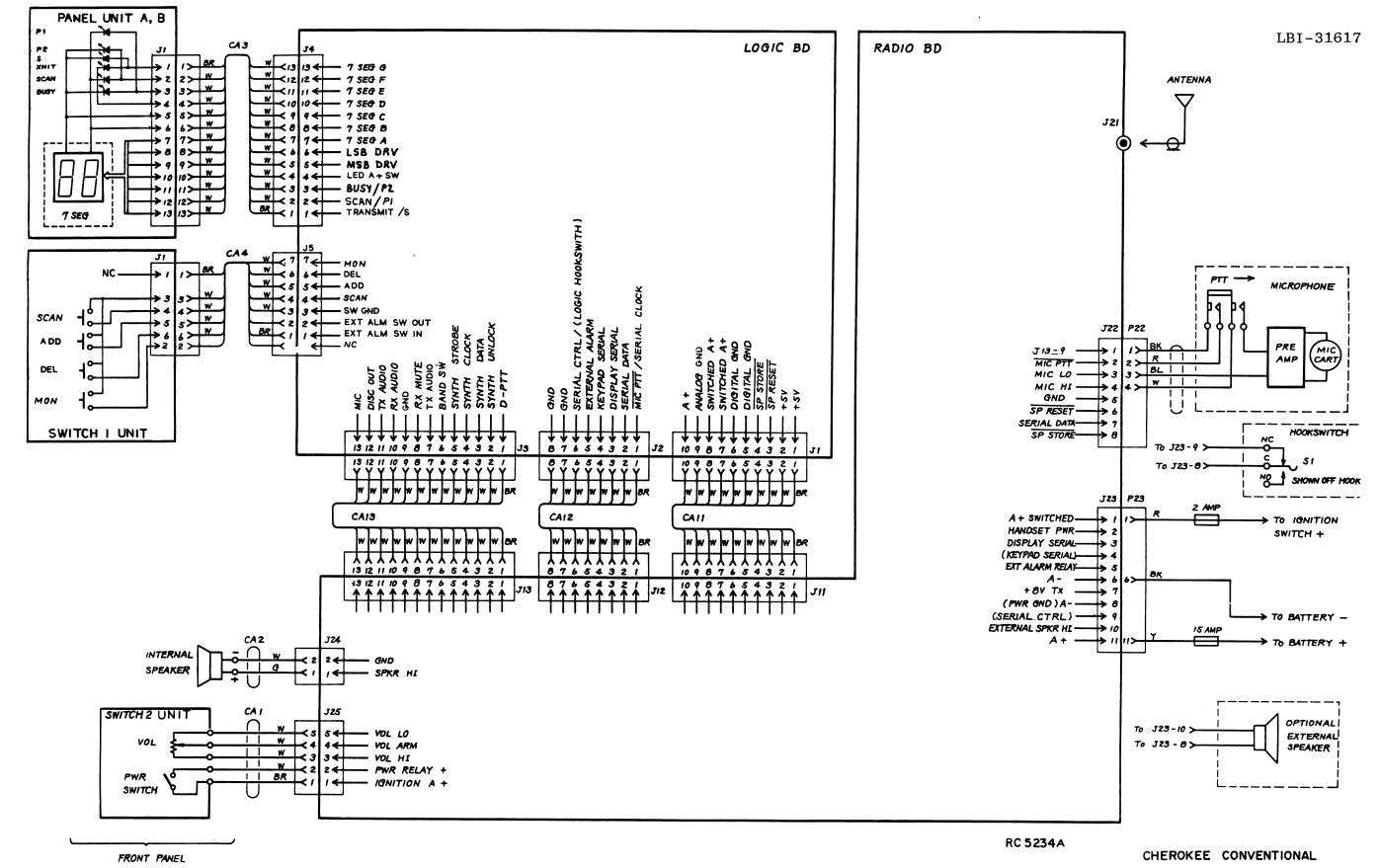
Receiver (Sheet 2 of 3)

# TROUBLESHOOTING PROCEDURE RECEIVER (3/3) FLOW CHART



# TROUBLESHOOTING PROCEDURE

Receiver (Sheet 3 of 3)



SYSTEM INTERCONNECTION DIAGRAM

## **INTERCONNECTION DIAGRAM**

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