

**MAINTENANCE MANUAL
VOTER CROSSCONNECT INTERFACE**

TABLE OF CONTENTS

	<u>Page</u>
Description	Front Cover
Assembly Diagram	3
Digital/Analog Cross Connect panel, 1 Channel, 12 Sites (Cabinet)	5
Digital/Analog Cross Connect panel, 2 Channel, 6 Sites (Cabinet)	6
Digital/Analog Cross Connect panel, 3 Channel, 2 Sites (69" Cab; 4 Sites 83" Cab/86" Rack)	7
Digital Voter Cross Connect Panel Pin-Out	8
Analog Voter Cross Connect Panel Pin-Out	15

DESCRIPTION

The purpose of this manual is to serve as a reference document to define the interface between the Voter Crossconnect Panel and the Control Point Site (including a Control Point with a co-located Transmit Site) and the Control Point GETC'S and Telephone Interconnect equipment. This manual defines the voter interface for digital and analog voter applications for various site and channel configurations. These diagrams are adaptable to all system configurations and will facilitate field installation and cable hook-up.

Included in the manual are the Outline diagram for the Voter Crossconnect Panel, voter interface cabling for the Digital and Analog Crossconnect Panels (Field Installation Diagrams), and Jack Pin-outs for the Digital and Analog Voter Crossconnect Panels.

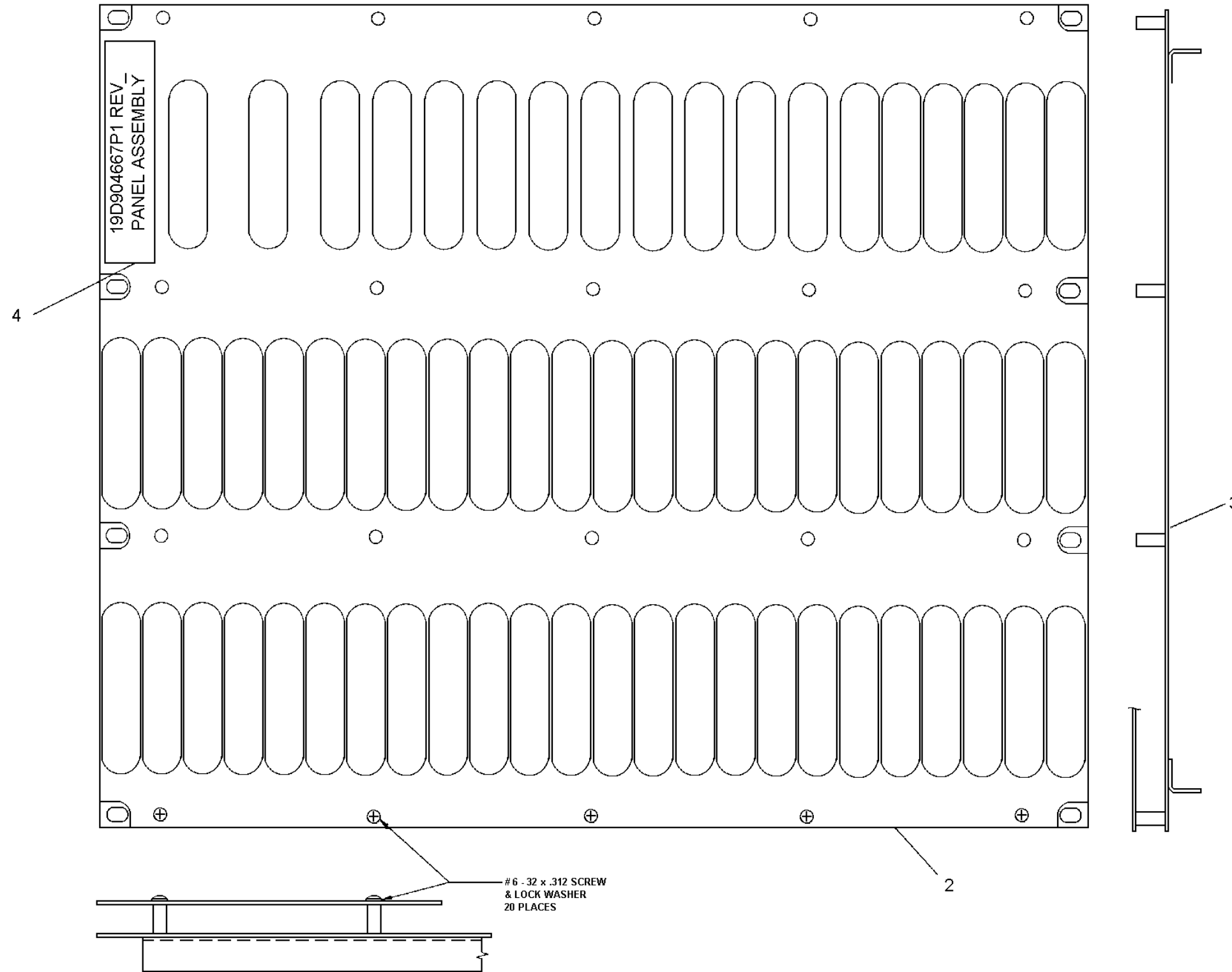
INTERRACK CABLING

The Interrack Cabling (Field Installation Diagrams) show the interface cabling for voted audio and data signals as well as the telephone interconnect, console and GETC interface cabling. Typical diagrams are provided for a single channel system with 12 sites; a 2 channel system with 6 sites; and 3 channel system with 2 sites.

Field Installation diagram 19C337787 shows the interface cabling for site audio and data interconnections between the voter digital and analog cross connect panels. It also shows the interconnections for the Telephone interconnect equipment, the Control Point GETC's, and the Console Switch.

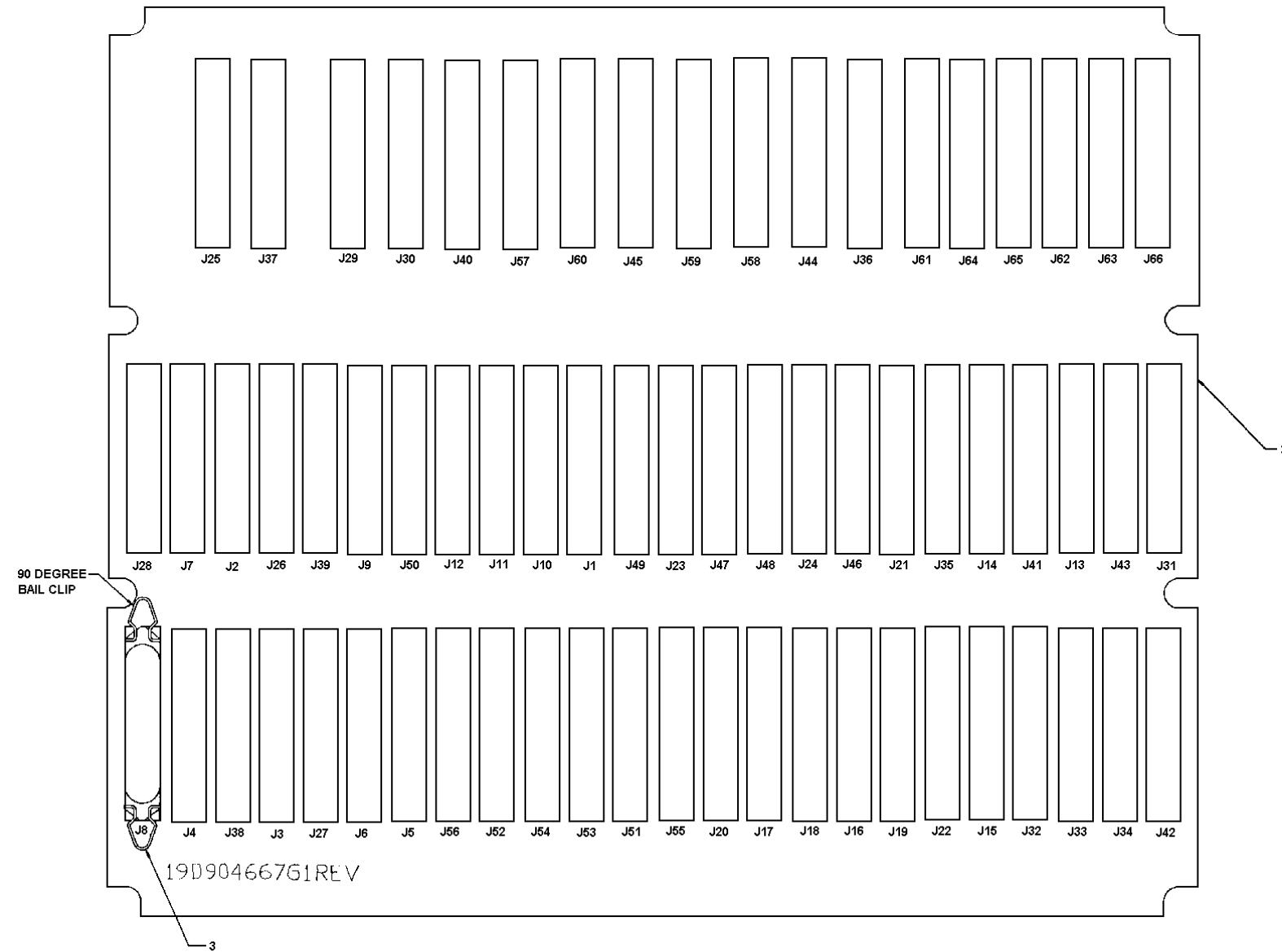
JACK PIN-OUTS

Jack pin-out diagrams identify the function and name of the signal on each pin for all jacks on the cross connect and voter system panels.



VOTER CROSSCONNECT PANEL

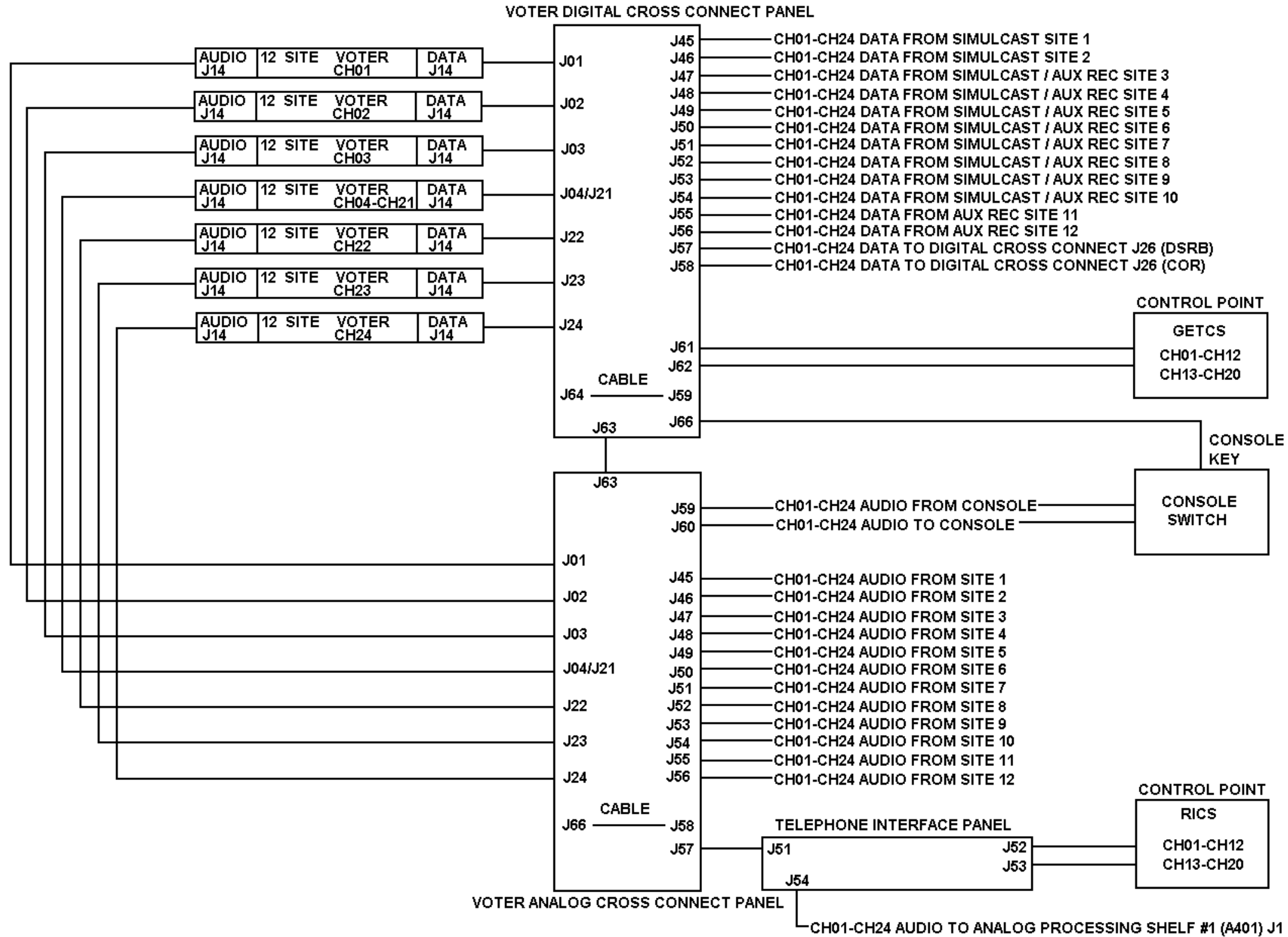
(19D904667P1 Sh. 1)



19D904667G1 MATERIAL LIST			
ITEM	IDENTIFICATION	DESCRIPTION	QTY
J1-66	19B800935P12	CONN., P.W.	66
2	19D904666P1REV2	BD., P.W.	1
3	19B800935P16	BAIL CLIP	66
4	19D904668REV0	PWB FAB. DIAG	X

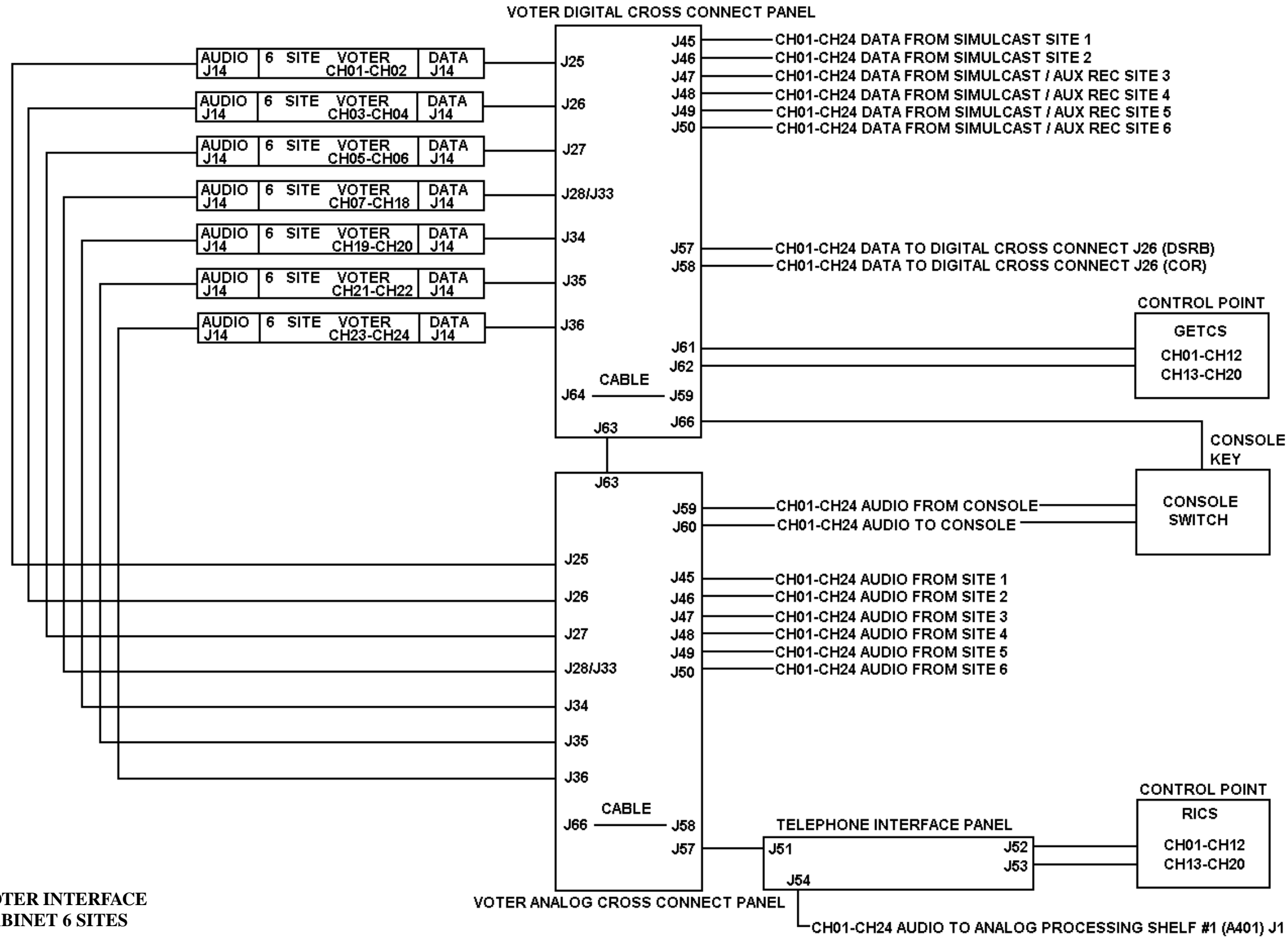
VOTER CROSSCONNECT PANEL

(19D904667P1 Sh. 2)



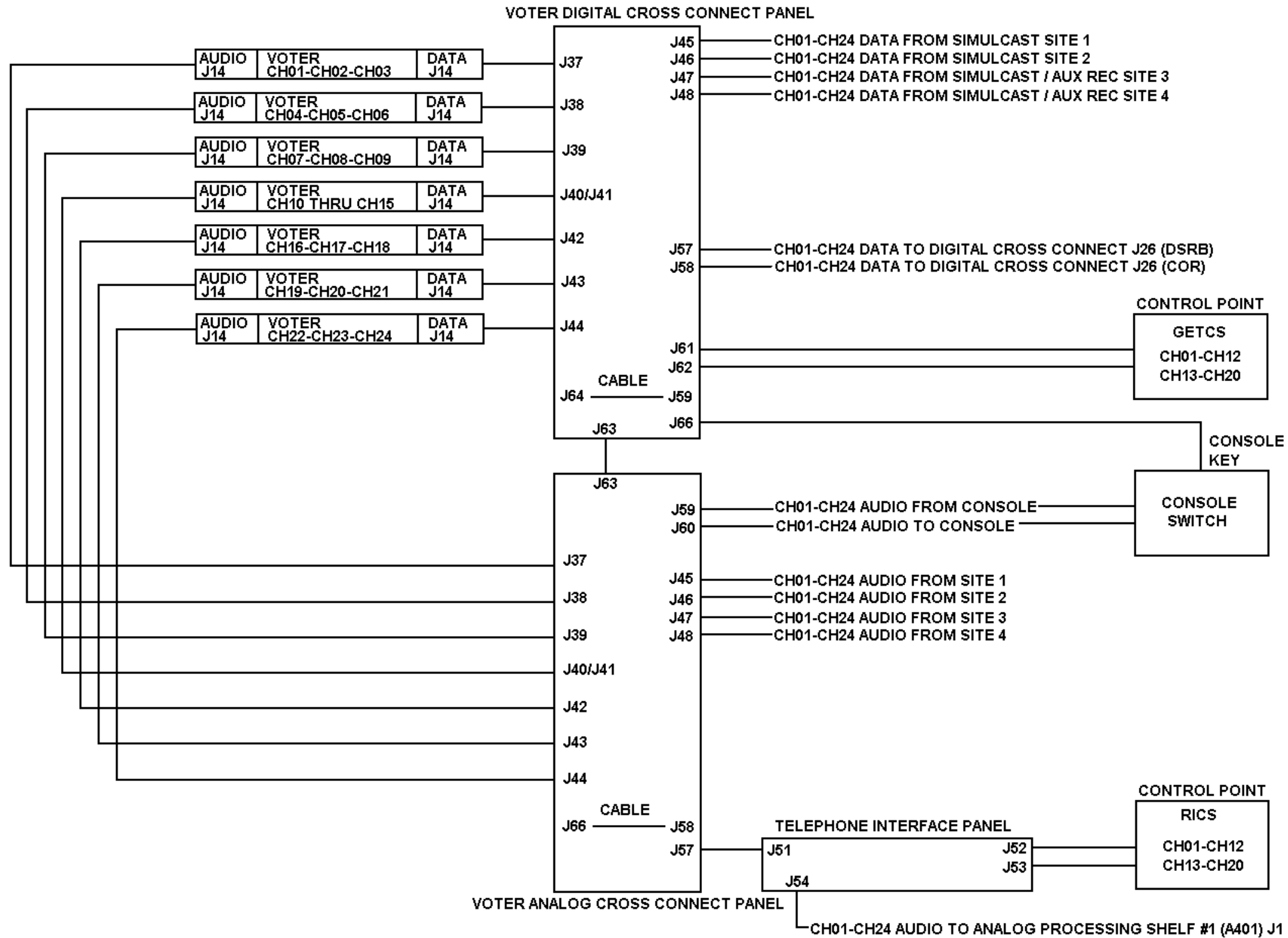
**SIMULCAST VOTER INTERFACE
1-CHANNEL/CABINET 12 SITES**

(19C337787 Sh. 1, Rev. 1)



SIMULCAST VOTER INTERFACE
2-CHANNEL/CABINET 6 SITES

(19C337787 Sh. 2, Rev. 1)

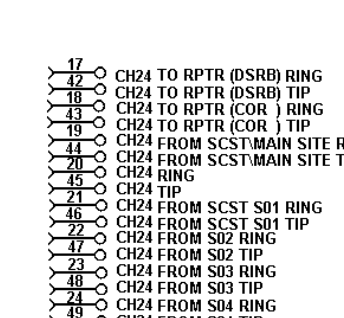
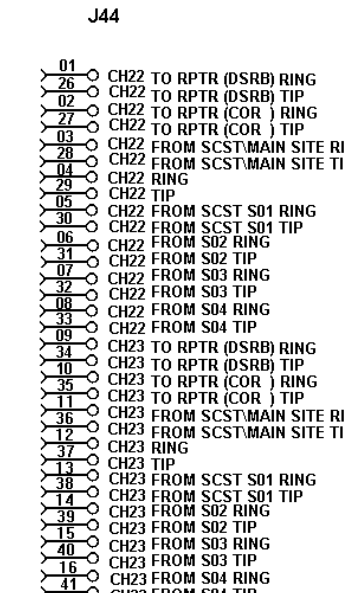
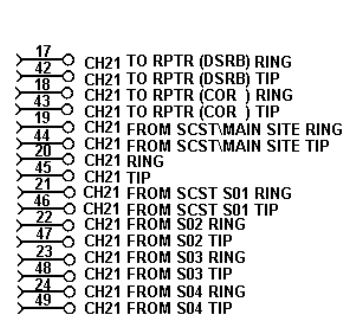
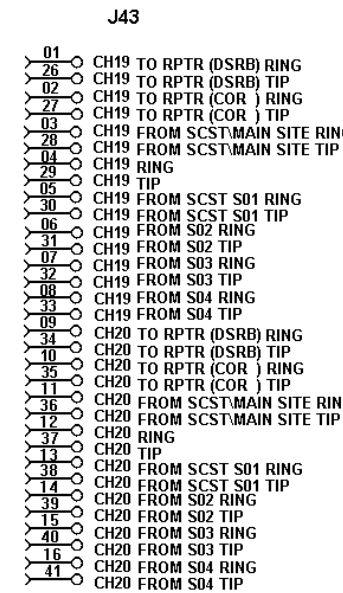
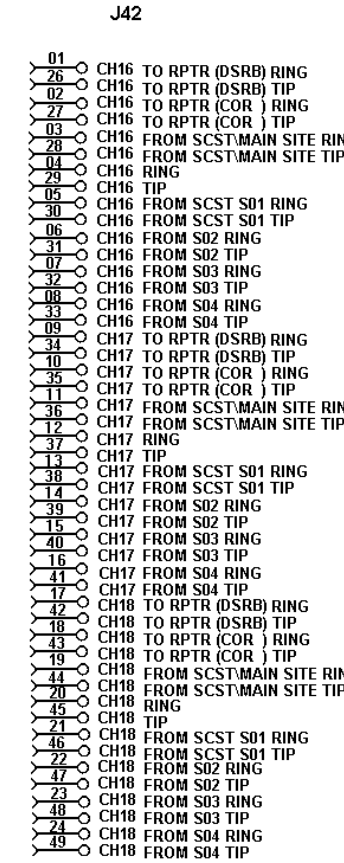
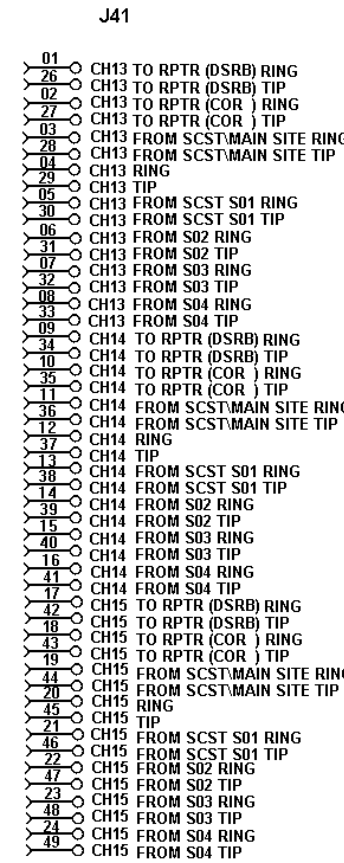
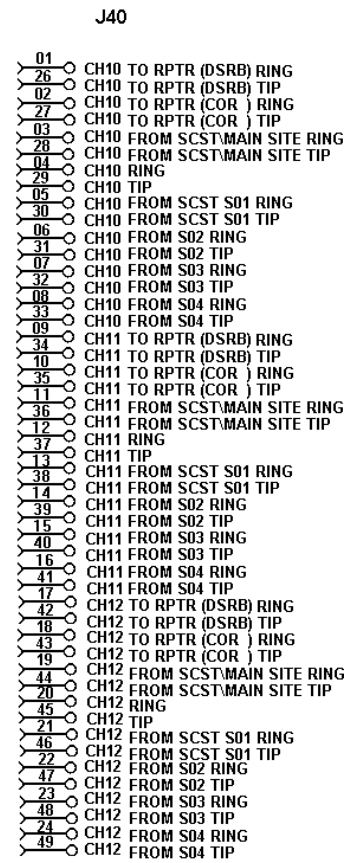
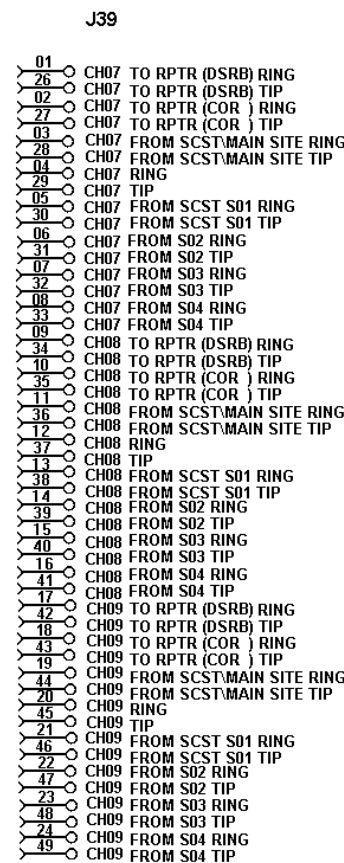
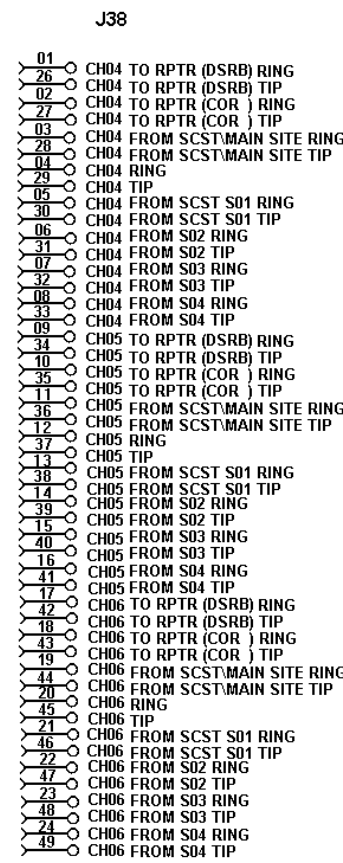
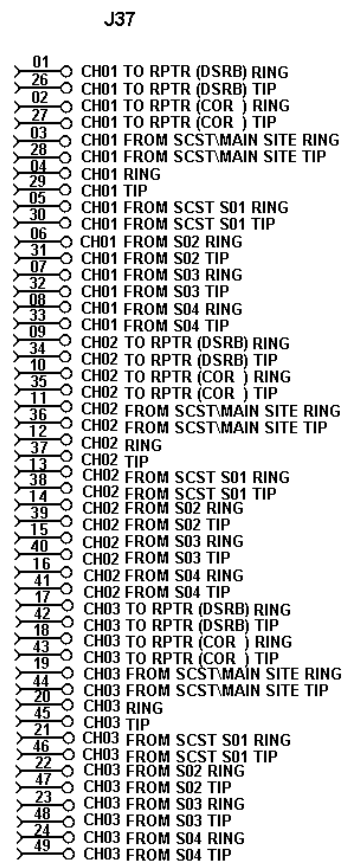


SIMULCAST VOTER INTERFACE
3-CHANNEL/CABINET 2 SITES (69" CAB), 4 SITES (83" CAB/86" RACK)

(19C337787 Sh. 3, Rev. 1)

PIN-OUT DIAGRAM

LBI-39142

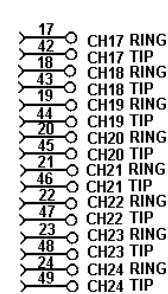
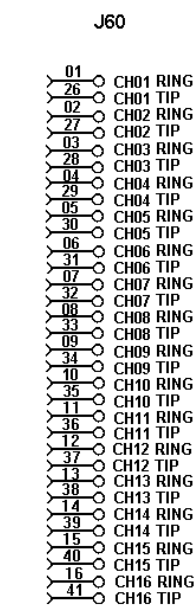
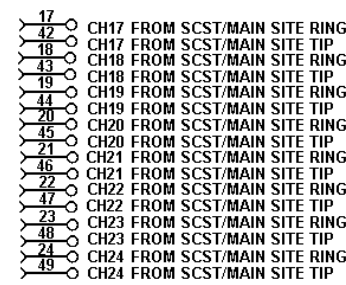
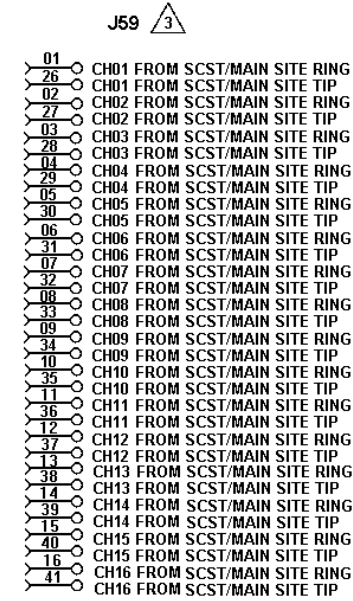
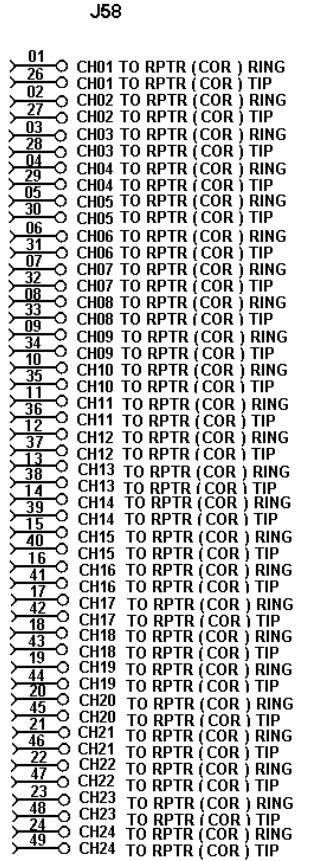
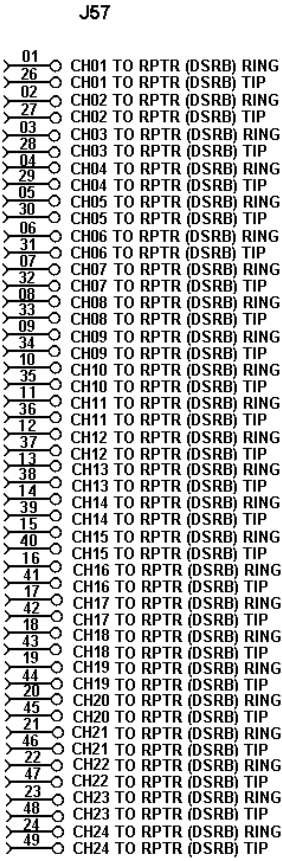
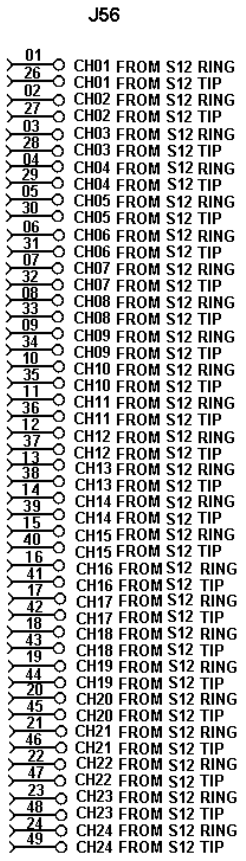
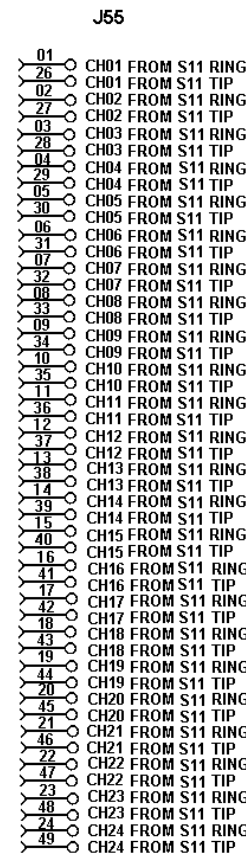
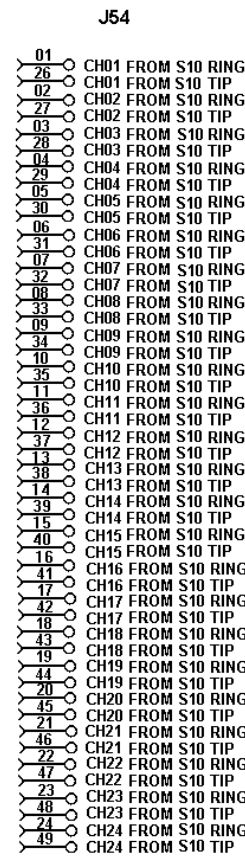
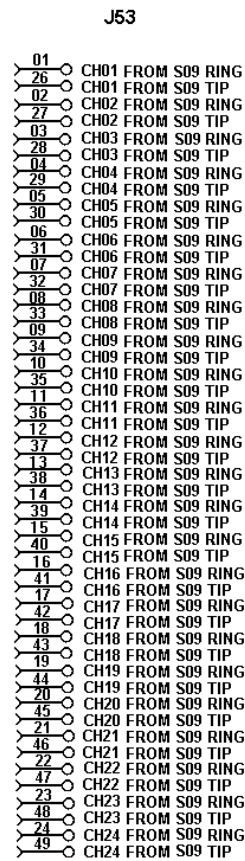


VOTER SYSTEM PANEL, DIGITAL JACK PIN OUT DIAGRAM

(19D904931 Sh. 4, Rev. 0)

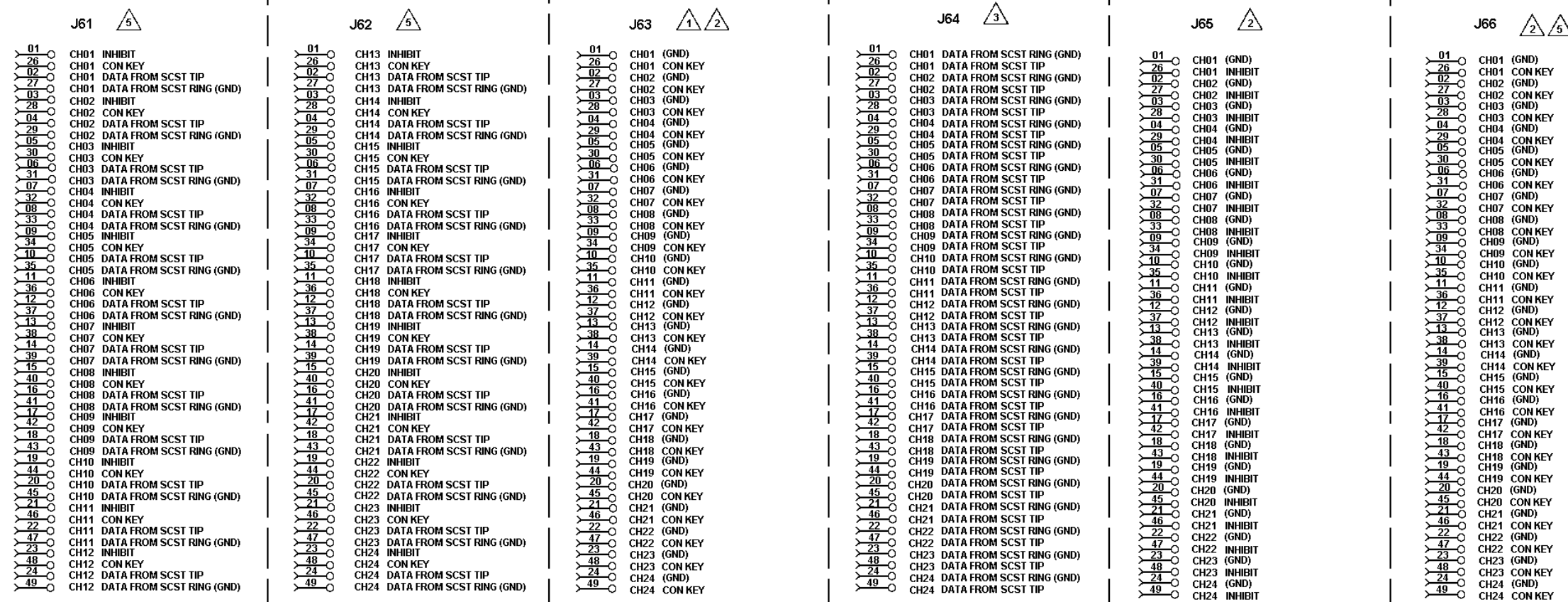
PIN-OUT DIAGRAM

LBI-39142








VOTER SYSTEM PANEL, DIGITAL JACK PIN OUT DIAGRAM

(19D904931 Sh. 6, Rev. 0)

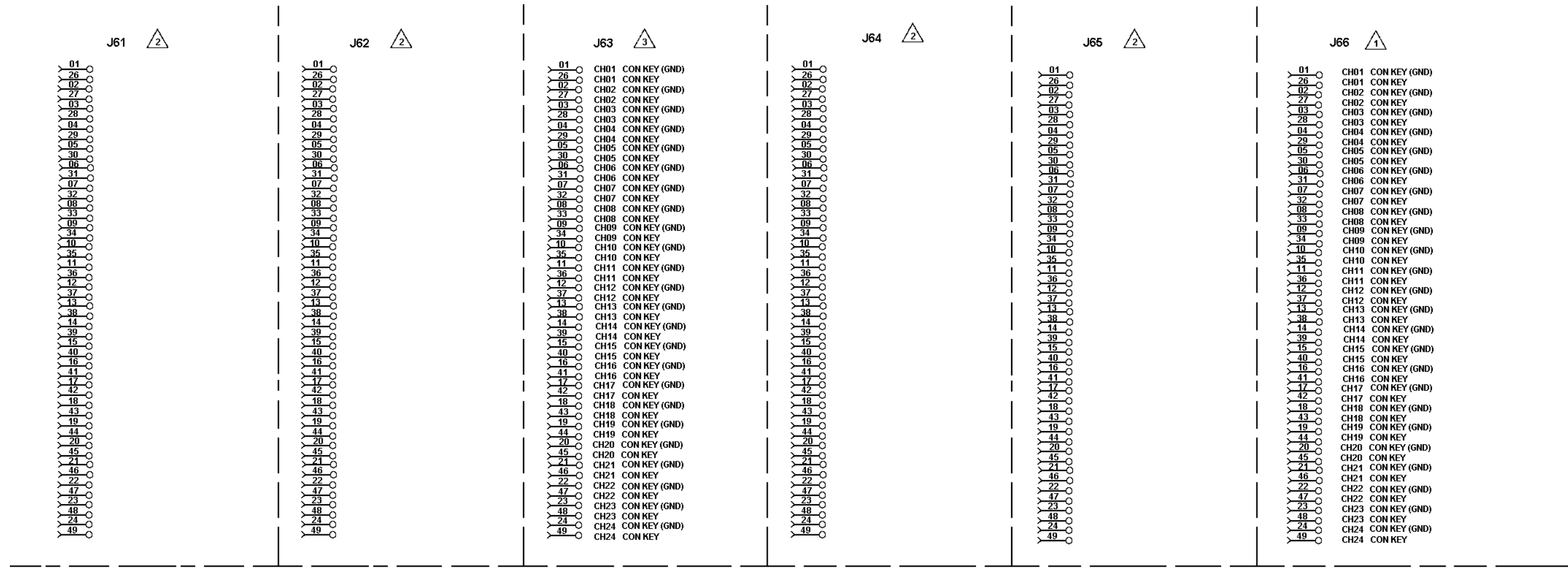


NOTES:

-  FOR SIMULCAST APPLICATIONS, DIGITAL VOTER PANEL J63 CONNECTS TO ANALOG VOTER PANEL J63 VIA JUMPER CABLE.
-  (GND) CONNECTS TO J61:J62 (GND).
-  FOR SCST APPLICATION, J59 CONNECTS TO J64 VIA JUMPER CABLE.
-  FOR NON SIMULCAST APPLICATIONS, MAIN SITE (S01) CONNECTS TO J59.
-  THESE CONNECTIONS ARE FOR SIMULCAST APPLICATION ONLY.

VOTER SYSTEM PANEL, DIGITAL JACK PIN OUT DIAGRAM

(19D904931 Sh. 7, Rev. 0)



NOTES:

- 1 J58 CONNECTS TO J66 VIA JUMPER CABLE.
- 2 THESE JACKS ONLY USED ON DIGITAL VOTER SYSTEM PANEL.
- 3 FOR SIMULCAST APPLICATION, ANALOG VOTER PANEL J63 CONNECTS TO DIGITAL VOTER PANEL J63 VIA JUMPER CABLE. FOR NON-SIMULCAST APPLICATIONS, J63 CONNECTS TO CONSOLE SWITCH OUTPUT.

VOTER SYSTEM PANEL, ANALOG JACK PIN OUT DIAGRAM