

MAINTENANCE MANUAL

INTRAPLEX MULTIPLEX CROSS CONNECT INTERFACE

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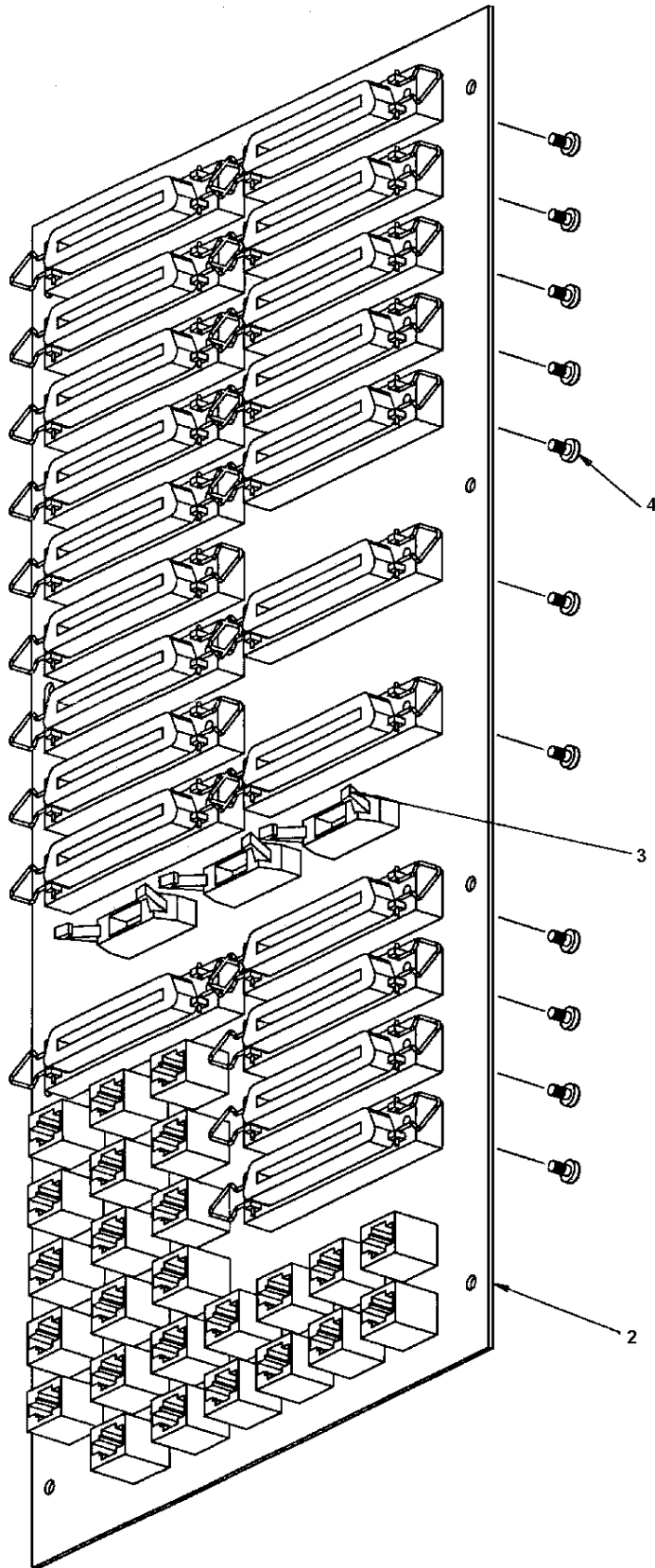
DESCRIPTION

The purpose of this manual is to serve as a reference document to define the interface between the Intraplex T1 Multiplex equipment and the Ericsson simulcast equipment at the Control Point, including a Control Point with a co-located Transmit Site.

This manual contains the the Assembly Diagram for the Multiplex Cross connect Panel, Interface Diagrams that describe the interface between the Intraplex Mux Cross connect Panel, the Intraplex Multiplexer (TDM-

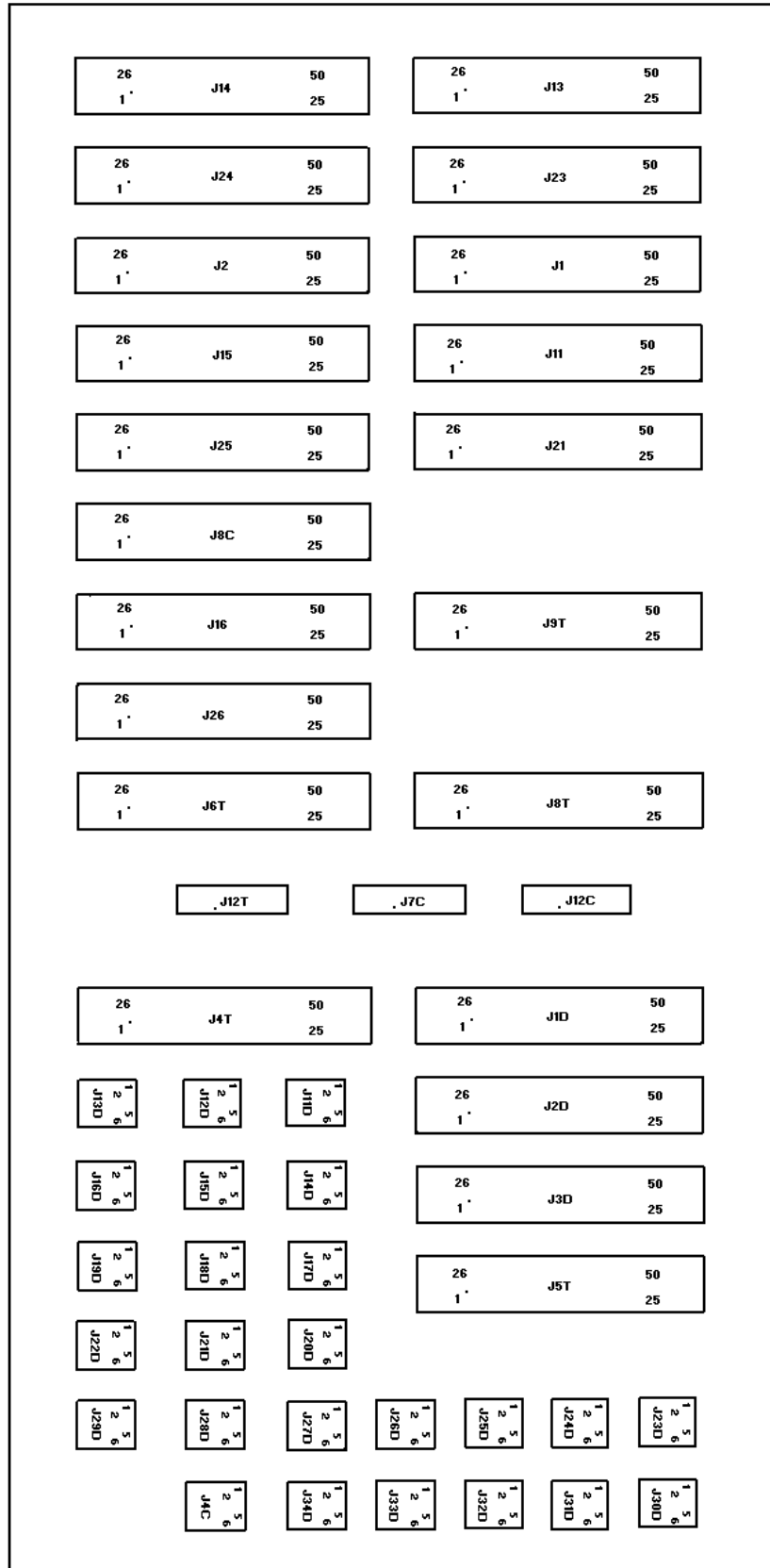
163), and the Control Point Cross connect Panel. Also included are the Jack Pin-Out Diagrams for the Intraplex Mux Cross connect Panel, including the Microwave Distribution Panel, Interface Panels, and Simulcast Control Equipment.

Jack Pin-Out Diagrams identify the function and name of the signal on each pin for all jacks on the cross connect panel.

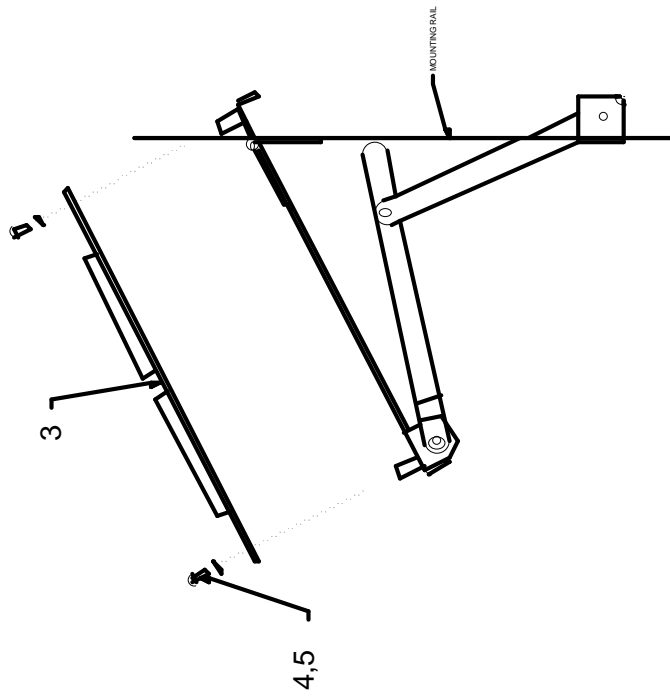


**INTRAPLEX MULTIPLEX
CROSS CONNECT PANEL**

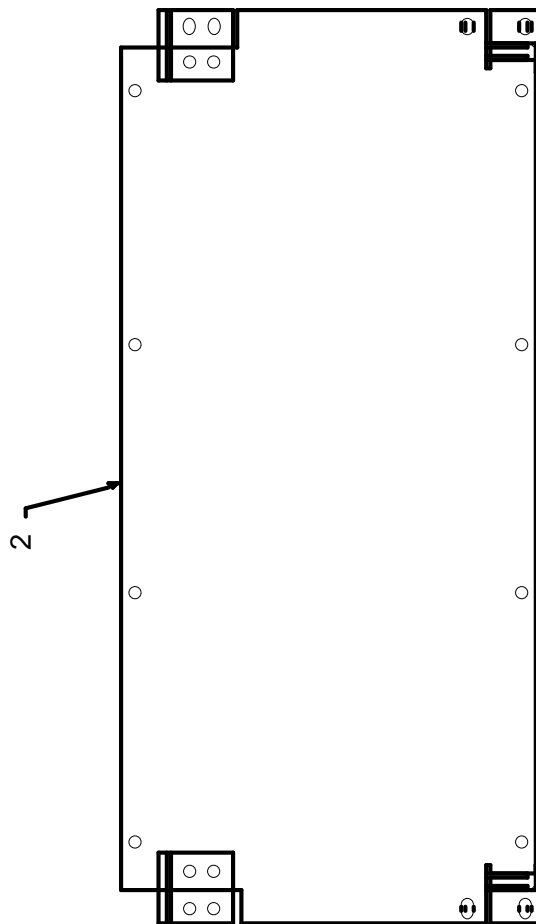
(1078 ROA 117 2213 Rev. 1)



INTRAPLEX MULTIPLEX CROSS CONNECT

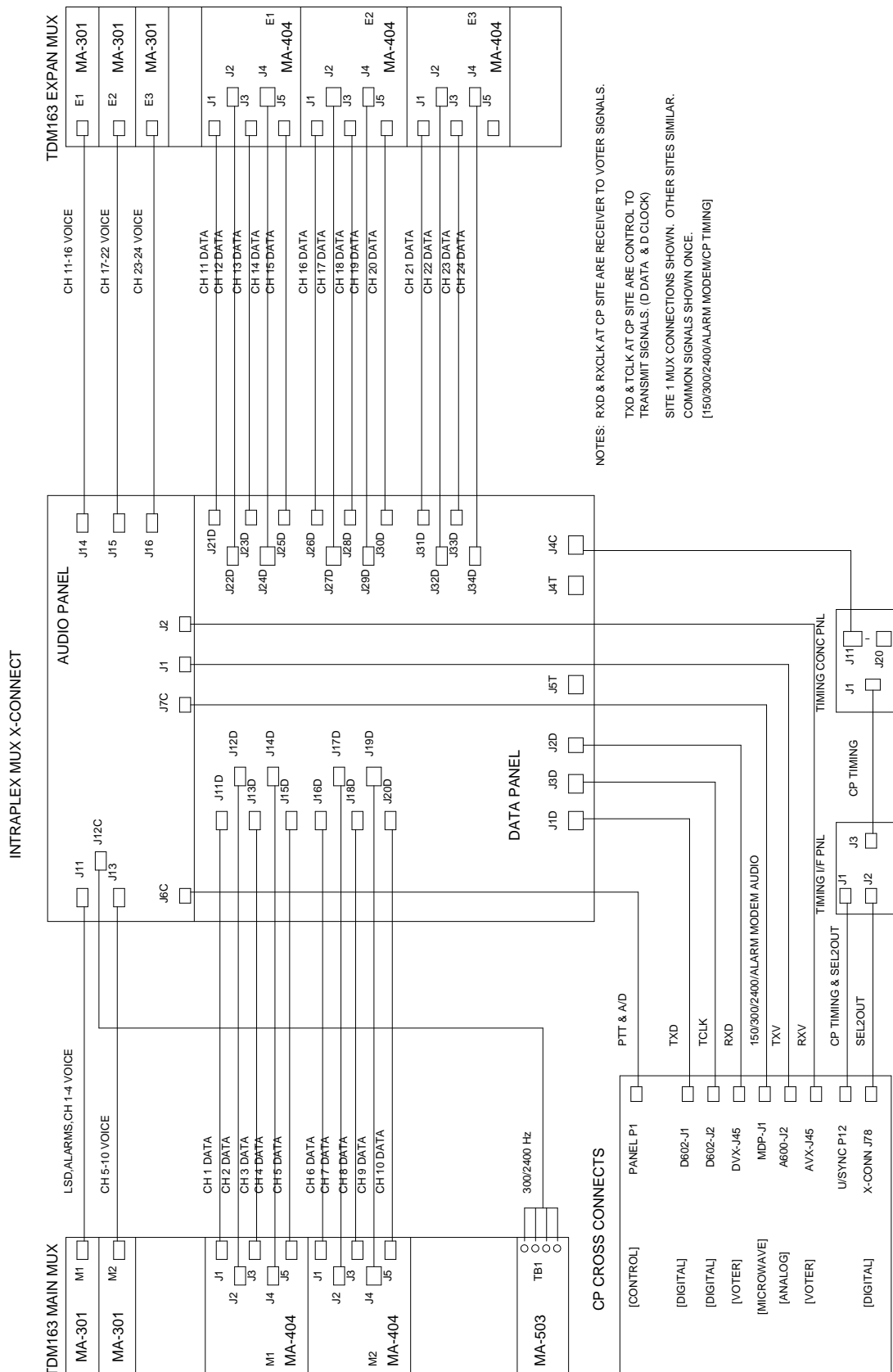


RIGHT SIDE VIEW
PARTIALLY OPEN



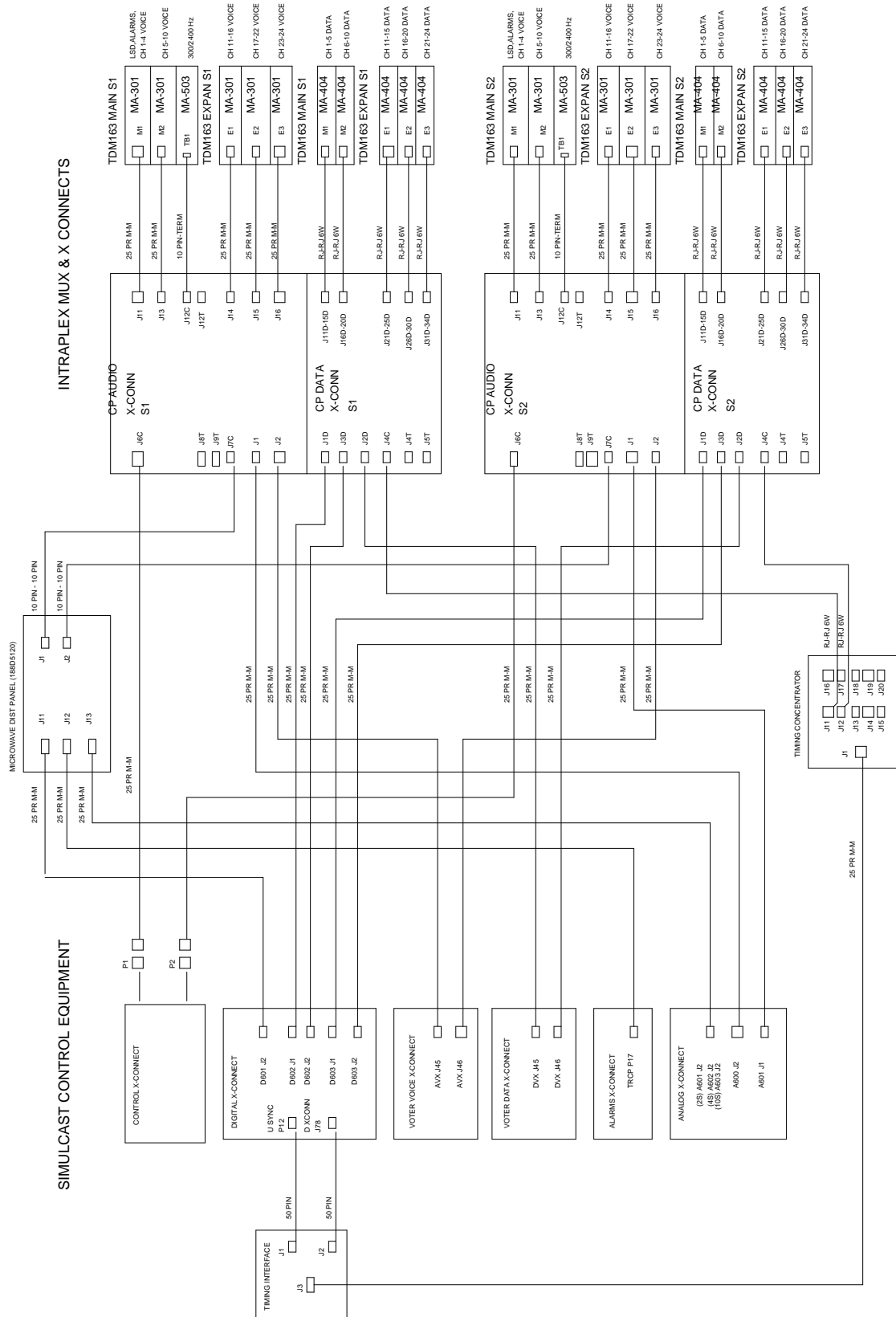
**INTRAPLEX MULTIPLEX
CROSS CONNECT PANEL ASSEMBLY**

(188D6225, Rev. 1A)



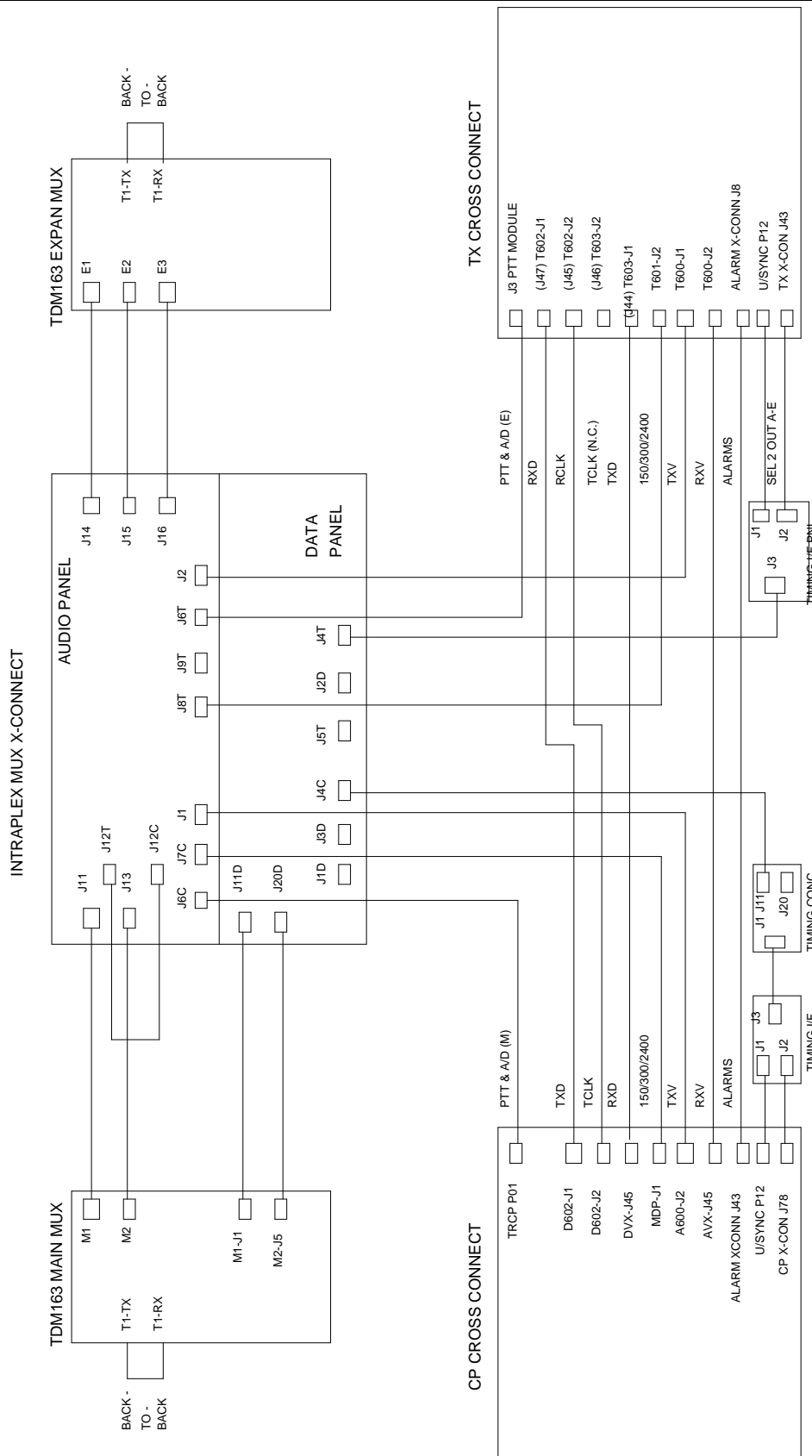
NOTES: RXD & RXCLK AT CP SITE ARE RECEIVER TO VOTER SIGNALS.
 TXD & TXCLK AT CP SITE ARE CONTROL TO TRANSMIT SIGNALS. (D DATA & D CLOCK)
 SITE 1 MUX CONNECTIONS SHOWN. OTHER SITES SIMILAR.
 COMMON SIGNALS SHOWN ONCE.
 [150/300/2400/ALARM MODEM/CP TIMING]

INTERFACE CABLING
 TDM 163 MULTIPLEXER TO CROSS CONNECTS



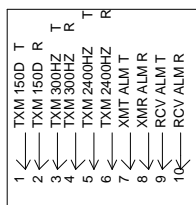
**INTERFACE CABLING, 2-SITE
SIMULCAST CONTROL EQUIPMENT TO INTRAPLEX MUX & CROSS CONNECTS**

(19C852615 Sh. 3 Rev. 1)

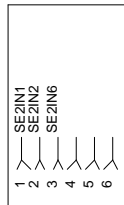


NOTES: TXD & TCLK AT TX SITE ARE RECEIVER TO VOTER SIGNALS.
 RXD & RCLK AT TX SITE ARE CONTROL TO
 TRANSMIT SIGNALS. (9.6D GOUTA & 9.6C GOUTA)
 MUX NOT EQUIPPED WITH PROGRAM CARDS.
 J12C-J12T JUMPER CABLE PROVIDES 300HZ & 2400HZ SYNC PATH.

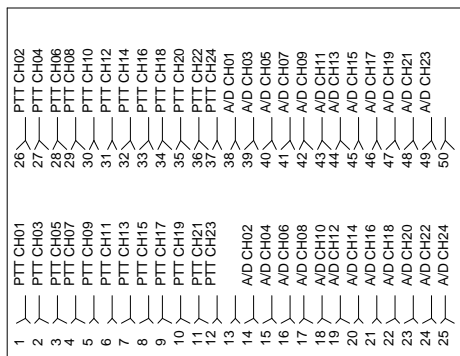
CO-LOCATED TX SITE INTERCONNECT CABLING
 TDM 163 MULTIPLEXER TO CROSS CONNECTS



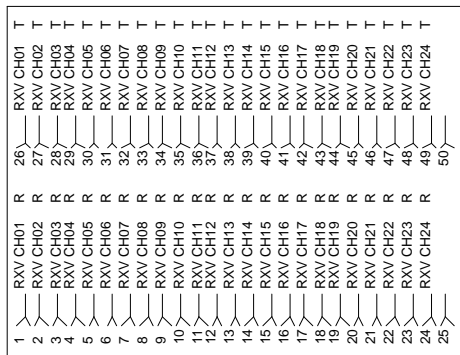
J7C



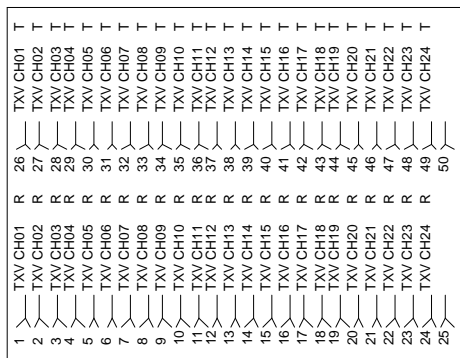
J4C



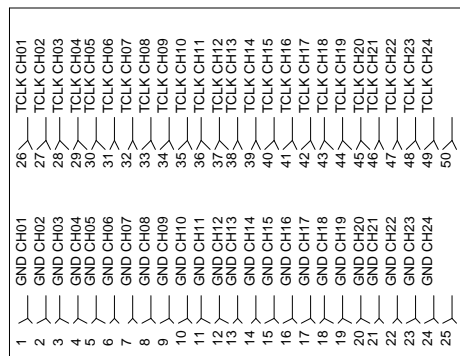
J6C



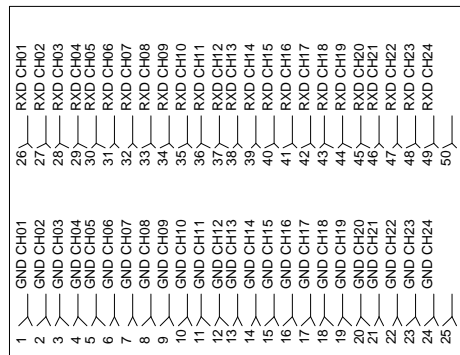
J2



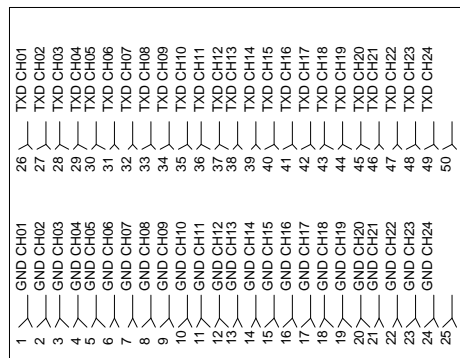
J1



J3D



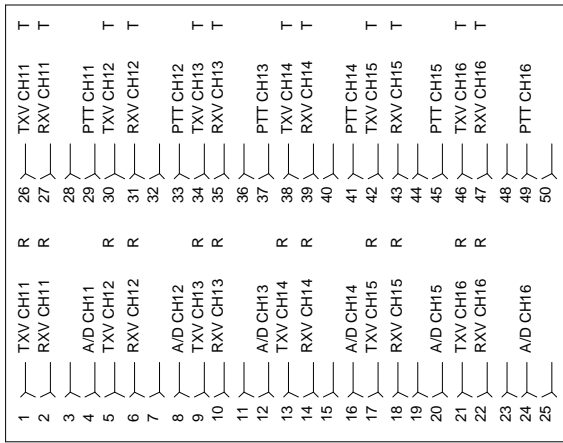
J3J



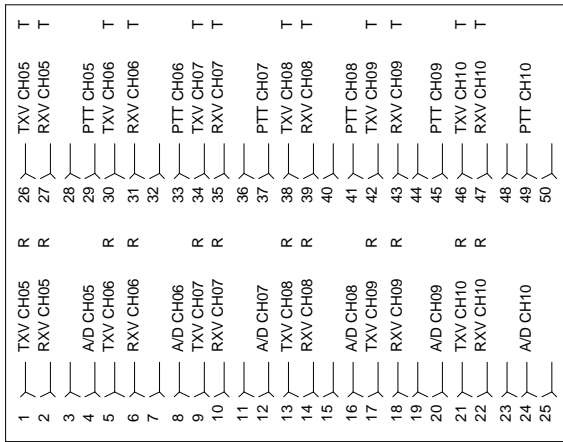
J2D

MULTIPLEXER CROSS CONNECT JACK PIN OUT DIAGRAM

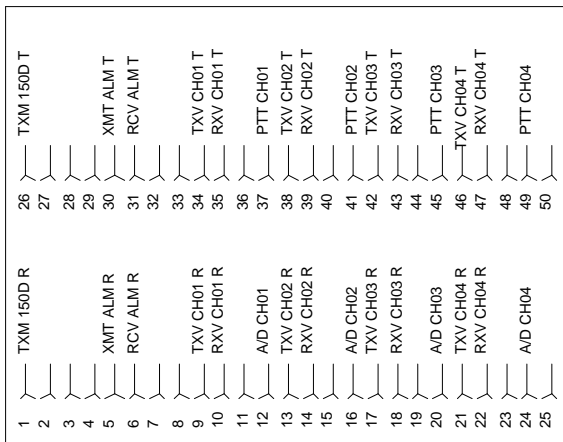
(19C852611 Sh. 1 Rev. 1)



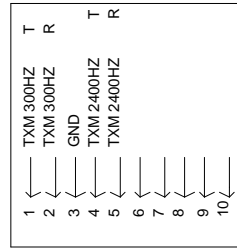
J11 & J21



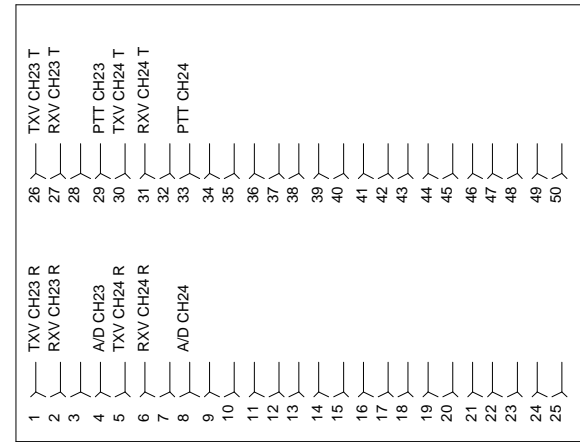
J13 & J23



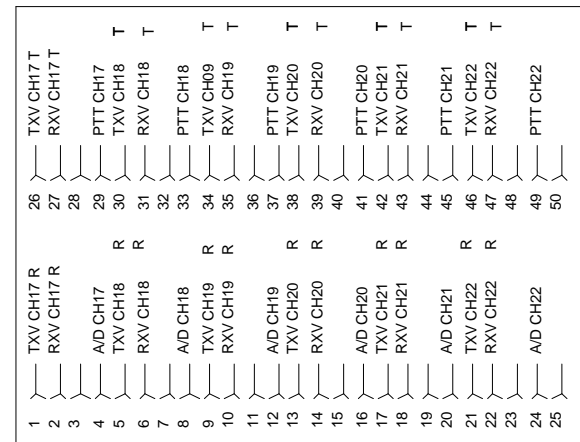
J15 & J25



J12C

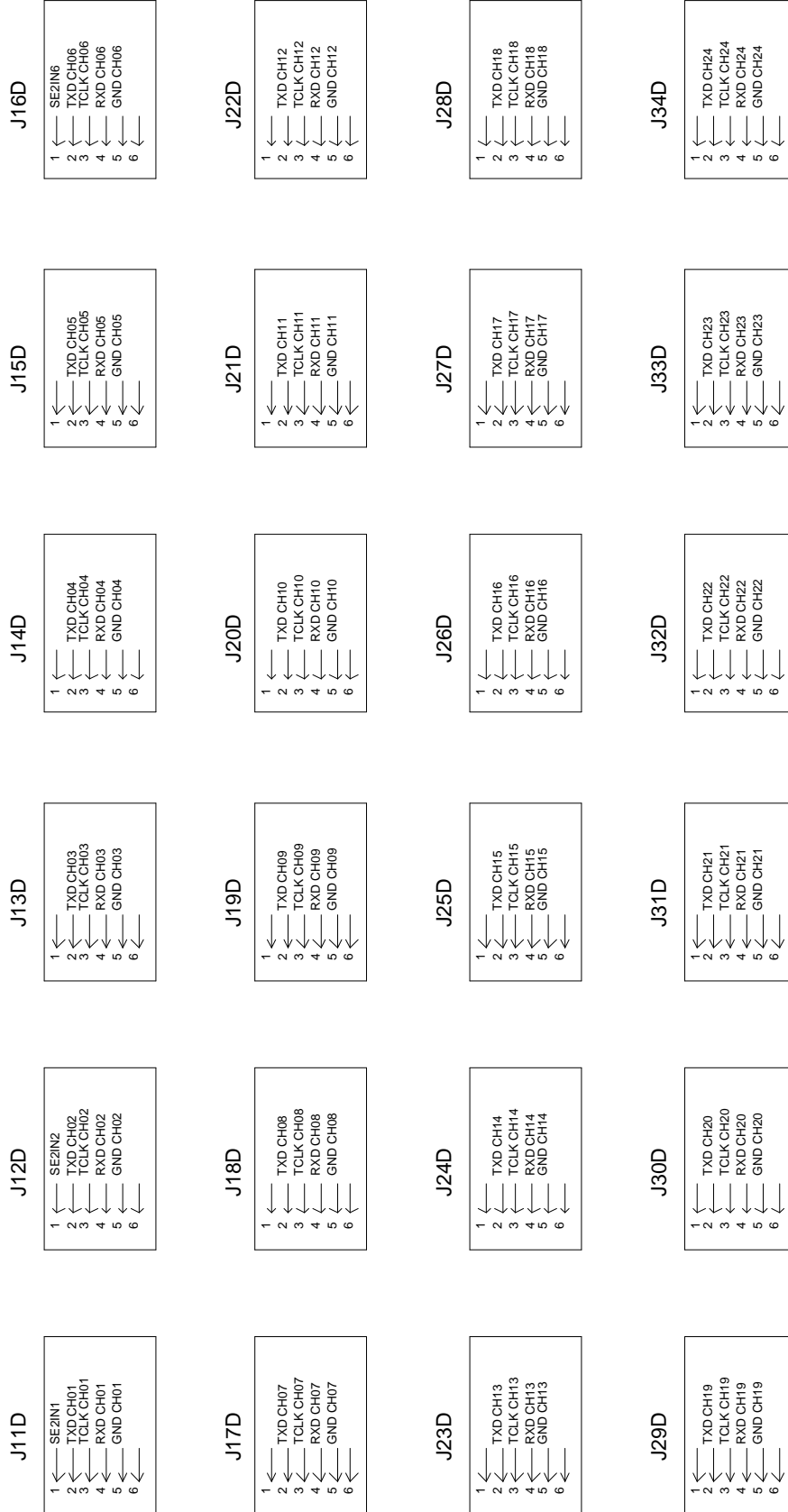


J16 & J26



J15 & J25

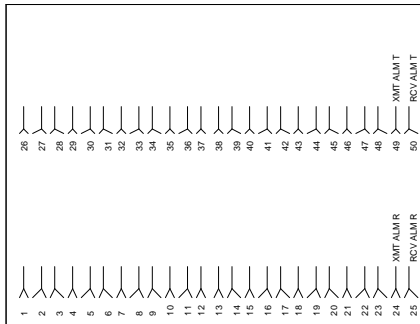
MULTIPLEXER CROSS CONNECT JACK PIN OUT DIAGRAM



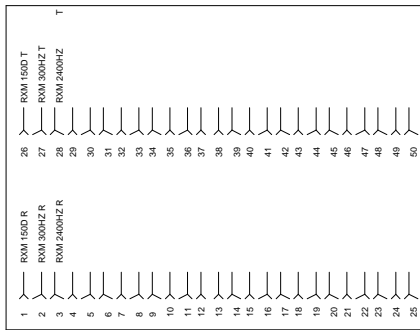
**MULTIPLEXER CROSS CONNECT
JACK PIN OUT DIAGRAM**

(19C852611 Sh. 3 Rev. 1)

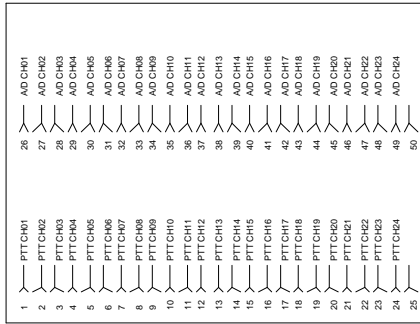
J9T



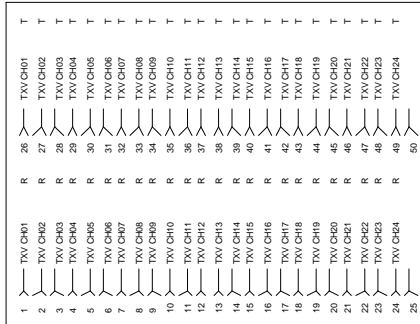
J8T



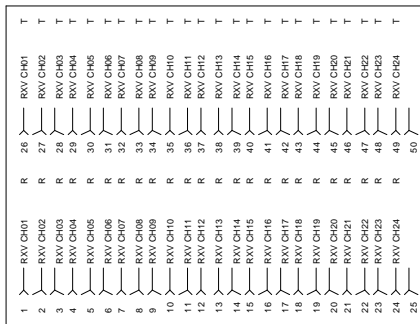
J6T



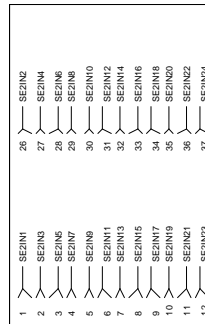
J2



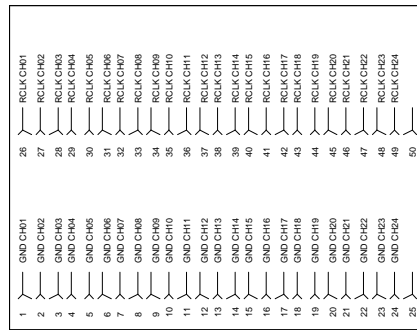
J1



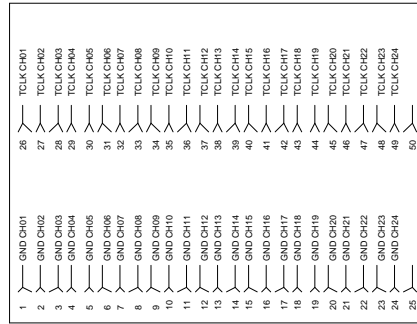
J4T



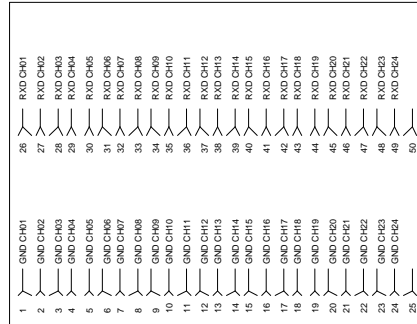
J5T



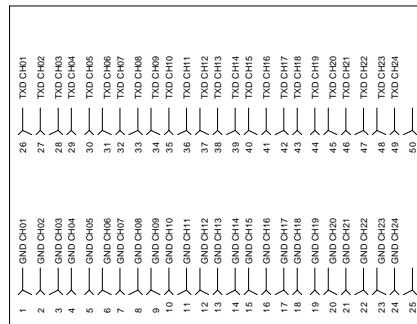
J3D



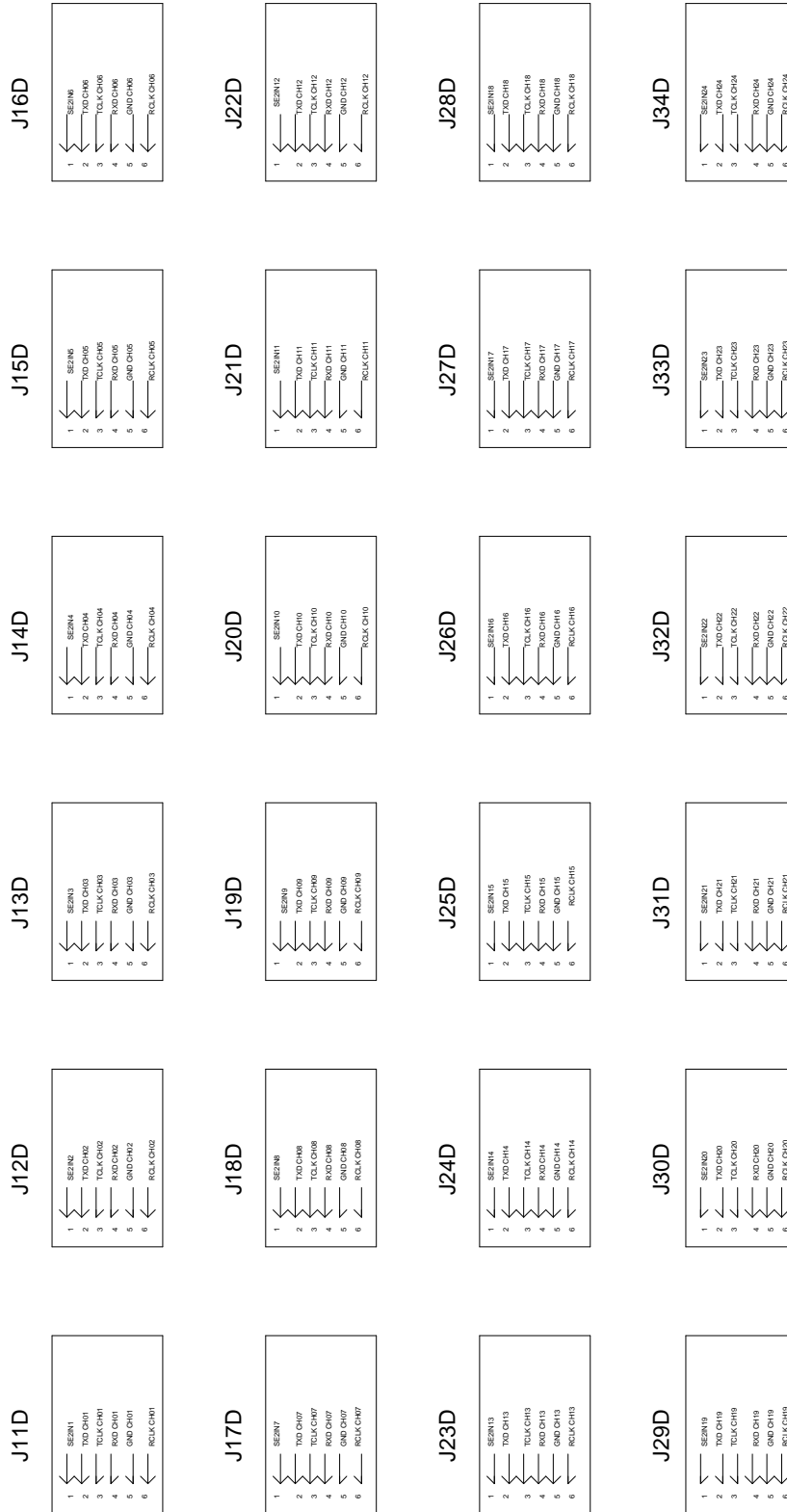
J2D



J1D



MULTIPLEXER CROSS CONNECT JACK PIN OUT DIAGRAM, TRANSMIT SITE



MULTIPLEXER CROSS CONNECT JACK PIN OUT DIAGRAM, TRANSMIT SITE

(19C852612 Sh. 3, Rev. 1)