```
┌─────────────────────────────────────────────────────────────────────┐
│ MOTOROLA RADIO SERVICE SOFTWARE    │ <MESSAGE  CORRESPONDS  TO  CUR- │
│ MSF    MODEL: UHF R2−CONV−RPTR     │ RENT LINE>                      │
│ PAGE 08 OF 10                      │                                 │
│ EDIT ADVANCED INFORMATION          │                                 │
├────────────────────────────────────┴─────────────────────────────────┤
│ SECURE DELAYS:                                                        │
│        Beep Delay                  0087    0 < time < 9998 msec       │
│        Extended Buffer Delay       0080    0 < time < 9998 msec       │
│        Fail Test Delay             0025    0 < time < 9998 msec       │
│        Max Code Detect DT Delay    0080    0 < time < 9998 msec       │
│        Rx Code Detect DOD          0320    0 < time < 2720 msec       │
│        Tx Code Detect DOD          0320    0 < time < 2720 msec       │
│        Rx DC End Of Message Delay  40      0 < time < 170 msec        │
│        Tx DC End Of Message Delay  40      0 < time < 170 msec        │
│        Takeover EOM Delay          0080    0 < time < 9998 msec       │
│ SYSTEM CONNECTOR:                                                     │
│        External PTT                LINE                               │
│        Spare Output                NULL                               │
│        Spare Output Pin Active:    LOW                                │
│                                                                       │
├───────────────────────────────────────────────────────────────────────┤
│  F1      F2      F3      F4      F5     F6    F7    F8    F9    F10    │
│  HELP                            PRINT                         EXIT   │
│                                  PAGE                                 │
└───────────────────────────────────────────────────────────────────────┘
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Figure 5.21: Advanced Information Screen #8

5.5.15. Secure Delays

***** WARNING *****

CHANGES ARE NOT RECOMMENDED IN THE FOLLOWING FIELDS. CHANGES WILL IMPACT SYSTEM OPERATION.

Beep Delay

The Beep Delay field indicates the length of time to unmute a 750 Hz tone. The time must be an integer between 0 and 9998 msec. The default is 87 msec.

Extended Buffer Delay

The Extended Buffer Delay field indicates the length of the extended buffer. The delay must be an integer between 0 and 9998 msec. The default is 80 msec.

Fail Test Delay

The Fail Test Delay field indicates the length of time to wait for hybrid failure indication. The delay must be an integer between 0 and 9998 msec. The default is 25 msec.

Max Code Detect DT Delay

The Max Code Detect DT Delay field indicates the maximum time to achieve a code detect. The delay must be an integer between 0 and 9998 msec. The default is 80 msec.

Rx Code Detect DOD

The Rx Code Detect DOD field indicates the delay while waiting for the Rx_Code_Detect to re−activate. The delay must be an integer between 0 and 2720 msec. The default is 320 msec.

Tx Code Detect DOD

The Tx Code Detect DOD field indicates the delay while waiting for the Tx_Code_Detect to re−activate. The delay must be an integer between 0 and 2720 msec. The default is 320 msec.

Rx DC End Of Message Delay

The Rx DC End of Message Delay field indicates the length of time to generate EOM for receiver DC glitch. The time must be an integer between 0 and 170 msec. The default is 40 msec.

Tx DC End Of Message Delay

The Tx DC End of Message Delay field indicates the length of time to generate EOM for wireline DC glitch. The time must be an integer between 0 and 170 msec. The default is 40 msec.

Takeover EOM Delay

The Takeover EOM Delay field indicates the length of time to generate EOM coded takeover. The delay must be an integer between 0 and 9998 msec. The default is 80 msec.

5.5.16. System Connector

External PTT

The External PTT field indicates which bit on the MUXbus will be activated when the External PTT input to the station is activated. The External PTT input is pin 12 of the System Connector (J2 on the Junction Box), and is active low. To set a MUXbus bit in response to the External PTT Input, enter MUX, followed by A (indicating the address), followed by the MUXbus address (0−F), followed by B (indicating the bit), followed by the bit number to set (0−3). For example, MUXA2B3 sets bit 3 of MUXbus address 2 (TX PL DS) when the External PTT Input is active, and clears the bit when the input is inactive. Also, the following inputs are valid: LINE (sets bit 2 of MUXbus address 2), TRNK (sets the Trunking PTT bit on the High Speed Ring), and NULL (sets nothing). Only one command may be entered via the RSS. Some SP stations may use more than one command, in order to set multiple bits on the MUXbus in response to the External PTT Input. When reading a codeplug that contains more than one command, the External PTT field will show MULTIPLE and will be non−editable. The default for this field is TRNK for trunking stations and LINE for all others.

Spare Output

The Spare Output field indicates which bit on the MUXbus or High−Speed Ring (HSR) will be used to activate the Spare Output Pin on the station's Junction Box. The Spare Output is pin 9 of the System Connector (J2 on the Junction Box); see Appendix J for Spare Output Active Polarity. To activate the Spare Output in response to a MUXbus bit being active, enter MUX, followed by A (indicating the address), followed by the MUXbus address (0−F), followed by B (indicating the bit), followed

by the bit number to read (0–3). For example, MUXA2B3 activates the Spare Output when bit 3 of MUXbus address 2 (TX PL DS) is active, and clears the Spare Output when it is inactive. To activate the Spare Output in response to a High Speed Ring (HSR) bit being active, enter HSR, followed by A (indicating the address), followed by the HSR address (0–4), followed by B (indicating the bit), followed by the bit number to read (0–7). For example, HSRA0B5 activates the Spare Output when bit 5 of HSR address 0 (TSTAT) is active, and clears the Spare Output when it is inactive. Also, NULL is a valid input, and it leaves the Spare Output always inactive. Only one command may be entered via the RSS. Some SP station may use more than one command, in order to set the Spare Output when a combination of MUXbus and/or HSR bits are active. When reading a codeplug that contains more than one command, the Spare Output field will show MULTIPLE and will be non–editable. The default for this field is NULL.

See the *MSF 5000* User Manual for a complete description of the MUXbus and High Speed Ring.

Spare Output Pin Active

The Spare Output Pin Level field indicates the "active" polarity level of the spare output signal sent to the Junction Box connector at J2 pin 9. This signal can also be tapped at the TTRC board at J2900 pin 9. The active polarity can be toggled either active HIGH or LOW by means of the UP/DOWN arrow keys. The Spare Output Pin Level field defaults to active LOW.

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┌──────────────────────────────────────────────────────────────────────┐
│ ┌──────────────────────────────────┬───────────────────────────────┐ │
│ │ MOTOROLA RADIO SERVICE SOFTWARE  │ <MESSAGE  CORRESPONDS  TO  CUR-│ │
│ │ MSF   MODEL: UHF R2–CONV–RPTR    │ RENT FIELD>                    │ │
│ │ PAGE 09 Of 10                    │                               │ │
│ │ EDIT ADVANCED INFORMATION        │                               │ │
│ ├──────────────────────────────────┴───────────────────────────────┤ │
│ │ SCANNING RECEIVER:                                                │ │
│ │     Non–Priority Scan Delay    3000        0 < time < 9998 msec    │ │
│ │     Priority Scan Delay        3000        0 < time < 9998 msec    │ │
│ │     Scan Sample Time           0090        0 < time < 9998 msec    │ │
│ │     Priority Recheck Time      0300        0 < time < 9998 msec    │ │
│ │     Rx Qualify Time            0347        0 < time < 9998 msec    │ │
│ │ SAM:                                                              │ │
│ │     Rx Loopback Frequency      435.10000                          │ │
│ │     Tx Loopback Frequency      436.12345                          │ │
│ │     Diversity Equipped         DISABLED                           │ │
│ │     GCC–480 Equipped           DISABLED                           │ │
│ │     Gate Data Always           ENABLED                            │ │
│ │     MDC Pretime Bit Sync       DISABLED                           │ │
│ │     Inactivity Delay           000000      0 < delay < 357913 minutes│ │
│ │                                                                   │ │
│ ├───────────────────────────────────────────────────────────────────┤ │
│ │ F1      F2      F3      F4      F5     F6    F7    F8    F9    F10  │ │
│ │ HELP                           PRINT                         EXIT  │ │
│ │                                PAGE                               │ │
│ └───────────────────────────────────────────────────────────────────┘ │
└──────────────────────────────────────────────────────────────────────┘
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Figure 5.22: Advanced Information Screen #9