## SECTION 18. FRONT PANEL ASSEMBLY (A18)

### 18.1 DESCRIPTION

The Front Panel assembly (A18) contains controls, switches, and LEDs which control the System Analyzer. It also contains miscellaneous input and output ports. Most of the control devices are mounted directly to the Front Panel; however, some of them are mounted on the Display board (A18A1). Front Panel circuitry is connected to the rest of the system by two flexible printed wiring boards - the Front Panel Left Flex, which connects the Display board to the System Motherboard (A19), and the Right Flex, which connects the Scope controls to the Motherboard (A19). A ribbon cable assembly also connects the Display board to the Front-Panel Interface board (A15).

A schematic of the Front Panel assembly is shown at the end of this section in Figure 18-1. This is followed by Figure 18-2a-f, which coordinates the Front Panel parts list with three views of the Front Panel,
with the printed wiring board assemblies of the Left and Right Flexes, and with the Switch Interconnect board.

A schematic of the Display board follows these figures in Figure 18-3, with the printed wiring board assembly and parts list in Figure 18-4.

### 18.2 CONTROLS, SWITCHES AND LEDs

Table 18-1 describes each control device on the Front Panel, including those on the Display board (A18A1). (See Figure 18-2a at the end of the section for exact positions of these controls.)

The step-attenuator control knob (Step), the Antenna port, the RF In/Out port, the Duplex Generator Output port, and the Duplex Generator On/Off switch are part of the RF Input module (A17). For information on these ports and switches, refer to Section 17 on the A17 module.

Table 18-1. Controls, Switches and LEDs on the Front Panel

| Control, Switch, or LED | Function |
| :---: | :---: |
| Main Switches |  |
| POWER (S3) | In the On position, the system is powered up. In the Standby and DC Off positions during ac operation, and in the Standby position during dc operation, all modules are powered down except the Frequency-Standard Interface board (A16). In the DC Off position during dc operation, all modules are powered down. |
| FUNCTION (S2) | Determines whether the system is in Generate or Monitor mode. |
| MODULATION (S1) | Determines whether the modulation generator output is continuously on (Cont), Off, or Burst. |
| RF SECTION Controls |  |
| RF Level (R12 and R26) | Regulates the RF output power by controlling the reference voltage applied to the ALC loop on the Wideband Amplifier board (A17A2). |
| RF Port Sel | Controls which port (Antenna or RF In/Out) is enabled. Pulled out enables Antenna port. Pushed in enables RF In/Out port. |
| Ports: Antenna, RF In/Out, and Ext Wattmeter | See Section 17 on the RF Input module (A17). |
| OSCILLOSCOPE Controls |  |
| Vert (S12 and R11) |  |
| S12 | Selects from 4 values of vertical-input sensitivity: $0.01 \mathrm{~V}, 0.1 \mathrm{~V}, 1 \mathrm{~V}$, and $10 \mathrm{~V} / \mathrm{Div}$. When one of the 0.01 V to $10 \mathrm{~V} /$ Div RNG SEL outputs is low, the corresponding vertical-input sensitivity has been selected. These outputs are interfaced to the Processor board by circuitry on the Front-Panel Interface board. |
| R11 | Provides a continuously variable but uncalibrated vertical-input sensitivity. When turned fully clockwise, calibrates the vertical-input sensitivity. |

Table 18-1. Controls, Switches and LEDs on the Front Panel (Cont)

| Control, Switch, or LED | Function |
| :---: | :---: |
| OSCILLOSCOPE Controls |  |
| Trig Level/Position (S11 and R10) Sll | Selects automatic or normal triggering. In automatic mode, the AUTO/NOR TRIG SEL output, which goes to the Scope Amplifier board (A2), is -5 V . In this mode, the scope sweep free-runs when there is no triggering. In normal mode, the AUTO/NOR TRIG SEL output is pulled to +5 V by a pull-up resistor on A2, and the scope sweep is blanked in the absence of triggering. |
| R10) | Adjusts the reference (Trig Level) to which the input signal is compared for scope triggering. The triggering circuit is on the Scope/DVM Control board (A7). |
| $\begin{aligned} & \text { Horiz (S10, R9, R8) } \\ & \text { Sio } \end{aligned}$ | Selects one of six horizontal-sweep speeds or external horizontal input. When one of the SWP SEL or EXT HORIZ SEL outputs is high, the corresponding horizontal mode has been selected. These outputs are interfaced to processor control by circuitry on the Front-Panel Interface board. |
| R9 | Provides a continuously variable dc voltage at the SWP VERNIER VOLT output, which goes to the Scope Amplifier board, and thus provides a continuously variable horizontal-sweep speed. When R9 is fully clockwise, the horizontal-sweep speed is calibrated. |
| R8 | In external horizontal mode, adjusts the horizontal-input sensitivity over a range of $0.1 \mathrm{~V} /$ Div to $10 \mathrm{~V} /$ Div. |
| Vert (R14) and Horiz (R15) R14 | Adjusts the scope's vertical position by adjusting the offset in the vertical preamplifier on the Front-Panel Interface board. |
| R15 | Adjusts the scope's horizontal position by adjusting the offset in the horizontal amplifier on the Scope Amplifier board. |
| Port: Vert/Sinad/Dist/DVM/CounterIn Multipurpose Input | Most of the basic measurement functions of the System Analyzer can be performed on signals connected to this port. The particular measurement being performed is indicated by the display LEDs. |
| Port: Ext Horiz (External Horizontal Input) | Controls the scope's horizontal sweep in external-horizontal mode. R8 controls input sensitivity of 0.1V/Div to 10V/Div. |
| MONITOR Controls |  |
| Squelch (R7) | Controls the dc voltage at the SQUELCH LEVEL output (which goes to the Receiver), and thus controls the RF level below which the Receiver is squelched. |
| Volume (R6) | Controls the volume of the speaker output. |
| Image/Dplx (S17) - Image/Duplex Switch | Selects high-side or low-side mixing during normal monitor operation, and selects high-side or low-side offset during Duplex operation. During high-side mix or high-side offset, S17 is open and the IMAGE HI/LO signal is pulled up to +5 V by a resistor on the Front-Panel Interface board. During low-side mix or low-side offset, S 17 is closed and the IMAGE HI/LO signal is pulled low. The IMAGE HI/LO output is interfaced to processor control by circuitry on the Front-Panel Interface board. |
| BW (S16) - Bandwidth Switch | Controls the generator's FM sensitivity, the Receiver's demodulation sensitivity, and audio filters in the Receiver. In wideband mode, S16 is open, and the WB/ NB output is pulled up to +5 V by a resistor on the Front-Panel Interface board. In narrowband mode, S16 is closed, and the WB/NB output is pulled low. The WB/NB output is interfaced to processor control by circuitry on the FrontPanel Interface board. |
| $\mathrm{BFO}(\mathrm{R} 18)$ | Controls the dc voltage at the BFO FREQ CNTL output, and thus controls the frequency of the varactor-tuned BFO on the Receiver board. |
| Port: Demod Out | In monitor mode, the Receiver's demodulation output comes out of this port. In FM, the output level is 1 volt-peak per kHz of peak deviation in narrowband, and 1 volt-peak per 10 kHz of peak deviation in wideband. In AM, the output level is a nominal 1 volt-peak per 10 percent AM. |

Table 18-1. Controls, Switches and LEDs on the Front Panel (Cont)

| Control, Switch, or LED | Function |
| :---: | :---: |
| MODULATION Controls |  |
| Code Synth Lvl (R5), Ext Level (R4), <br> 1 KHz Level (R13) <br> Switch S15 <br> Port: Mod Out <br> Port: Ext Mod In <br> Port: Mic (Microphone Input) | Adjust the amplitude of the signals that modulate the RF signal generator. <br> Turns the 1 KHz signal on and off. <br> Connects to the modulation generator's output. In generate mode, this port is the modulation source for the signal generator. <br> Allows external signals to be summed into the output of the modulation generator. The sensitivity of this output to the EXT MOD input is adjusted with the Ext Level control. <br> Allows voice information to be incorporated into the internal modulation. Its level is set by the Ext Level control. |
| Miscellaneous Controls |  |
| Intensity (R2) and Focus (R:3) <br> I)ispr/Sweep (R1) <br> RFScan | Control the intensity and focus of the CRT by controlling the dc level on the INTENSITY LEVEL and FOCUS LEVEL signals going to the Scope Amplifier board (A2). <br> Controls the amplitude of the SYNTH SWEEP signal going to the RF Synthesizer and thus controls the dispersion of the spectrum-analyzer sweep or the frequency range of the sweep generator's output. <br> Scans the RF Synthesizer frequency up or down. When the optical encoder is rotated clockwise, the OPTICAL ENC B signal leads the OPTICAL ENC A signal by $90^{\circ}$. When the optical encoder is rotated counter clockwise, OPTICAL ENC A leads OPTICAL ENC B by $90^{\circ}$. The OPTICAL ENC A and B signals go to circuitry on the A15 board, which interfaces these signals with the Processor board. |
| DISPLAY BOARD (A18A1) Controls |  |
| Keypad Switches (Sl-S.4) <br> si S!. S:3, S4 <br> Display, Function and Modulation LEDs <br> (I)S1-I)S:4) <br> Batt and A( LEEI)s (I)S"5, ISS26) <br> Oven-Ready LED | Contains 12 switches which control the cursor and allow entry of numeric data. Control system display, function, and modulation, respectively. The particular display, function, and modulation modes are indicated by LEDs DS1-DS24. <br> Pressing any of these switches connects a unique combination of row and column lines. A PIA on the Processor board detects this, and the processor takes the appropriate action. <br> Indicate display, function, and modulation status. Control output to these LEDs is accomplished via AF BUS addresses 0,1 , and 2 on the Front-Panel Interface board. Latch-selects LS0, LS1, and LS2 are pulsed low to latch the data on the AF DATA BUS into latches on U6, U7, and U8, respectively, when the corresponding address is enabled on the AF ADD BUS. This data is decoded (as shown in Table 18-2) by U6, U7, and U8, and the appropriate LEDs are lit by drivers U1-U4. For more information on the AF BUS, refer to Section 15 on the Front-Panel Interface board. <br> Indicate application of dc and ac power, respectively. <br> Indicates when the oven has reached operating temperature on System Analyzers that have the ovenized frequency standard. |

### 18.3 DECODING FOR DISPLAY, FUNCTION AND MODULATION LEDs

Table 18-2 shows which LED is selected for each state of the AF DATA BUS when the appropriate latch-select (LS0, LS1, or LS2) is strobed low.

Table 18-2. Decoding for Display, Function, and Modulation LEDs

| Configuration of AF DATA BUSSES |  |  |  | Display LED Selected <br> (LS0 strobed low) |  | Function LED Selected <br> (LS1 strobed low) | Modulation LED Selected <br> (LS2 strobed low) |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0 | 1 | 2 | 3 |  | FM | PL/DPL |  |
| 0 | 0 | 0 | 0 | Gen/Mon Mtr | CW | PL/DPL INV |  |
| 0 | 0 | 0 | 1 | Modulation | AM | Tone A |  |
| 0 | 0 | 1 | 0 | Spect Analyzer | SSB/DSBSC | Tone B |  |
| 0 | 0 | 1 | 1 | Duplex Gen | SWP 1-10 MHz | Tone Seq. |  |
| 0 | 1 | 0 | 0 | RF Memory | SWP 0.01-1 MHz | Tone Remote |  |
| 0 | 1 | 0 | 1 | Signaling Seq | Not Allowed | Not Allowed |  |
| 0 | 1 | 1 | 0 | Freq Counter | Not Allowed | Not Allowed |  |
| 0 | 1 | 1 | 1 | DVM/DIST | Not Allowed | Not Allowed |  |
| 1 | 0 | 0 | 0 | Ext Wattmeter | Not Allowed | Not Allowed |  |
| 1 | 0 | 0 | 1 | IF | Not Allowed | Not Allowed |  |
| 1 | 0 | 1 | 0 | Scope AC | Not Allowed | Not Allowed |  |
| 1 | 0 | 1 | 1 | Scope DC | Not Allowed | Not Allowed |  |
| 1 | 1 | 0 | 0 | Not Allowed | Not Allowed | Not Allowed |  |
| 1 | 1 | 0 | 1 | Not Allowed | Not Allowed | Not Allowed |  |
| 1 | 1 | 1 | 0 | Not Allowed | Not Allowed | Not Allowed |  |
| 1 | 1 | 1 | 1 | Not Allowed |  |  |  |









front panel assembly (ais)

| Find No. No. | $\begin{aligned} & \text { ary. } \\ & \text { Req. } \end{aligned}$ | Parto. | Nomenclature | Part value |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | NuT Comeoner |  |
|  |  | coide | Sicte |  |
|  |  | ${ }^{18} 8$ enasara |  |  |
|  |  | ${ }_{\text {a }}^{\text {a }}$ | Resision vanimit |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  | come | Pusmurion swral |  |
|  |  |  | 隹 |  |
|  |  |  | Custurion sirch |  |
|  |  |  |  | gopnwoeans |
|  |  | coich | cismer |  |
|  |  | cose | (kiod |  |
|  |  |  | citicl | 18, $1 /$ Shati |
|  |  |  | (cate |  |
|  |  |  |  |  |
|  |  | Stiole | Stineme | ${ }_{632 \times 312}$ |
|  |  | cincose | CABLE ASSEMBLY DISPLAY BOARD ASSY A18A1 | ${ }^{\text {ALusfrex }}$ |
|  |  | ciole | comection |  |
|  |  |  | ${ }_{\text {Remsilfor van }}^{\text {Rem }}$ |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  |  | $\substack{\text { REESsiob } \\ \text { Resisfor }}$ |  |
|  |  | coill | ceicision |  |
|  |  | coicle | Swich |  |
|  |  |  | Stion |  |
|  |  | ciole | Smwer fogit |  |
|  |  | Cable | ly (A18J5/FLEX) 34389 |  |
|  |  | 88.839 | conector.anc |  |




FRONT PANEL ASSEMBL
DISPLAY BOARD (A18A1)
Figure 18-4. Printed Wiring Board Assembly and Parts List

