# REPEATER CONTROL RCL-54 B Sub-Assembly No.14.0571

## **W**R communications Itd.

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RCL-54B REPEATER CONTROL, SUB-ASSEMBLY 14.0571

1 SPECIFICATIONS
POWER REQUIREMENTS 13 VDC at 5 mA.nominal
DROP-OUT DELAY RANGE 0.15 to 5 seconds, nominal
TIME LIMIT 4 min.
P.C.EDGE CONNECTOR 22 pin, 3.96 mm spacing
DIMENSIONS LXWXH 154 mm x 20 mm x 107 mm

2 DESCRIPTION

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The RCL-54B Repeater Control is an electronic interface to operate a radio transmitter and receiver in a radio-repeater configuration. The circuit board plugs into a 22 pin P.C. edge connector. A labelled front panel allows easy access to the adjustment controls and a folded metal card-holder allows quick removal of the plug-in assembly for service or testing. An on-board front-panel switch is provided for disabling the repeater transmit function during tests.

3 FUNCTIONAL CONNECTIONS (BY PIN NUMBER)

1	GND	Common ground.
2	RX AF IN LO	Receiver low-level audio input.
3	RX AF IN HI	Receiver high-level audio input.
6	+13V IN	+13 volt supply input.
8	+8V OUT	+8 volt supply output. Used to power 8 volt accessory circuits.
10	MOD	Leveled output of the audio coupler for voice-modulation input to the transmitter.
11	AF OUT	An amplified version of receiver audio input. Used for driving the inputs of tone decoders or accessory circuits.
12	AF IN	Input to the AF Switch. This input is normally connected to AF OUT at pin ll.
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13	RPT EN	Application of ground to this pin enables the audio coupler and PTT circuits to function according to the state of COS IN. This pin is used as a control gate for tone-operated squelch systems and must be grounded externally for carrier-squelch systems.
14	COS IN	Carrier-operated switch input. This circuit is connected to the receiver squelch circuit and will detect an input carrier as being present for more than about 3 volts input.
15	COS OUT	Carrier-operated switch output. This output is buffered for loads up to 15 mA.
16	COS OUT DIS	COS OUT is disabled when this pin is grounded.
17	TRANSMIT IN	Grounding this pin will ground the PTT, provided Sl is in REPEAT position and the COS driven limit timer permits.
18	PTT	An internal open-collector NPN switch grounds this pin to key the transmitter. It will connect to the transmitter's PTT input.
19	LIMIT DIS	The limit timer is disabled when this pin is grounded.
20	SW MUTE IN	A low applied to this pin will mute the AF switch.
21	TRANSMIT OUT	This output is high (+8V CMOS) when the transmitter is in the transmit state via pin 18 (PTT).
22	GND	Common ground.

#### 4 CIRCUIT DESCRIPTION

The carrier-operated switch (COS) is made up with Q1 as a threshold detector and U1C to form a fast-acting switch with hysteresis. U1D and Q3 provide a buffered COS OUT that will source up to 15 mA. If a low is applied to COS OUT DIS then COS OUT will be disabled. The COS signal output from U1C is ANDED with a repeater enable signal from U1A to provide an enabled COS

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at UlB output. This  $\overline{\text{COS}}$  signal is enabled when  $\overline{\text{RPT}}$   $\overline{\text{EN}}$  input is low. The  $\overline{\text{COS}}$  signal is fed to the delay timer, limit timer and pre-emphasizing switch.

The delay timer U2D will provide a high at its output if either COS is low or if TRANSMIT IN is low. In the case that COS is low and TRANSMIT IN is high (or no connection) the output of U2D will remain high after COS goes high (loss of a carrier at the receiver) for the time set using the DELAY control R3. This will cause the transmitter to be active for a drop out delay time after the loss of received signal (adjustable 0.15 to 5 s). For test purposes U2C allows switch S1 to manually disable the electronic keying of the transmitter PTT through this card.

The limit timer uses a stable 2 kHz oscillator and a delay counter U4 to provide a limit of 4 minutes on COS being low before the output at U3B disables keying of the transmitter PTT through gate U3A. The time can be changed in octaves from 1 to 64 minutes by cutting the PC trace provided and placing a wire to an appropriate pin as shown on the schematic diagram. The limit timer can be disabled by applying a low to LIMIT DIS. Q2 is the PTT keying switch. The logic drive is available at TRANSMIT OUT.

COS is inverted with U2B and applied to CR2 to gate open the pre-emphasizing switch when COS is low (a signal is received and the card is enabled). CR3 allows an external device to mute this switch when SW MUTE IN is low. U5B accepts both standard low level and high level RX AF inputs and produces a buffered output at AF OUT. This output can be connected directly or through an external filter to AF IN which is the input to the pre-emphasizing switch U5A. The LEVEL control, R13, is used to adjust the repeater audio gain.

U6 provides a regulated +8 volts DC supply for the on-board electronics and up to 100 mA of external load.

5 ADJUSTMENTS

- DELAY Sets transmitter drop-out delay. Front-panel, single-turn pot., (slot-driver) adjustable from 0.15 to 5 s nominal.
- LEVEL Sets repeater deviation ratio. Normally set for one to one at 2 kHz deviation for 1000 Hz tone.

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#### VIEW FROM COMPONENT SIDE



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C1	0.022 uF ceramic	Kemet	C330C223M1R5CA	24,4050				
C2	6.8 uF tant. 25V			26 1022				
C3	0.0033 uF polystyrene	Phil.	279АНС ЗКЗ	25.0008				
C4	1.5 uF tant., 35V	1		26.1032				
C5	0.0033 uF polystyrene	Phil.	279АНС ЗКЗ	25.0008		÷		
C6	10 uF tant., 25V	1		26.1023				
C7	1.5 uF tant., 35V			26.1032				
C8	0.33 uF ceramic	Kemet	C330C334M5U1CA	24.4051				
C9	1.5 uF tant.	11	T362B155M035AS	26.1002				
C10	0.1 uF ceramic	tı	C320C104M5R5CA	24.4045				
C11	0.01 uF "	11	C320C103M5R5CA	24.4044				
C12	0.1 uF "	11	C320C104M5R5CA	24.4045				
	C;		1324740					
CRI-3	51	+		37.0600	_3			
		<u>+-</u>						
		<u> </u>						
Q1	NPN trans.		2N3904	64.0120				
Q2	NPN "		2N3904	64.0120				
Q3	PNP "		2N3906	64.0122				
		<u> </u>						
Rl	47K ¼W 5% tol.	Rohm	R25J	55.2473				
R2	15K "	и	11	55.2153				
R3	1M pot. single-turn P.C.	Spect.	63 X 105	54.5202				
R4	18K ¼W 5% tol.	Rohm	R25J	55.2183				
R5	18K "	<u>81</u>	"	55.2183				
R6	150K "	11	11	55.2154				
R7	62K "	11	11	55.2623				
R8	6.2K "	11	11	55.2622				
R9	33K "	u	u 	55.2333				
Ref	Description	Mfr	Mfr Part No	WR Part No	Qty	ltem		
<b>UP</b> communications Itd.								
		vancouver, b	b.c., canada.					
			PARTS	LIST				
		DATE	<sup>:</sup> May 27, 1980	MODEL : RCL-	54B			
AAC	Dec 2,80 C9 change type/value	APPF	OVED : fc	ASSY. NO. 14.0571				
REV APP	DATE ITEM CHANGE TO WR PART	NO.	0	SHEET 1 0	F 2			

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		238 LW	Ee	+01				De	hm	D25 T		55 2222		
	<u>RIU</u>	0 17	<u> </u>			4						55.2333		
	RTT	<u>9.1K</u> "									55.2912			
	<u>R12</u>	<u>62K</u>										55.2623		
	<u>R13</u>	1K pot. single-turn P.C.							ect	<u>63 X 102</u>		54.5200		
	<u>R14</u>	4.7K 1	<u>V 58</u>	tol.					hm	<u>R25J</u>		55.2472		
	<u>R15</u>	<u>15K</u>							"	II 		55.2153		
	<u>R16</u>	30K	11						"			55.2303		
RF	<u>C1</u>	<u>1 - ½ ti</u>	irn f	24 on	bead							31.1065		
	RN1	res. net	twork	7 pos	. 27	<u>K</u>		Be	ck.	764-1R27K		56.0201		
	<u>S1</u>	SPDT too	ggle					Ca	K	7101A		61.0601		
								<u> </u>						
	U1	CMOS qua	ad So	hmitt ]	NAND	)		Mo	t.	MC14093BCP		41.1796		
	U2	CMOS qua	ad Sc	hmitt ]	NAND	)		"		MC14093BCP		41.1796		
1	<u>U3</u>	CMOS qua	ad NC	R				"		MC14001BCP		41.1700		
	U4	CMOS 24-	-bit	count.				"		MC14521BCP		41.1909		
1	U5	dual Op. Amp.						"		MC1458CP1		41,1403		
1	U6	3-term. reg. 8V						11		MC7808CT		41.1603		
					<u></u>									
		РСВ										51.0571		
		Card Handle								23.1070				
		Card Holder									23.1083			
		Heatsink	<u> </u>					Th	erm	THM6106-14		23,2151		
		IC Socket 8-pin					TI		C8408-02		33.1175			
		" " 14-pin				"		C8414-02		33.1176	3			
		" " 16-pin					"		C8416-02		33.1177			
<b> </b>		·· <u>··</u> ····												
R	ef	Description						M	lfr	Mfr Part No		WR Part No	Qty	Item
	4							-		<b>UR</b> comn vancouve PAR	nur er, b. TS	<b>nications</b> .c., canada. LIST	itd.	
									DATE: May 27,1980 MODEL: RCL-54B					
			1			1			APPROVED : 10 ASSY. ND. 14.0571					
REV	APP	DATE	ITEM	CHANGE	то	) WR PART NO.				0	-	SHEET 2 0	F 2	

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