

TABLE OF CONTENTS

	<u>Page</u>
DESCRIPTION .....	1
MAINTENANCE .....	1
Disassembly .....	1
ICOM Frequency Adjustment .....	2
Alignment Procedure .....	3
Test Procedure .....	4
Troubleshooting .....	7 - 8
OUTLINE DIAGRAM .....	9
SCHEMATIC DIAGRAM .....	11
PARTS LIST .....	12

### DESCRIPTION

The MASTR<sup>®</sup> Executive II 152.030-152.810 MHz Synthesized receiver is a single conversion, superheterodyne FM receiver designed for up to thirty-two channels. The solid-state receiver utilizes integrated circuits and discrete components with crystal filters located between gain stages to provide 85 dB selectivity and maximum protection from desensitization and intermodulation.

The receiver consists of the following modules:

- RF Assembly
- Mixer/IF (MIF)
- Synthesizer
- Audio Amplifier (Part of System Board)
- IF Detector
- VHF Receiver Adapter

Supply voltages are connected from the System Board through J903 and P903 on the IF Detector board. The +10 Volt regulated supply is used for all receiver stages and the audio PA stage on the System Board operates from the A+ system supply.

Centralized metering jacks are provided for use with the GE Test Set 4EX3A11. The test set meters oscillator, multipliers, FM Detector and IF amplifier stages. Speaker high and low are metered on the System Board metering jack.

A block diagram of the complete receiver is shown in Figure 1.

Refer to the appropriate Maintenance Manual for complete details on each receiver module as listed in the receiver Table of Contents.

### MAINTENANCE

#### DISASSEMBLY

To gain access to the receiver for servicing, unlock the radio and remove the two retaining screws in the front cover. Then pull the radio out of the mounting frame.

To remove the receiver modules from the radio:

1. Remove all power to the radio.
2. Remove the three countersunk Phillips head screws in the siderail of the radio near the RF casting. NOTE: Do NOT remove the three screws in the bracket along the top edge of the RF casting.
3. Loosen the screws in the two locking tabs on the corners of the RF casting and release the tabs.
4. Remove the two screws securing the IF-DET board to the mounting frame.
5. Unplug the receiver antenna connector.
6. Unplug the lead on the Synthesizer board.
7. Lift the receiver modules out of the radio with a gentle rocking motion.

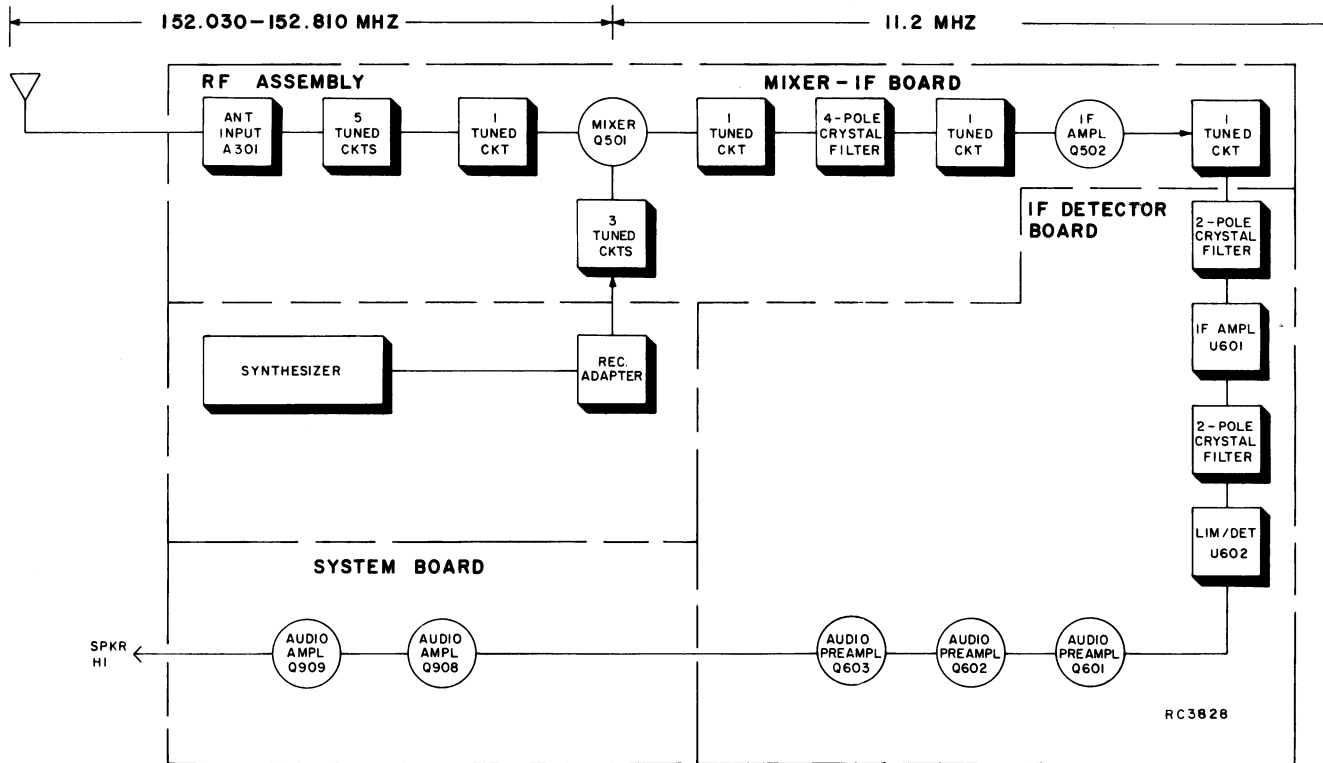


Figure 1 - Synthesized Receiver Block Diagram

ICOM FREQUENCY ADJUSTMENT

First, check the frequency to determine if any adjustment is required. The frequency should be set with a frequency meter or counter with an absolute accuracy that is 5 to 10 times better than the tolerance to be maintained, and with the entire radio as near as possible to an ambient temperature of 26.5°V (79.8°F).

MASTR II ICOMs should be reset only when the frequency shows deviation in excess of the following limits:

1. ±0.5 PPM, when the radio is at 26.5°C (79.8°F).
2. ±2 PPM at any other temperature within the range of -5°C to +55°C (23°F to +131°F).
3. The specification limit (±2 PPM) or ±5 PPM at any temperature within the ranges of -40°C to -5°C (-40°F to +23°F) or +55°C to +70°C (+131°F to +158°F).

If the radio is at an ambient temperature of 26.5°C (79.8°F), set the oscillator for the correct operating frequency.

If the radio is not at an ambient temperature of 26.5°C, setting errors can be minimized as follows:

1. To hold the setting error to ±0.6 PPM (which is considered reasonable for 5 PPM ICOMs):

- a) Maintain the radio at 26.5°C (±5°C) and set the oscillator to desired frequency, or -
- b) Maintain the radio at 26.5°C (±10°C) and offset the oscillator, as a function of actual temperature, by the amount shown in Figure 2.

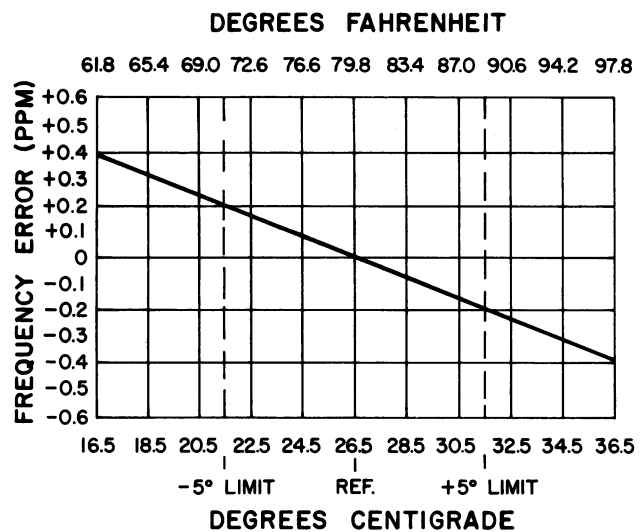


Figure 2 - ICOM Frequency Offset Chart

- To hold setting error to  $\pm 0.35$  PPM (which is considered reasonable for 2 PPM ICOMs): Maintain unit at 26.5°C ( $\pm 5^\circ\text{C}$ ) and offset the oscillator, as a function of actual temperature, by the amount shown in Figure 2.

RECEIVER ALIGNMENT

EQUIPMENT NEEDED

- GE Test Set Model 4EX3A11 or Test Kit 4EX8K12.
- An 11.2 MHz signal source (GE Test Set Model 4EX9A10). Also, a 150-174 MHz signal source (Measurement 803) with a one-inch piece of insulated wire no larger than 0.65 inch diameter connected to the generator probe.
- A Voltmeter.
- An Ammeter (capable of measuring 20 milliamperes).
- Distortion analyzer.

ALIGNMENT PROCEDURE

- Connect the black plug from the Test Set to centralized metering jack J17 on the Synthesizer mother board A901. Connect the red plug to System Board metering jack J902. Set range selector switch to the TEST 1 position (or 1 Volt position on 4EX8K12).

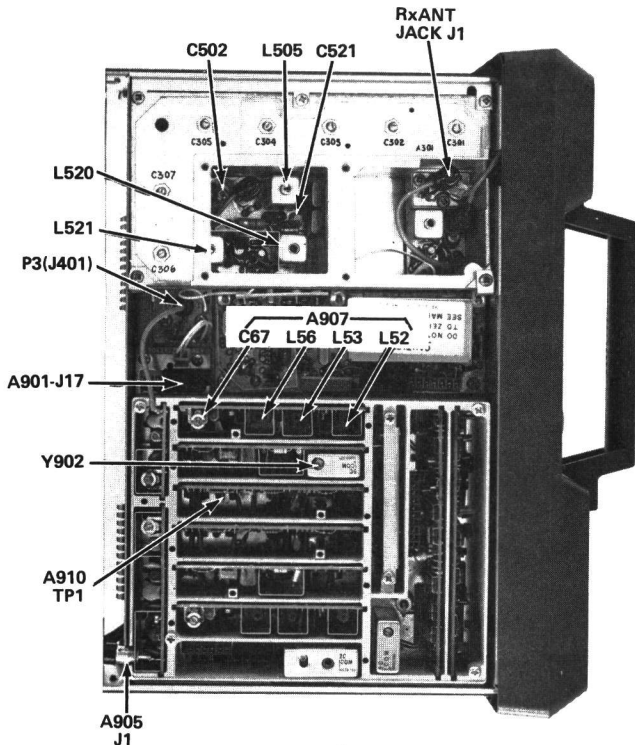


Figure 3 - Alignment Adjustment Locations

- With Test Set in position "J", check for regulated +10 Volts. If necessary, adjust R911 on the System Board for 10 Volts.
- Connect the transmitter VCO output A905-J1 to a 50 ohm load. Connect a 200 MHz frequency counter through a 10 dB pad to P3.
- Select Channel 1 on the control unit and make the following adjustments on Oscillator/Multiplier board A907:

Test Meter Position	Adjust Component on A907
A	Peak L52
B	Peak L53
D	Dip L56
D	Peak C67
D	Peak L56 and C67

- Connect a 10 Volt, 10 megohm DC voltmeter to TP1 on the A910 Phase Detector and adjust C5 on the A905 Receiver VCO to produce 6.0 Volts as read at TP1.
- Adjust the frequency of the Y902 receiver ICOM to produce a local oscillator frequency of 140.830 MHz (RCC) or 141.310 (IMTS)  $\pm 0.4$  PPM (56 Hz).
- Disconnect the 10 dB pad and frequency counter from P3. Measure power output. This should be +10 dBm or greater.
- Plug P3 into J401 on the Receiver Adapter Board.
- Pre-adjust C306 and C307 on the RF Assembly to the minimum capacity (full out) position.
- With the Test Set in the "D" position, carefully dip C306. DO NOT RE-ADJUST.
- Carefully dip C307. DO NOT RE-ADJUST.
- With the signal generator probe inserted in the hole adjacent to C305, set the generator to the desired receiver frequency and adjust C502 on the MIF board for maximum IF current reading on the Test Set at position "B".
- Using the probe at the end of the signal generator lead, make the following adjustments:
  - Probe in hole adjacent to C304, adjust C305 for maximum.
  - Probe in hole adjacent to C303, adjust C304 for maximum.
  - Probe in hole adjacent to C302, adjust C303 for maximum.
  - With probe in hole adjacent to C302, peak C305, C304 and C303.
- Connect the signal generator to the antenna input jack J1 and adjust C301,

C302, C303, C304, C305 and C502 for a peak reading, reducing the signal generator output level as necessary to keep the reading below saturation.

15. The mixer and IF circuits have been aligned at the factory and will normally require no further adjustment. If adjustment is necessary, connect the signal generator, scope and probe as shown in Figure 4. Set signal generator level for 3 to 5  $\mu$ V and modulate with 10 kHz at 20 Hz. With probe between J601-1 and A- on the IF Detector Board, tune L505, L520, L521 and C521 on the MIF board for a double trace as shown on the scope pattern.

### TEST PROCEDURES

These Test Procedures are provided for servicing a receiver that is not operating properly. The problems encountered could be low power, poor sensitivity, distortion, limiter not operating properly and low gain. By following the test steps, the defect can be quickly located and corrected. Additional corrective measures are provided in the Troubleshooting Procedures. Before beginning the test procedure, be sure the receiver is properly aligned to the operating frequency.

### TEST EQUIPMENT REQUIRED

- Distortion Analyzer (similar to Heath IM-12).

- Signal Generator (similar to Measurements 803).
- 6 dB Attenuation Pad and 600 ohm, 1 Watt Resistor.

### PRELIMINARY CHECKS

1. Connect the test equipment to the receiver as shown in Figure 5.
2. Turn on all equipment and allow a warm-up period of 20 minutes.

### AUDIO POWER OUTPUT AND DISTORTION

Measure audio power output as follows:

1. Apply a 1,000 microvolt, on-frequency test signal modulated by 1000 Hertz with  $\pm 3.0$  kHz deviation to the antenna jack A301-J1.
2. Lift the handset on the control unit off-hook. Connect the distortion analyzer input across the earpiece leads with the earpiece disconnected. Connect a 600 ohm, 1 Watt resistor across the leads.
3. Read 0.3 milliwatts output using the distortion analyzer as a VTVM (.42 Vrms).
4. Make distortion measurements according to the distortion analyzer manufacturer's instructions. Reading should be less than 5%.
5. If the distortion is more than 5%, or maximum audio output is less than .3 milliwatts, make the following checks:

NOTE 1: Appendix A of DATAFILE Bulletin 1000-6 contains instructions for building a sweep modulator.

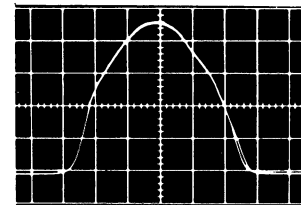
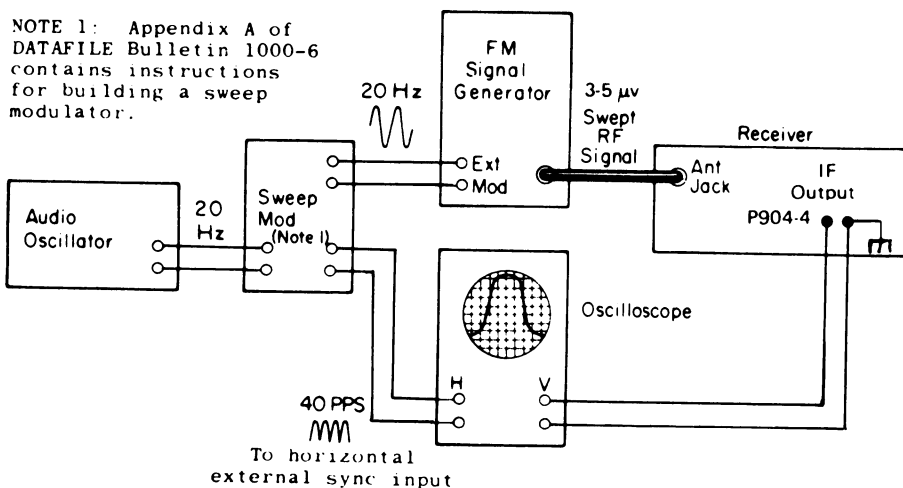


Figure 4 - Test Setup for 20 Hz Double-Trace Sweep Alignment

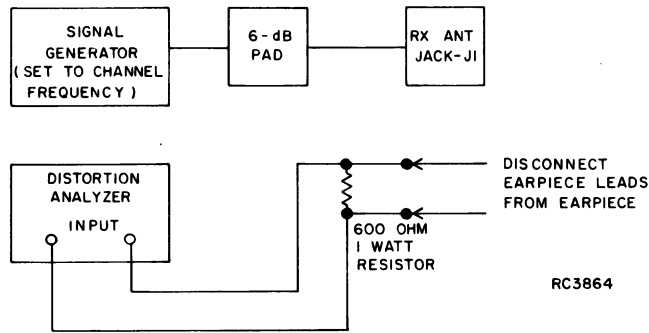


Figure 5 - Test Equipment Set-up

- a) Battery and regulator voltage. Low voltage will cause distortion.
- b) Audio gain.
- c) FM Detector board adjustment (refer to receiver alignment instructions).

### USABLE SENSITIVITY (12 dB SINAD)

Measure the receiver sensitivity as follows:

1. Apply a 1000 microvolt, on-frequency signal modulated by 1000 Hertz with 3.0 kHz deviation to antenna jack A301-J1.
2. Place the RANGE switch on the distortion analyzer in the 200 to 2000 Hz distortion range position (1000 Hz filter in the circuit). Tune the filter for minimum reading or null on the lowest possible scale (100%, 30%, etc.).
3. Place the RANGE switch to the SET LEVEL position (filter out of circuit) and adjust input LEVEL control for a +12 dB reading on a mid-range (30%).

4. While reducing the signal generator output, switch the RANGE switch from SET LEVEL to the distortion range until a 12 dB difference (+2 dB to -10 dB) is obtained between the SET LEVEL and distortion range positions (filter out and filter in).
5. The 12 dB difference (signal plus noise and distortion to noise plus distortion ratio) is the "usable" sensitivity level. The sensitivity should be less than rated 12 dB SINAD specifications.
6. If necessary, slightly adjust C301, C302, C303, C304 and C305 on the RF Assembly and C502 on the MIF board for best SINAD or sensitivity. C306 and C307 may also be tuned slightly, not exceeding 1/4 turn.

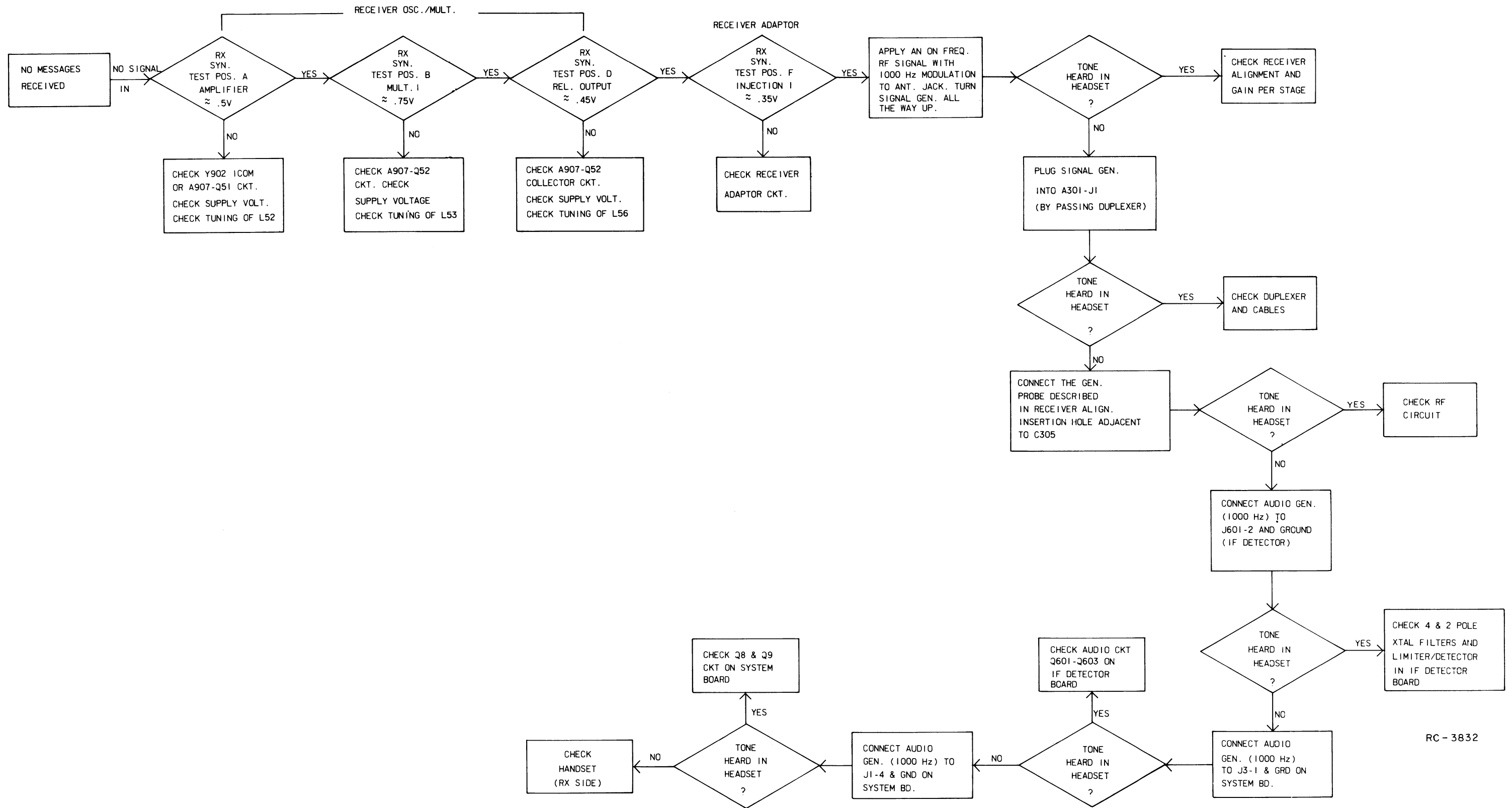
### MODULATION ACCEPTANCE BANDWIDTH

Measure the bandwidth as follows:

1. Set the signal generator output for twice the microvolt reading obtained in the 12 dB SINAD measurement.
2. Set the RANGE control on the distortion analyzer to the SET LEVEL position (1000 Hz filter out of the circuit), and adjust the input LEVEL control for a +2 dB reading on the 30% range.
3. While increasing the deviation of the signal generator, switch the RANGE control from SET LEVEL to distortion range until a 12 dB difference is obtained between the SET LEVEL and distortion range readings (from +2 dB to -10 dB).
4. The deviation control reading for the 12 dB difference is the Modulation Acceptance Bandwidth of the receiver. It should be more than 6.5 kHz.
5. If the Modulation Acceptance Bandwidth test does not indicate the proper bandwidth, check the Troubleshooting Procedure.

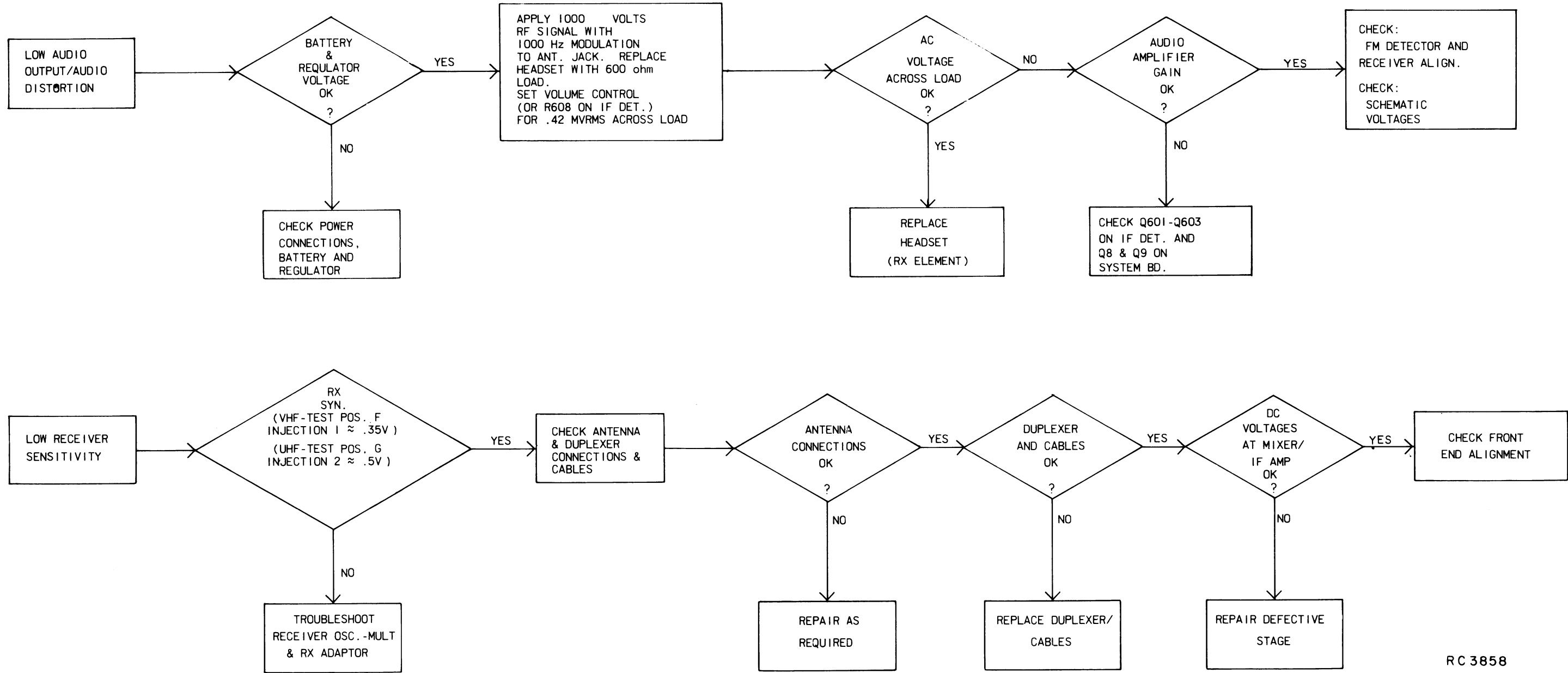
GENERAL ELECTRIC COMPANY • MOBILE COMMUNICATIONS DIVISION  
WORLD HEADQUARTERS • LYNCHBURG, VIRGINIA 24502 U.S.A.

**GENERAL**  **ELECTRIC\***  
U.S.A.



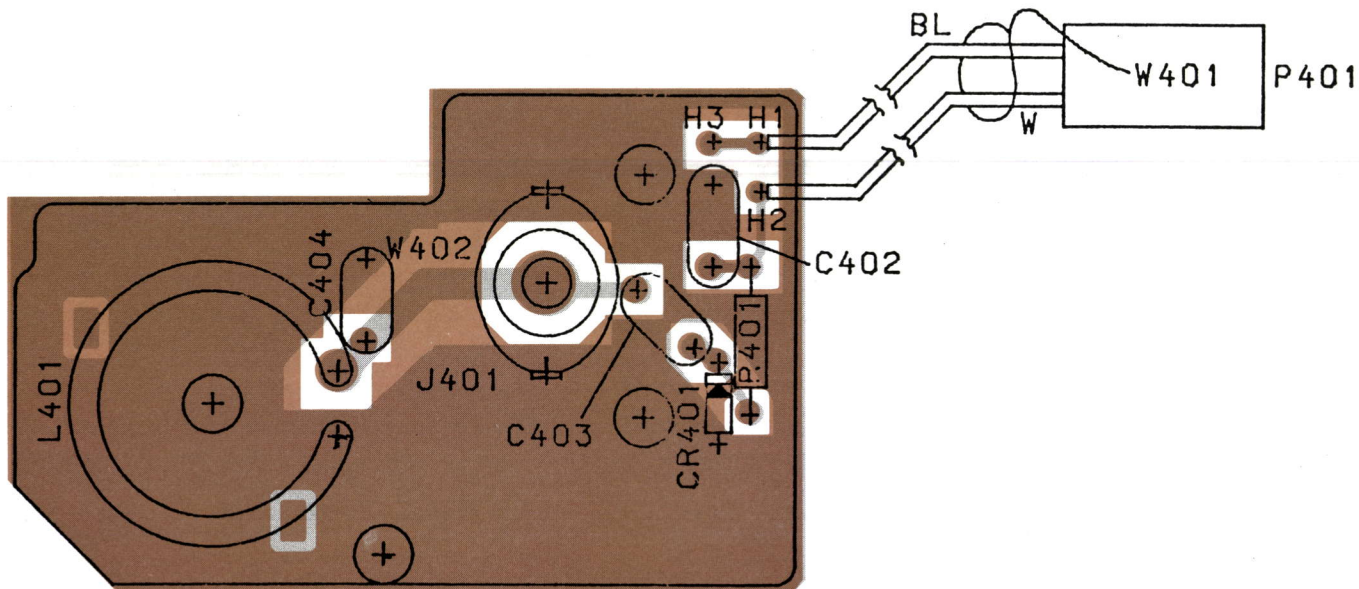
RC - 3832

FIGURE 6 - TROUBLESHOOTING FLOW CHART (SHEET 1)

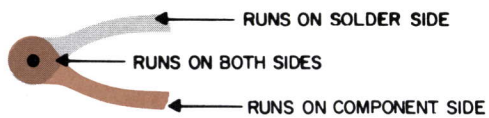


RC 3858

FIGURE 6 - TROUBLESHOOTING FLOW CHART (SHEET 2)

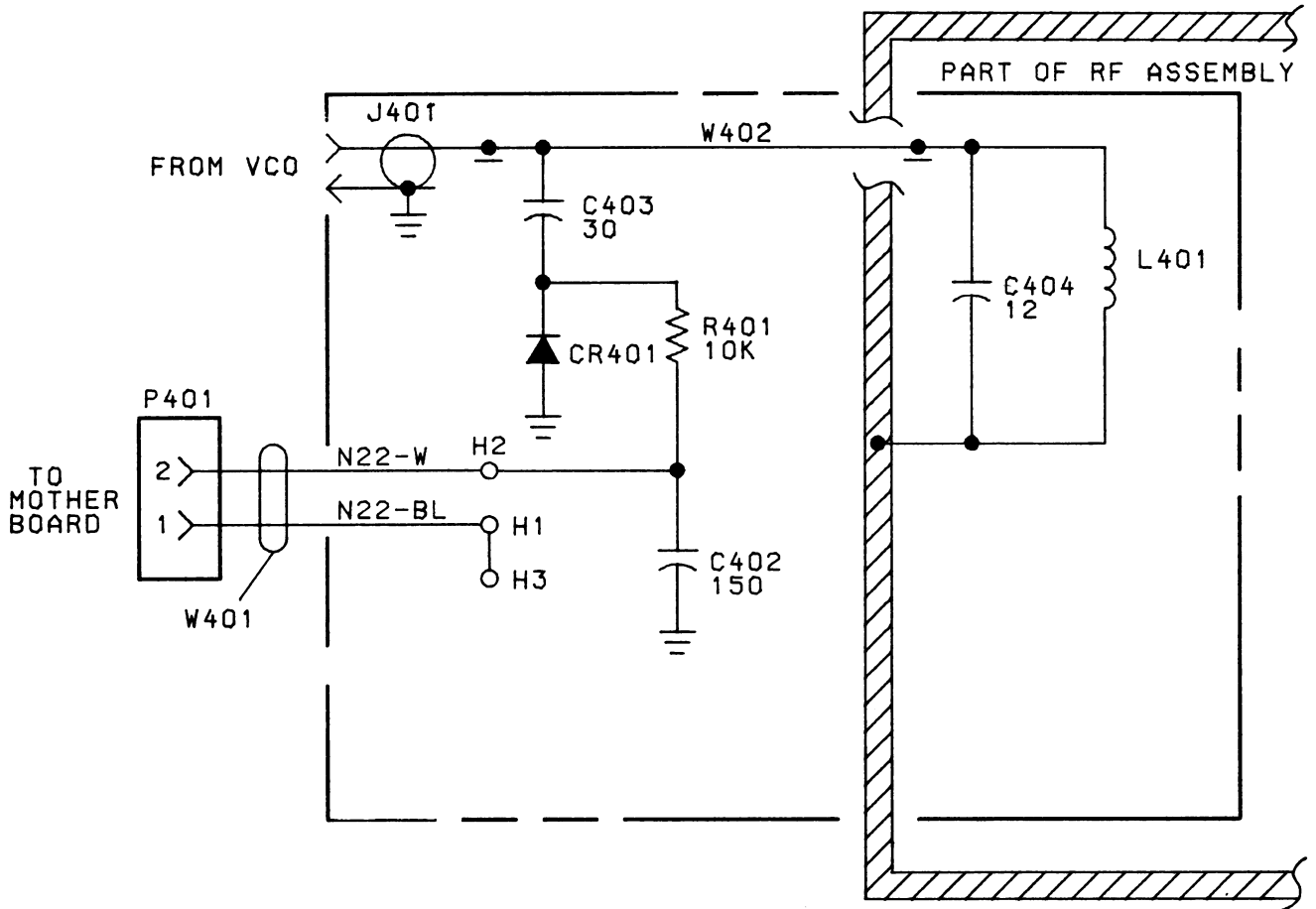


(19B232920, Rev. 1)  
(19A137435, Sh. 1, Rev. 0)  
(19A137435, Sh. 2, Rev. 0)



OUTLINE DIAGRAM  
VHF RECEIVER ADAPTER  
19B232538G1





ALL RESISTORS ARE 1/4 WATT UNLESS OTHERWISE SPECIFIED AND RESISTOR VALUES IN OHMS UNLESS FOLLOWED BY K-1000 OHMS OR MEG-1,000,000 OHMS. CAPACITOR VALUES IN PICO FARADS (EQUAL TO MICROMICROFARADS) UNLESS FOLLOWED BY UF-MICROFARADS. INDUCTANCE VALUES IN MICROHENRYS UNLESS FOLLOWED BY MH-MILLIHENRYS OR H-HENRYS.

MODEL NO	REV LETTER
PL19B232538G1	

(19B232923, Rev. 1)

**SCHEMATIC DIAGRAM**

**VHF RECEIVER ADAPTER  
19B232538G1**

PARTS LIST

VHF RECEIVER ADAPTER  
 19B232538G1  
 ISSUE 1

SYMBOL	GE PART NO.	DESCRIPTION
		----- CAPACITORS -----
C402	19A116655P7	Ceramic disc: 150 pf ±20%, 1000 VDCW; sim to RMC Type JF Discap.
C403	19A116656P30J0	Ceramic disc: 30 pf ±5%, 500 VDCW, temp coef 0 PPM.
C404	19A116656P12J0	Ceramic disc: 12 pf ±5%, 500 VDCW, temp coef 0 PPM.
		----- DIODES AND RECTIFIERS -----
CR401	19A116052P1	Silicon, hot carrier: Fwd. drop .350 volts max.
		----- JACKS AND RECEPTACLES -----
J401	19A116832P1	Receptacle, coaxial: jack type; sim to Cinch 14H11613.
		----- INDUCTORS -----
L401	19A129280P1	Coil.
		----- PLUGS -----
P401		Connector. Includes:
	19A116659P138	Shell.
	19A116781P6	Contact, electrical: wire range No. 22-26 AWG; sim to Molex 08-50-0108. (Quantity 2).
		----- RESISTORS -----
R401	3R152P103J	Composition: 10K ohms ±5%, 1/4 w.
		----- CABLES -----
W401	19B232538G2	Cable assembly. (Includes P401).
W402		(Part of printed wire board 19C328261P1).