

MAINTENANCE MANUAL FOR

General Electric

MPI CHANNEL GUARD, DIGITAL CHANNEL GUARD & CARRIER CONTROL TIMER

LB131174D



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CHANNEL GUARD (CG)

DESCRIPTION

The MPI Channel Guard is a continuous-tone encoder and decoder for operation on tone frequencies in the 71.9 to 210.7 Hz range. Both the encoder and decoder operate on the same frequency. The option consists of a Channel Guard module and an option board. The option board provides the proper interface with the transmitter-receiver board.

The Channel Guard module contains a tone frequency synthesizer, encoder, decoder and Squelch Tail Eliminator circuitry (see Figure 1). The synthesizer is programmable to produce Channel Guard tones from 67 to 210.7 Hz in 0.25 Hz increments.

The synthesizer uses a crystal controlled 32,768 Hz reference to produce the desired clock inputs to the encoder and decoder circuits and produce digitally generated time delays for the STE circuitry.

The Channel Guard circuit is controlled by an CG-OFF switch on the control unit. Placing the switch in the OFF position disables the decoder circuits to permit monitoring all calls on the channel. Placing the switch in the CG position enables the Decoder.

CIRCUIT ANALYSIS

ENCODE

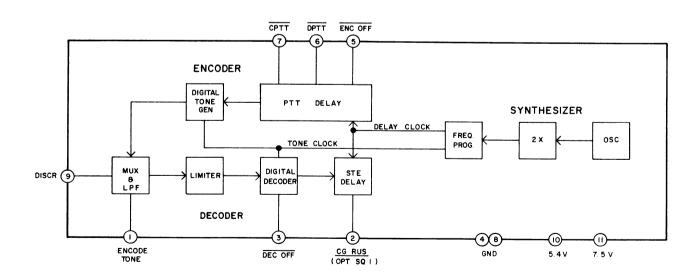
When the transmit circuit is keyed, the CPTT lead is pulled low and the

Channel Guard module responds by pulling the DPTT lead low, holding the transmit circuit in a keyed condition. The encoder circuit generates a sine wave encoder tone which passes through a low pass filter to remove any clock and tone harmonics. This output tone is connected by the CG output lead to the transmitter audio circuit.

When the radio is unkeyed, the CPTT lead goes high but the PTT delay circuit holds the transmit circuit in a keyed condition for an additional 160 milliseconds by holding the DPTT lead low during this time. During this 160 millisecond time, the encode circuit sends the tone with a 135° phase shift. This combination of 135° phase shift and 160 millisecond delay causes the CG decoder in other receivers to squelch the receiver audio prior to loss of RF signal. This reduces or eliminates the receiver noise burst.

DECODE

In the decode mode, receiver audio from the quadrature detector is applied to the CG module where it passes through a 212 Hz low pass filter to remove voice information. This prevents voice falsing or clipping in the decoder circuit. The digital decoder compares the frequency of the incoming tone to a reference clock produced by the synthersizer. If the correct tone is detected, the module responds by releasing the RUS (Receiver UnSquelched) lead which is normally held in a low voltage condition when the correct tone is not detected.



RC-3751

Figure 1 - Channel Guard Module

After decoding the tone, the decoder then looks for a phase shift to occur. If the phase shift occurs, the decoder responds by pulling CG RUS low for 200 milliseconds using the STE delay circuit. This forces the receive circuit to squelch for 200 ms during which time the received carrier should disappear.

DIGITAL CHANNEL GUARD (DCG)

DESCRIPTION

The DCG module contains three integrated circuits consisting of a system linear circuit, an encoder circuit and a decoder circuit (see Figure 2).

The system linear circuit contains the required analog circuits, including a crystal controlled slow clock oscillator circuit, a free running fast clock oscillator circuit, a limiter circuit, for received audio, active filter components and system interfacing.

CIRCUIT ANALYSIS

ENCODE

The encoder circuit is programmed to produce a repetitive, twenty-three bit binary word. This binary word can be one

of 83 possible codes between 023 and 754 (see DCG Programming Procedure). The programming is accomplished by nine programming pins located on the top of the module.

When the transmit circuit is keyed, the CPTT lead is pulled low and the Digital Channel Guard module responds by pulling the DPTT lead low, holding the transmit circuit in a keyed condition. The encoder circuit generates a repetitive twenty-three code word, using the slow 32 kHz clock, which passes through a low pass filter (Digital Code Filter) to remove any high frequency components. The digital output is connected by the CG Encode lead to the transmit audio circuit.

When the radio is unkeyed, the CPTT lead goes high, but the PTT delay circuit holds the transmit circuit in a keyed condition for an additional 180 milliseconds by holding the DPTT lead low during this time. During this 180 milliseconds delay, the digital word generator generates a 135 Hz sine wave. This transmitted sine wave and the 180 millisecond delay causes the DCG decoder in other receivers to squelch the receiver circuit audio prior to loss of RF signal. This reduces or eliminates the receiver noise burst.

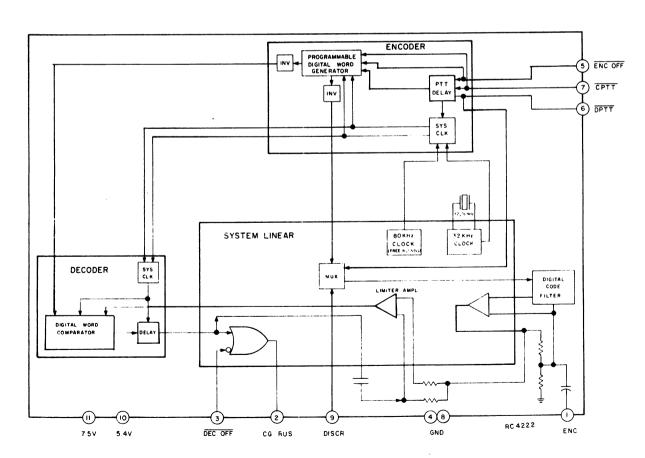


Figure 2 - Digital Channel Guard Module

DECODE

In the decode mode, receiver audio from the quadrature detector lead is fed to the DCG module where it passes through a 212 Hz low pass filter to remove voice information. This prevents interference in detecting the correct code word in the decoder circuit. The decoder circuit compares the digital code word received by the receive circuit to the digital code word generated by the digital word generator. All possible combinations of the code word are check to insure the correct code word is detected. Comparisons are made at the fast 80 kHz clock rate for a fast response time. The module responds by releasing the RUS lead normally held in a low voltage condition

when the correct code word is not detected. Releasing the RUS lead unsquelches the receive circuit.

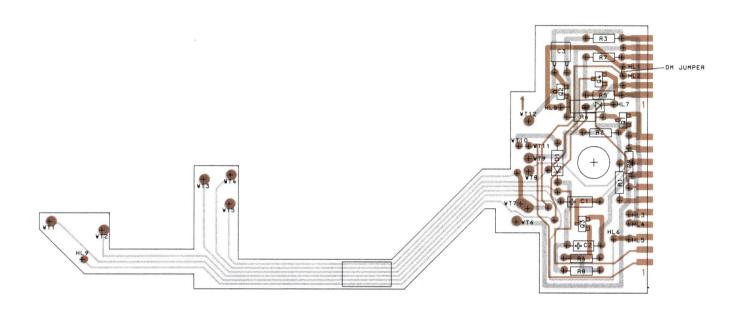
CARRIER CONTROL TIMER

DESCRIPTION

MPI Carrier Control Timer automatically interrupts the transmission of a transmitter by deactivating the system PTT after a 70 second ±10 seconds timing cycle. The timer also alerts the operator that the transmitter is off with a 950 Hertz alarm tone from the speaker as long as the push-to-talk switch is pressed. Transmission can be resumed by releasing the push-to-talk switch and re-keying the transmitter.

GENERAL ELECTRIC COMPANY+ MOBILE COMMUNICATIONS DIVISION WORLD HEADQUARTERS+LYNCHBURG, VIRGINIA 24502 U.S.A.



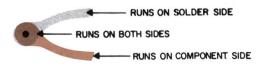


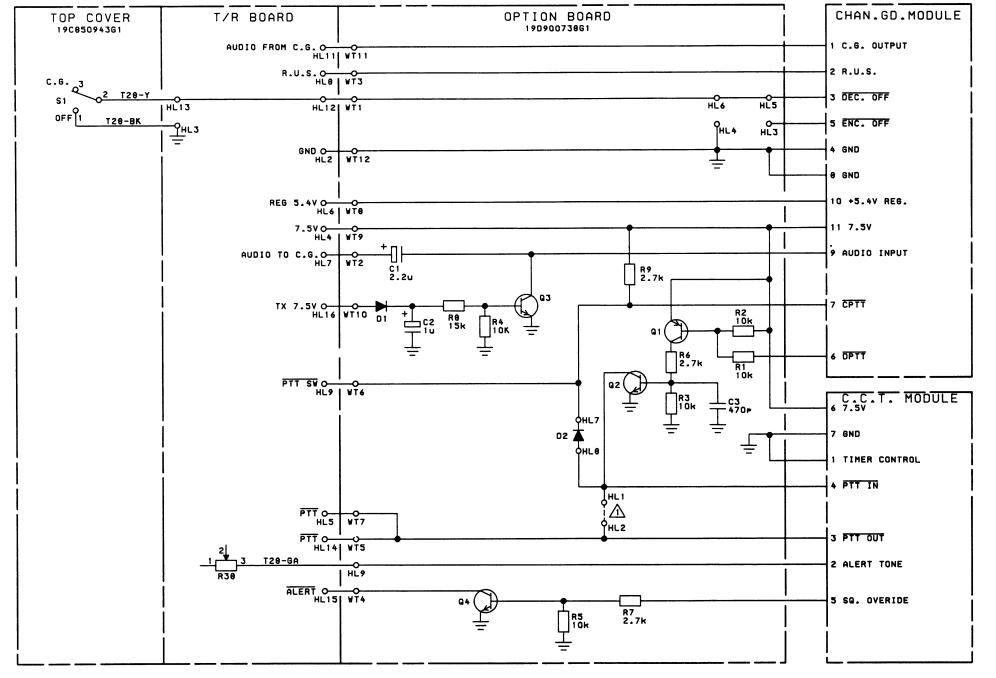


(19D900740, Rev. 3) (19A702588, Sh. 1, Rev. 1) (19A702588, Sh. 2, Rev. 1)

OUTLINE DIAGRAM

CHANNEL GUARD AND CARRIER CONTROL TIMER





NOTES

JUMPER FOR NON CCT UNITS.

ALL RESISTORS ARE 1/0 WATT UNLESS OTHERWISE SPECIFIED.
RESISTOR VALUES IN A UNLESS FOLLOWED BY MULTIPLIER & OR M.
CAPACITOR VALUES IN F UNLESS FOLLOWED BY MULTIPLIER # OR P.
INDUCTANCE VALUES IN H UNLESS FOLLOWED BY MULTIPLIER # OR P.

MODEL NO. REV. LETTER
PL19D900738G1

(19D900741, Rev. 5)

SCHEMATIC DIAGRAM

CHANNEL GUARD AND CARRIER CONTROL TIMER

LBI31174

PARTS LIST

MPI OPTION BOARD 19D900738G1 ISSUE 2

PARTS LIST

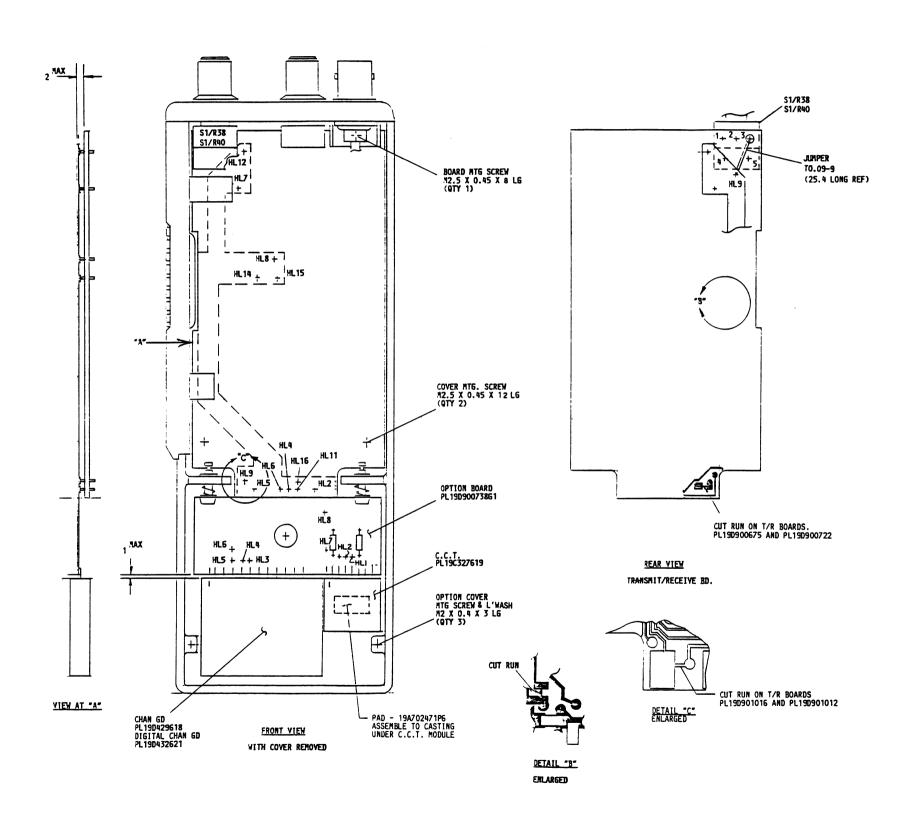
CARRIER CONTROLLED TIMER
AND
CHANNEL GUARD HARDWARE KIT
19A702765G1 CARRIER CONTROLLED TIMER
19A702765G2 CHANNEL GUARD ENCODER
19A702765G3 CHANNEL GUARD ENCODER/DECODER
ISSUE 2

 CHAIN	1,17	u	Un
	ISS	U	Е

SYMBOL	GE PART NO.	DESCRIPTION
C1	19B800650P14	Tantalum: 2.2 uF +40-20%, 10 VDCW.
C2	19B800650P13	Tantalum: 1 uF -20+40%, 10 VDCW.
СЗ	19A700121P7	Ceramic: 470 pF <u>+</u> 20%, 50 VDCW.
	ı	
D1	19A702015P1	Silicon; sim to IN458A.
D2	19A115100P1	Silicon: sim to Type 1N458A.
Q1	19A700059P1	Silicon, PNP.
Q2	19A134739G1	Silicon, NPN.
Q3 and	19A700076P1	Silicon, NPN.
Q4		
		RESISTORS
R1 thru R5	19A702585P87	Composition: 10K ohms ±5%, 150 VDCW, 1/8 w.
R6 and R7	19A702585P73	Composition: 2.7K ohms ±5%, 150 VDCW, 1/8 w.
R8	19A702585P91	Composition: 15K ohms ±5%, 150 VDCW, 1/8 w.
R9	19A702585P73	Composition: 2.7K ohms ±5%, 150 VDCW, 1/8 w.
WT1 thru WT12	19A702752P1	Contact.
		MISCELLANEOUS
	19A702471P6	Pad. (Located on printed board 19D900739P1).
		·
	I	ı

SYMBOL	GE PART NO.	DESCRIPTION
	19D900738G1	MPI Option Board.
1	19C850943G1	Top Cover.
	19D429521P1	Cover. (Located over Channel Guard select pins)
	19B219681P1	Contact, electrical. (Located on terminals 0-9 program CG freq. or DCG Code).
		ASSOCIATED PARTS (Not part of Hardware Kit 19A702765)
	19B800865G3	Top Cover, Channel Guard.
	19D429618G5	1 Tone Channel Guard - Encode Module.
	19D429618G6	1 Tone Channel Guard - Encode/Decode Module.
	19C327619G1	Carrier Controlled Timer.
	19D432621G9	1 Code Digital Channel Guard (Postive Phase) Module.
	19D432621G10	1 Code Digital Channel Guard (Negative Phase) Module.
<u> </u>	<u> </u>	

*COMPONENTS ADDED, DELETED OR CHANGED BY PRODUCTION CHANGES.



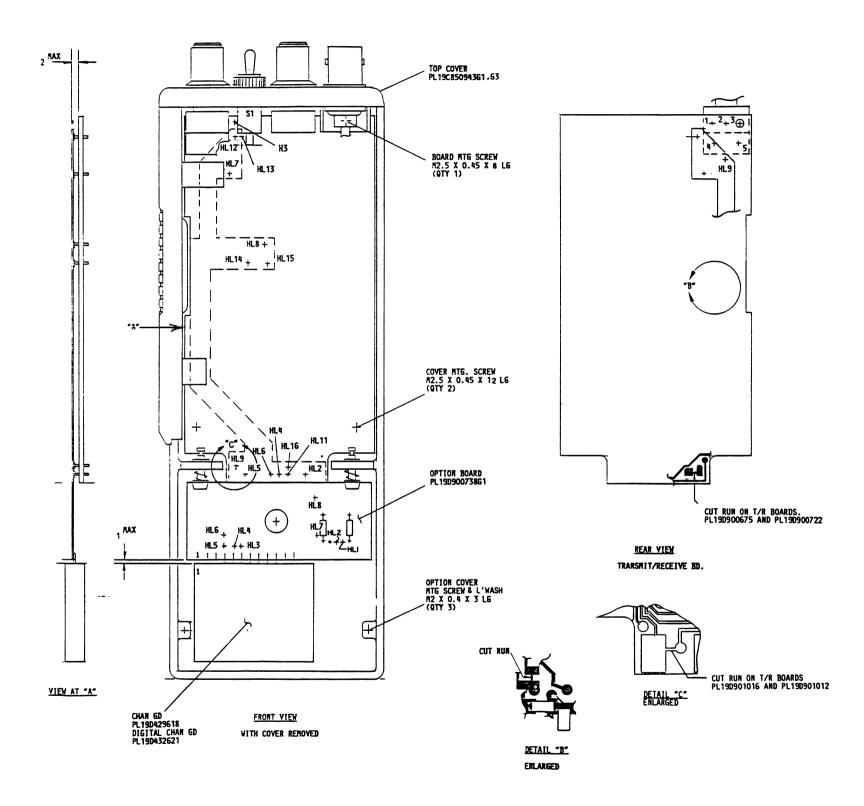
(19D900782, Sh.1, Rev. 4)

- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAM. GD. KIT PL'99A70276561 FOR APPLICATION OF CARRIER CONTROL TIMER 10 MPI PERSONAL RADIO
 - ★ 1. REMOVE BATTERY PACK, FRONT COVER AND OPTION COVER. REMOVE SCREN FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/ RECEIVE BOARD AS AN ASSEMBLY.
 - 2. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN. (2 PLACES).
 - 3. SOLDER C.C.T. MODULE TO OPTION BOARD AS SHOWN.
 - 4. SOLDER JUMPER FROM HL9 ON OPTION BOARD TO S1/R38-3 OR S1/R40-3 ON SOLDER SIDE OF TRANSMIT/RECEIVE BOARD. REMOVE DM JUMPER FROM HL1 TO HL2 ON OPTION BOARD.
 - ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD
 - 来 6. REASSEMBLE TRANSMIT/RECEIVE BOARD, TOP COVER, REAR COVER, OPTION COVER, FRONT COVER, AND BATTERY PACK.
- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAM. GD. KIT PL19A702765G2 FOR APPLICATION OF CHAMNEL GUARD/DIGITAL CHAMNEL GUARD ENCODE ONLY TO
 - * 1. REMOVE BATTERY PACK, FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/RECEIVE BOARD AS AN ASSEMBLY.
 - 2. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES).
 - SOLDER DESIRED CHANNEL GUARD MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS 4 OR 5 FOR CODING INSTRUCTIONS.
 - 4. ON DIGITAL CHANNEL GUARD ONLY-SOLDER DA JUMPER FROM HL4 TO HL5 ON
 - ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD
 - ♣ 6. REASSEMBLE TRANSMIT/RECEIVE BOARD, TOP COVER, REAR COVER, OPTION COVER, FRONT COVER, AND BATTERY PACK.
- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAN. GD. KIT PL19A70276562 FOR APPLICATION OF CHANNEL GUARD/DIGITAL CHANNEL GUARD ENCODE ONLY AND CARRIER CONTROL TIMER TO M.P.I. PERSONAL RADIO
 - * 1. REMOVE BATTERY PACK, FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/RECEIVE BOARD AS AN ASSEMBLY.
 - 2. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES).
 - SOLDER DESIRED CHANNEL GUARD MODULE AND CARRIER CONTROL TIMER MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS 4 OR 5 FOR CODING INSTRUCTIONS.
 - 4. SOLDER JUMPER FROM HL9 ON OPTION BOARD TO S1/R38-3 OR S1/R40-3 ON SOLDER SIDE OF TRANSMIT/RECEIVE BOARD. REMOVE DM JUMPER FROM HL1 TO HL2 ON OPTION BOARD. ON DIGITAL CHANNEL GUARD ONLY-SOLDER DA JUMPER FROM HL4 TO HL5 ON OPTION BOARD.

 5. ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD

 - * 6. REASSEMBLE TRANSMIT/RECEIVE BOARD: TOP COVER, REAR COVER, OPTION COVER, FRONT COVER, AND BATTERY PACK.
 - * APPLIES ONLY IF OPTION IS INSTALLED IN AN ASSEMBLED RADIO.

LBI31174



INSTALLATION INSTRUCTIONS

Issue 2

(19D900782, Sh. 2, Rev. 4)

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- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAN. GD. KIT PL19A70276563,64 FOR APPLICATION OF CHANNEL GUARD/DIGITAL CHANNEL GUARD ENCODE ONLY WITH OFF/ON SWITCH TO MPI PERSONAL RADIO
 - * 1. REMOVE BATTERY PACK, FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/ RECEIVE BOARD AS AN ASSEMBLY.
 - *2. REMOVE KNOBS & NUTS SECURING TOP COVER TO TRANSMIT/RECEIVE BOARD POTS, REMOVE EXISTING TOP COVER. BEFORE ASSEMBLING NEW TOP COVER PL19C850943G1,63 MAKE THE FOLLOWING CONNECTIONS.

FROM	TO	WIRE
S1-1	HL3	BK
S1-2	HL13	Y

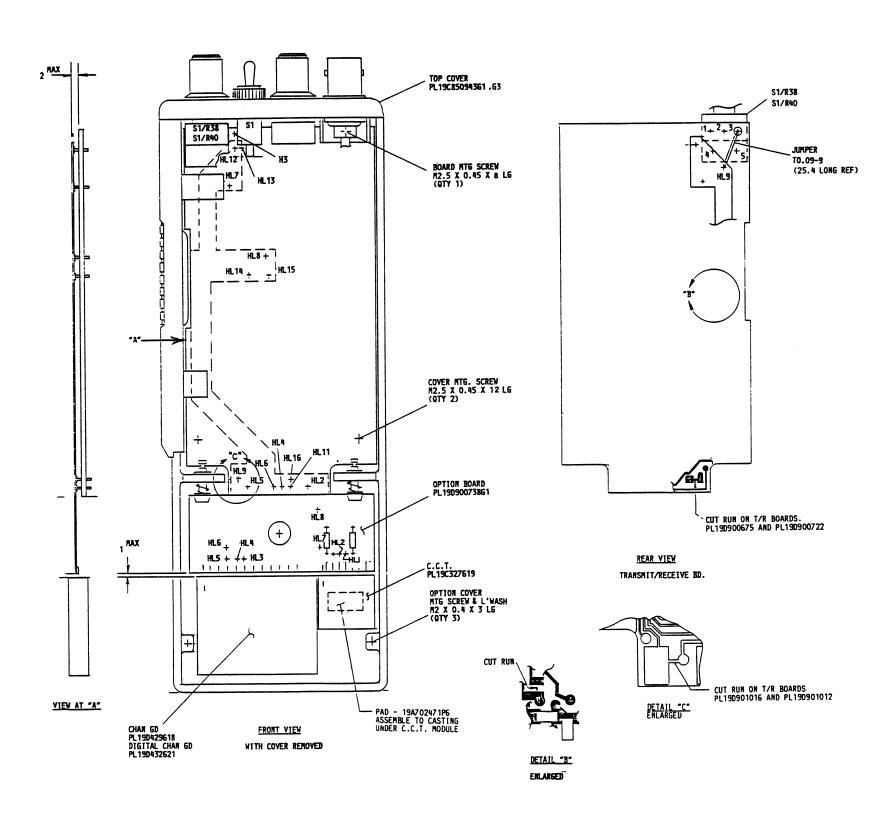
- 3. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES)
- SOLDER DESIRED CHANNEL GUARD MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS & OR 5 FOR CODING INSTRUCTIONS.
- 5. ON OPTION BOARD CUT RUN BETWEEN HLS & HL6 AS SHOWN.
- 6. MAKE THE FOLLOWING CONNECTIONS ON THE OPTION BOARD

FROM	то	WIRE	REMARKS
HL4	HL5	DA	DIGITAL CHANNEL GUARD ONLY
HL3	HLF	DA	

- ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD.
- ★ 8. REASSEMBLE TRANSMIT/RECEIVE BOARD, TOP COVER, REAR COVER, OPTION COVER, AND BATTERY PACK.
- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAN. GD. KIT PL19A70275553.64 FOR APPLICATION OF CHANNEL GUARD/DIGITAL CHANNEL GUARD ELECT WITH MONITOR SWITCH TO MPI PERSONAL RADIO
 - *1. REMOVE BATTERY PACK. FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/RECEIVE BOARD AS AN ASSEMBLY.
 - *2. REMOVE KNOBS 8 MUTS SECURING TOP COVER TO TRANSMIT/RECEIVE BOARD POTS, REMOVE EXISTING TOP COVER. BEFORE ASSEMBLING NEW TOP COVER PL19C850943G1,G3 MAKE THE FOLLOWING CONNECTIONS.

FROM	TO	WIRE
S1-1	HL3	BK
S1-2	HL13	Y

- 3. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES).
- 4. SOLDER DESIRED CHANNEL GUARD MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS 4 OR 5 FOR CODING INSTRUCTIONS.
- ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD.
- ★ 6. REASSEMBLE TRANSMIT/RECEIVE BOARD_TOP_COVER; REAR_COVER; OPTION COVER, AND BATTERY PACK.
- * APPLIES ONLY IF OPTION IS INSTALLED IN AN ASSEMBLED RADIO.



- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAN. 6D. KIT PL19A70276563.64
 FOR APPLICATION OF CHANNEL GUARD/DIGITAL CHANNEL GUARD ENCODE ONLY
 WITH OFF/ON SWITCH AND CARRIER CONTROL TIMER TO MPI PERSONAL RADIO
 - * 1. REMOVE BATTERY PACK, FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/ RECEIVE BOARD AS AN ASSEMBLY.
 - * 2. REMOVE KNOBS & MUTS SECURING TOP COVER TO TRANSMIT/RECEIVE BOARD POTS, REMOVE EXISTING TOP COVER. BEFORE ASSEMBLING NEW TOP COVER PL19C850943G1,G3 MAKE THE FOLLOWING CONNECTIONS.

51-1	HL3	BK
51-2	HL 13	Y

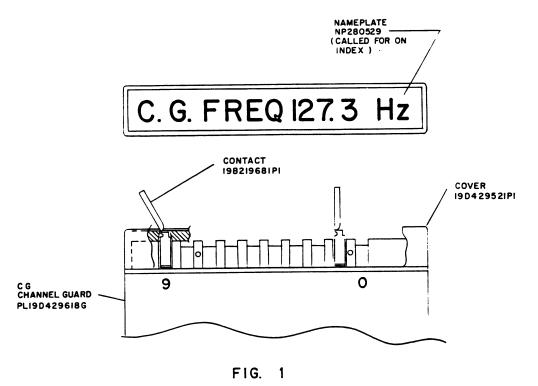
- ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES).
- 4. SOLDER DESIRED CHANNEL GUARD MODULE AND CARRIER CONTROL TIMER MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS 4 OR 5 FOR CODING INSTRUCTIONS.
- 5. ON OPTION BOARD CUT RUN BETWEEN HL5 & HL6 AS SHOWN.
- 6. MAKE THE FOLLOWING CONNECTIONS ON THE OPTION BOARD

	FROM	TO	WIRE	REMARKS
	HL4	HL5	DA	DIGITAL CHANNEL GUARD ONLY
Ī	HL3	HL6	DA	
	HLI	HL2	DM	REMOVE

- 7. SOLDER JUMPER FROM HL9 ON OPTION BOARD TO S1/R38-3 OR S1/R40-3 ON SOLDER SIDE OF TRANSMIT/RECEIVE BOARD.
- 8. ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER: MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD.
- # 9. REASSEMBLE TRANSMIT/RECEIVE BOARD, TOP COVER, REAR COVER, OPTION COVER, AND BATTERY PACK.
- THESE INSTRUCTIONS COVER THE INSTALLATION OF CHAN. GD. KIT PL19A702765G3.64 FOR APPLICATION OF CHANNEL GUARD/DIGITAL CHANNEL GUARD ENCODE/DECODE WITH MONITOR SWITCH AND CARRIER CONTROL TIMER TO MPI PERSONAL RADIO
 - ★ 1. REMOVE BATTERY PACK. FRONT COVER AND OPTION COVER. REMOVE SCREW FROM ANTENNA MOUNTING BRACKET AND REMOVE TOP COVER AND TRANSMIT/ RECEIVE BOARD AS AN ASSEMBLY.
 - *2. REMOVE KNOBS & NUTS SECURING TOP COVER TO TRANSMIT/RECEIVE BOARD POTS, REMOVE EXISTING TOP COVER. BEFORE ASSEMBLING NEW TOP COVER PL19C850943G1.G3 MAKE THE FOLLOWING CONNECTIONS.

FROM	TO	WIRE
S1-1	HL3	BK
S1-2	HL13	Y

- 3. ON TRANSMIT/RECEIVE BOARD CUT RUNS AS SHOWN, (2 PLACES).
- 4. SOLDER DESIRED CHANNEL GUARD MODULE AND CARRIER CONTROL TIMER MODULE TO OPTION BOARD AS SHOWN. SEE SHEETS 4 OR 5 FOR CODING INSTRUCTIONS.
- SOLDER JUMPER FROM HL9 ON OPTION BOARD TO S1/R38-3 OR S1/R40-3 ON SOLDER SIDE OF TRANSMIT/RECEIVE BOARD. REMOVE DM JUMPER FROM HL1 TO HL2 ON OPTION BOARD
- 6. ALIGN PINS ON OPTION BOARD WITH CORRESPONDING HOLES ON TRANSMIT/RECEIVE BOARD. SEAT FULLY AND SOLDER. MAX ASSEMBLED HEIGHT TO BE 2 BELOW TRANSMIT/RECEIVE BOARD.
- **★** 7. REASSEMBLE TRANSMIT/RECEIVE BOARD, TOP COVER, REAR COVER, OPTION COVER, AND BATTERY PACK.
 - * APPLIES ONLY IF OPTION IS INSTALLED IN AN ASSEMBLED RADIO



THESE INSTRUCTIONS COVER THE FREQ CODING FOR C. G. MODULE 190429618 USING THE STANDARD C. G. FREQ.

- INSTALL CONTACT PINS (1982)9681), PER FIG. 1, IN POSITIONS INDICATED BY "X" IN CHART I, WHICH AGREES WITH DESIRED CG FREQ. (NOTE EXAMPLE SHOWN FOR 127.3Hz).
- 2. ASSEMBLE SNAP ON COVER (190429521P1).
- 3. BREAK OFF CONTACT TABS ABOVE COVER BY BENDING TOWARD EITHER END OF MODULE. (DO NOT BEND TAB TOWARD SIDE OF MODULE).
- 4. STAMP APPROPRIATE CG FREQ ON LABEL (NP280529) AND ASSEMBLE IN RECESS ON TOP OF COVER. (EXAMPLE 127.3)

CHART I

FREQ CHART										
C.G. CONTACT PIN POSITION										
FREQ	9	8	7	6	5	4	3	2	-	0
67	X		×	X	X	X			×	X
71.9	х		×	×		X	X	X	X	X
77	х		X	Х			X		X	Х
82.5	X		×		X	X		X		X
88.5	X		X			X	X	X		X
94.8	Х		X					X		
100	X			X	Х		X	X	X	X
103.5	Х			×	×					X
107.2	х			X		X			X	
110.9	X			X					Х	Х
114.8	X				X	X		×		
118.8	X				X			×		
123	X					×			X	X
127.3	X								X	
131.8		X	X	X	X	×				
136.5		X	X	X		X	X	X		X
141.3		X	X	X			X		X	
146.2		X	X		X	X		X	X	
151.4		X	X		X					X
156.7		X	X				X	X		
162.2	Г	X	Г	X	X	X		X	X	
167.9	П	X		X		X	X	X	X	X
173.8	Г	X	Г	x	Γ		X			
179.9	Г	×	Г	Г	X		X	X	X	X
186.2	Г	×	Г			X		х	X	
192.8	Г	Г	X	x	X	x	X	x	Г	П
203.5	Г	Т	X	x	П	x	П	Г	Г	X
210.7	۲	1	x	\vdash	x	x	T	x	1	П

THESE INSTRUCTIONS COVER THE FREQ CODING FOR CG MODULE 190429618 USING THE NON STANDARD CG FREQ.

- 1. USE CHART 2 TO CALCULATE THE CG FREQ DESTRED.
- 2. FIND THE FREQ DESIRED BY ADDING UP THE FREQ IN CHART 2.
 ABOVE EACH FREQ IS A CONTACT PIN POSITION NUMBER, IF THIS
 POSITION IS OPEN (THAT IS A PIN IS NOT INSTALLED), THE CG
 WILL PRODUCE THAT FREQ. IF MORE THAN ONE IS LEFT OPEN, THE
 OUTPUT FREQ WILL BE THE SUM OF THE OPEN POSITIONS.
- EXAMPLE: CG FREQ 128Hz THEREFORE CONTACT PIN POSITION #9

 WILL BE OPEN AND CONTACT PINS WILL BE INSTALLED
 IN POSITION 0, 1, 2, 3, 4, 5, 6, 7 AND 8.

 EXAMPLE: CG FREQ 132.75Hz. THEREFORE CONTACT PIN POSITION
 19 WHICH IS 128, %4 WHICH IS 4, %1 WHICH IS .5,
 AND %0 WHICH IS .25 WILL BE OPEN. ADD THE FREQ
 128 + 4 + .5 + .25 = 132.75. CONTACT PINS WILL BE
 INSTALLED IN POSITION #2, 3, 5, 6, 7 AND 8.
- 3. INSTALL CONTACT PINS, ASSEMBLE COVER AND STAMP LABEL PER INSTRUCTIONS FOR FREQ CODING THE STANDARD CG FREQ.

CHART 2

9	8	7	6	5	4	3	2	1	0	CONTACT PIN POSITION
128	64	32	16	8	4	2	-	.5	.25	FREQ IN Hz

(19D900782, Sh. 4, Rev. 0)

CG PROGRAMMING INSTRUCTIONS

10 Issue 2

Chart 2 - Primary and Equivalent Octal Codes

OCTAL CODES									
PRIMARY CODE	EQUIVALENT CODE	PRIMARY CODE	EQUIVALENT CODE	PRIMARY CODE	EQUIVALENT CODE				
023	340, 766	205	135, 610	464	237, 642, 77				
025		223	350, 475, 750	465	056, 656				
026	566	226	104, 557	466	144, 666				
031	374, 643	243	267, 342	503	157, 312				
032		244	176, 417	506	224, 313, 57				
043	355	245	370, 554	516	067, 720				
047	375, 707	251	236, 704, 742	532	161, 345				
051	520, 771	261	227, 567	546	317, 614, 75				
054	405, 675	263	213, 136	565	307, 362				
065	301	265	171, 426	606	153, 630				
071	603, 717, 746	271	427, 510, 762	612	254, 314, 70				
072	470, 701	306	147, 303, 761	624	075, 501				
073	640	311	330, 456, 561	627	037, 560				
074	360, 721	315	321, 673	631	231, 504, 63				
114	327, 615	331	372, 507	630	745				
115	534, 674	343	324, 570	632	123, 657				
116	060, 737	346	616, 635, 724	654	163, 460, 607				
125	172	351	353, 435	662	363, 436, 443 444				
131	572, 702	364	130, 641	664	344, 471, 715				
132	605, 634, 714	365	107	703	150, 256				
134	273	371	217, 453, 530	712	136, 502				
143	333	411	117, 756	723	235, 611, 671				
152	366, 415	412	127, 441, 711	731	447, 473, 474				
155	233, 660	413	133, 620	720	744				
156	517, 741	423	234, 563, 621	732	164, 207				
162	416, 553	431	713	734	066				
165	354	431	262, 316, 730 276, 326	743	312, 515, 663				
172	057	432	,	754	076, 203				
174	142, 270	445	222, 457, 575						

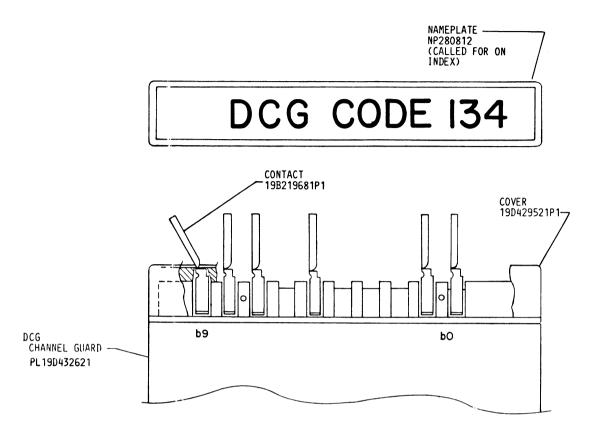


FIG. 2

DC6	CODE CHART CONTACT PIN POSITION									
CODE	* B 9	в8	в7	в6	в5	B4	в3	в2	в1	ВØ
023	X		Χ	Х	Х		Х	X		
025	X	X	Χ	X	X		X		X	
026	X	X	Χ	X	X		X			X
031	X	X	X	X	X			X	Х	
032	X	X	X	X	Ŷ			X		X
043	− ŷ 	Ŷ	X	Ŷ	^	Χ	Х	Ŷ		^_
047	- ŷ-	Ŷ	Ŷ	X		Ŷ	X	_^-		
Ŏ51		Ŷ	Ŷ				-^-	\mathbf{x}	Y	
054	_x_	X	Ŷ	X		X		-^-	X	
065		Ŷ	Ŷ	- x +			Х		- ŷ-l	
067	$\frac{\lambda}{\lambda}$	Ŷ	Ŷ	- x - l			Ŷ			
071		-	Ŷ	Ŷ				- 		
072	X	Ŷ	Ŷ	\hat{x}				-X	Х	
073		≎		- ≎ - 				X		X
	_ X _	Ÿ	Ϋ́	Ϋ́				X	-,-	,
074	_ऱ्⊐	- Ÿ	X	X	 -	— ,, 			X	X
114	X	X	X		X	X			X	Χ
115	X	X	X		Χ	Х			Χ	
116	λ.]	X	Χ	Χ			Х	X	Χ	Χ
125	<u> X</u>]	X	Χ		X		X	1	Х	
131	_ \	X	Χ		Χ			X	X	
132	_ X	_*_	X		X			X		Χ
134	λŢ	X	X		Х				X	X
143	x	_ X	X			Χ	Х	Х		
152	xi	X	X			Х		X		X
155	X	X	X			X			\mathbf{x}	
156 162 165	X	X	X			Ŷ				X
162	x	-\$-	Ÿ			-^-	Y	X		X
165	×		XXXXX				X	-^-	X	
172	╼┰╌┪	-X -X	· Ç	X.1		-v-l			-^-	
174	X	-◊- 1	Ŷ	-		X	X	- X-		X
	$\hat{\mathbf{x}}$	-2-1	- \$ -		$\overline{\mathbf{v}}$	-		-^-	Х.	
205 223	ŷ	- ŷ - 	-	X	X		. x.	X	-^-	
226	_£	X ·	X		ŶΙ	- v - l	· ŵ	-^-	- x - 	X
243	₹1	- ≎ - I		X-1		· X	· $\hat{\chi}$	 +		
244	\ \	Ŷ		^-		-^-		Х		-0-
245		Ÿ	Χ							X
	マコ	X		X	-,, 	Х	Х		X	
251	X I	X		X.	X					X
261	_X _Ţ	X		Х	Χ		X			
263	\supseteq	X		Х	Χ	Χ		X		
265	X	X	X					Х	X	
271	\mathbf{x}	X		X				Х	Х	
306	X	X	Χ			Х	Х			
311	$\overline{\mathbf{x}}$	X			X	X		X	Χ	
315	- x 1				X	X			X	

* B9 IS LEFT OPEN IN 800 MHz RADIOS

DC6	CODE CHART(CONT.) CONTACT PIN POSITION									
CODE	в9	В8	В7	В6	в5	B4	в3	в2	В1	В
331	X	X			X			X		_
343	X	X			X		X		X	Х
346	X	X				X	X			X
351	X	X				X		X	X	
364	X	X	X		X			X	X	X
365	X	X	X		X	X	X		<u> </u>	
371	X	Х	1	X	X	X				
411	X	X	Х		X	X				
412	X	X	X		X X		X			_
413	Х	X	X		X			X		
423	X	X	<u> </u>	Х	X			 	X	X
431	Χ	X		X			X	X		X
432	Χ	X		Х						X
445	X	X		X	X	† — ·	X	Х		X
464		X		X	X					 ``
465	X	X	X	X		X				X
466	Х	X	X		1	X	X		X	X
503	X	X X X	X			X			'`	^
506	X	X		Χ	X		X		X	X
516	Х	Х	Χ	X			X		``	-^`
532	X	Х	X				Ϋ́	X	X	_
546	X	X			Х	Х			^	_
565	Χ	X			X	X	X			_
606	Χ	X	X			X		X		_
612	X	X X X		X		Ϋ́			Y	X
624	X	X	X	X					X	_^
627	X	X	X		X				^	_
631	X	X		X	X			X	X	_
632	Χ	X X X	X		Ŷ		X	X	 	
654	X	X	X				X	X	-	
662	X	X			-		X			
664	X	Ϋ́				Χ.	X		X	Χ
703	Х	X	Χ			χ̈́		X	X	X
712	X	X X X	X		. X					X
723	Χ	X		X	X				X	
731	Χ		X	X		X	\mathbf{x}			
732	X	X	X				X		X	X
734	X	X	X	Χ	•		X			X
743	X	X			X	Х		X		X
754	X	X	X	X	• • •					X

THESE INSTRUCTIONS COVER THE FREQ CODING FOR DCG MODULE 19D432621 USING THE STANDARD DCG CODES.

- INSTALL CONTACT PINS (198219681), PER FIG. 2, IN POSITIONS INDICATED BY "X" IN CHART 1, WHICH AGREES WITH DESIRED DCG CODE. (NOTE - EXAMPLE SHOWN FOR 134).
- 2. ASSEMBLE SNAP ON COVER (19D429521P1).
- 3. BREAK OFF CONTACT TABS ABOVE COVER BY BENDING TOWARD EITHER END OF MODULE. (DO NOT BEND TAB TOWARD SIDE OF MODULE).
- 4. STAMP APPROPRIATE DCG CODE ON LABEL (NP280812) AND ASSEMBLE IN RECESS ON TOP OF COVER. (EXAMPLE 134).

RC 4235

DCG PROGRAMMING INSTRUCTIONS

Issue 3