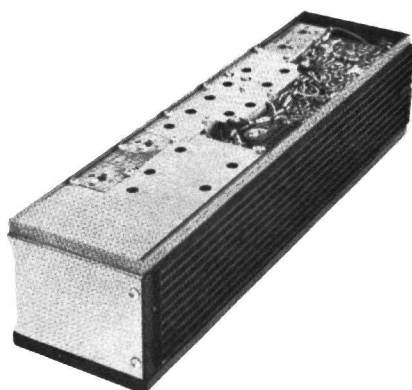


MASTR[®] Progress Line

25-50 MHz RECEIVER MODELS 4ER39A10-18



SPECIFICATIONS *

FCC Filing Designation

ER-39-A

Frequency Range

25-50 MHz

Audio Output

2 watts at less than 10% distortion

Sensitivity

12-db SINAD (EIA Method)
20-db Quieting Method

0.25 μ v
0.35 μ v

Selectivity

EIA Two-Signal Method
20-db Quieting Method

-85 db (adjacent channel, 20 kHz channels)
-100 db at ± 15 kHz

Spurious Response

-100 db

First Oscillator Stability

$\pm 0.0005\%$ (-30°C to $+60^{\circ}\text{C}$)

Modulation Acceptance

± 7 kHz (narrow-band)

Squelch Sensitivity

Critical Squelch
Maximum Squelch

0.15 μ v
Greater than 20 db quieting (less than 2 μ v)

Intermodulation (EIA)

-60 db

Maximum Frequency Separation

0.4%

Frequency Response

+1 and -8 db of a standard 6-db per octave
de-emphasis curve from 300 to 3000 Hz
(1000-Hz reference)

*These specifications are intended primarily for the use of the serviceman. Refer to the appropriate Specification Sheet for the complete specifications.

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WARNING

No one should be permitted to handle any portion of the equipment that is supplied with high voltage; or to connect any external apparatus to the units while the units are supplied with power. KEEP AWAY FROM LIVE CIRCUITS.

DESCRIPTION

General Electric MASTR[®] Progress Line Receiver Type ER-39-A is a double conversion, superheterodyne FM receiver designed for operation on the 25-50 megahertz band.

The receiver is of single-unit construction and is completely housed in an aluminum casting for maximum shielding and rigidity. The top compartment of the casting contains the RF, oscillator, converter, high IF and 1st low IF amplifier stages. The bottom portion of the casting contains the audio squelch board and the optional Channel Guard board.

CIRCUIT ANALYSIS

The MASTR Progress Line Receiver is completely transistorized, using a total of 18 silicon transistors. Input leads to the receiver are individually filtered by the 20-pin feed-through by-pass connector J443.

A regulated 10 volts is used for all receiver stages except the audio PA stage which operates from the 12-volt system supply.

Centralized metering jack J442 is provided for use with General Electric Test Set, Model 4EX3A10, for ease of alignment and servicing. The Test Set meters the oscillator, multiplier, and limiter stages as well as the discriminator, audio PA, voice coil and regulated 10 volts.

RF AMPLIFIER (A341)

RF Amplifier (A341) consists of two high-Q helical resonators and an RF amplifier stage (Q1). The RF signal from the antenna is coupled by RF cable W441 to a tap on L341/L343/L345. The tap is positioned to insure the proper impedance match to the antenna. RF energy is coupled through the two coils by an opening in the shield wall to the base of RF Amplifier Q1. The coils are tuned to the incoming frequency by air trimmer capacitors C341 and C342. The output of Q1 is coupled through three tuned circuits to the base of the first mixer.

1ST OSCILLATOR AND MULTIPLIER (A352-A357)

The receiver 1st oscillator operates in a transistorized Colpitts oscillator circuit. The oscillator crystal operates in a fundamental mode at a frequency of approximately 10 to 15 megahertz. The crystal is cut to provide temperature compensation at the high end of the temperature range and is thermistor compensated at low temperatures. This provides $\pm 0.0005\%$ frequency stability as soon as the receiver is energized—without the warm-up time required by crystal ovens or warmers.

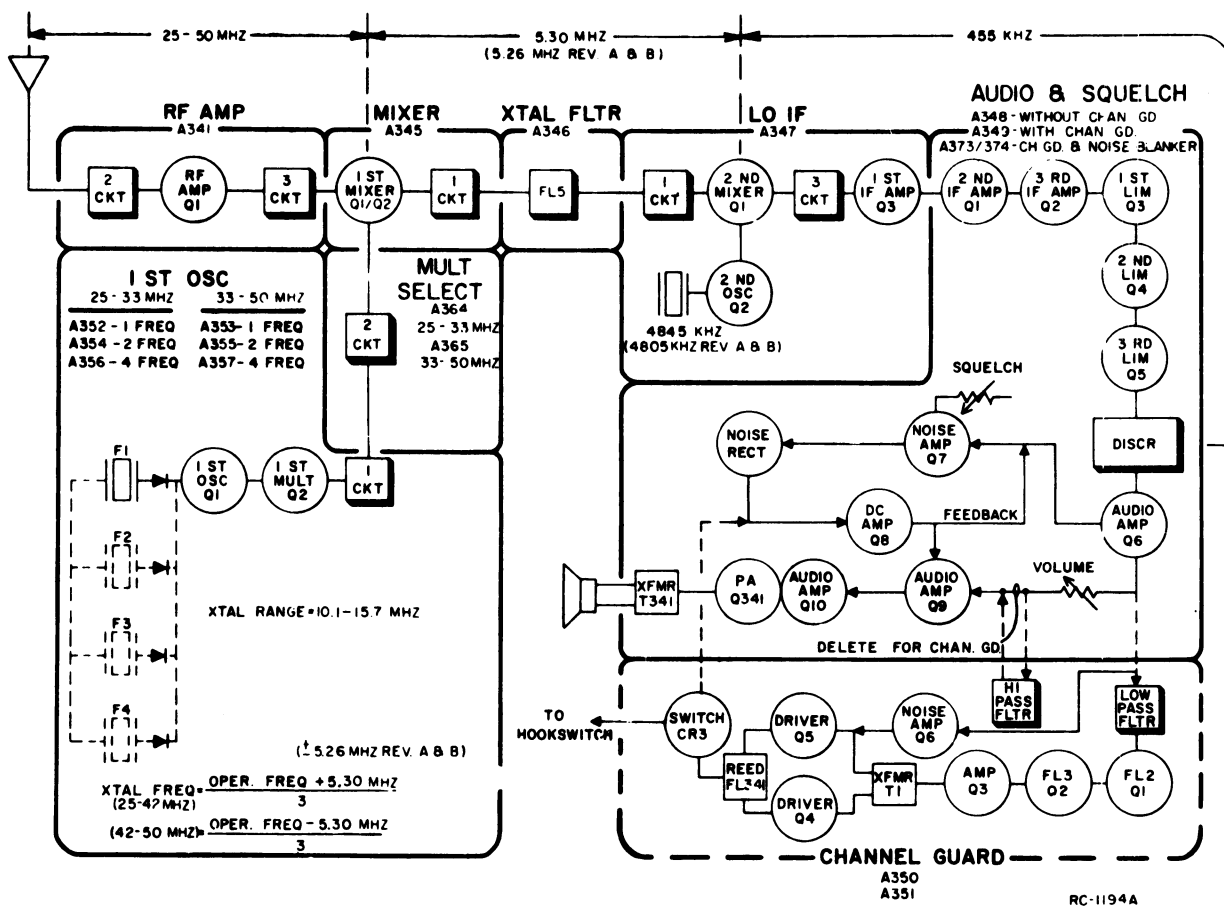


Figure 1 - Receiver Block Diagram

In single frequency receivers, bias for the oscillator transistor is obtained by a jumper from C363 to P304.

In multi-frequency receivers, a diode is connected in series with the crystal, and up to three additional crystal circuits can be added. The 10-volt jumper is removed and the proper frequency is selected by switching the desired crystal circuit to +10 volts by means of a frequency selector switch on the control unit.

Switching the +10 volts to the crystal circuit forward biases the diode and reduces its impedance. This applies the crystal frequency to the base of oscillator transistor Q1. Feedback for the oscillator is developed across C21/C22. The output is coupled to the base of 1st multiplier Q2.

The output of the 1st multiplier is transformer-coupled (T3/T4) to multiplier selectivity assembly A364/A365. The 1st multiplier tank is tuned to three times the crystal frequency.

The stage is metered at centralized metering jack J442-4 through metering network CR1, R1, C7 and C8.

MULTIPLIER SELECTIVITY ASSEMBLY (A364/A365)

Following the 1st multiplier tank (T3/T4) are three additional tuned circuits (A364/A365-L1 -L2 and -L3). Capacitor C12/C16 couples the multiplier selectivity output to the base of the first mixer.

1ST MIXER (A345) AND CRYSTAL FILTER (A346)

The RF signal from the RF amplifier and the injection voltage from the 1st multiplier are applied to the base of 1st mixer A345-Q1. The mixer collector tank (L2 and C3) is tuned to 5.3 megahertz (5.26MHz in Rev. A and B receivers) and provides impedance matching to the high IF filter.

The highly selective crystal filter (FL5) following the 1st mixer provides the major selectivity for the receiver. The output of the filter is fed through impedance matching transformer A347-T1 to the base of the 2nd mixer.

2ND OSCILLATOR, 2ND MIXER AND 1ST IF AMPLIFIER (A347)

The 2nd oscillator A347-Q2 operates in a Colpitts oscillator circuit, with feedback supplied through C2. Crystal Y1 maintains the oscillator frequency at 4845kHz (4805kHz in Rev. A and B receivers). The low-side injection voltage is fed to the base of the 2nd mixer.

The Hi-IF signal from the filter is fed to the base of 2nd mixer Q1 with the 2nd oscillator output. The 455kHz 2nd mixer output is fed to three tuned low IF circuits (L1, L2, L3). L1, L2 and L3 are required for shaping the nose of the IF waveform and provide some additional selectivity.

Capacitor C14 couples the low IF signal to the base of the 1st low IF amplifier, A347-Q3. The output of Q3 is RC coupled to the base of the 2nd IF amplifier.

2ND IF AMPLIFIERS AND LIMITERS (A348)

Following A347-Q3 are two additional RC coupled low IF amplifiers (A348-Q1 and -Q2). The 2nd IF amplifier stage is metered at J442-2 through metering network C8, CR1 and R12.

After the IF amplifiers are three RC coupled limiter stages (A348-Q3, -Q4 and -Q5). The 1st limiter is metered at J442-3 through metering network C13, CR2, R18 and C15.

DISCRIMINATOR (A348)

The receiver utilizes a Foster-Seely type discriminator. The output of the 3rd limiter is connected to a tap on the primary tuned circuit of discriminator T1. This allows the discriminator to operate at a higher level. Diodes CR5 and CR6 rectify the 455kHz IF signals to recover the audio. The stage is metered at J442-10 through metering network R27 and C22.

1ST AUDIO AMPLIFIER (A348)

The output of the discriminator is fed to the 1st audio amplifier (Q6). This stage operates as an emitter follower to match the impedance of the discriminator to the noise amplifier stage and VOLUME control. Q6 also provides some power gain.

AUDIO AMPLIFIERS

When audio is present in the incoming signal, it is taken off the emitter of Q6 and connected to the VOLUME control through A348-J9. The VOLUME control arm connects to A348-J8 which feeds the audio signal to the base of the 2nd audio amplifier, Q9. C34, C36, C53, C37 and L4 make up the de-emphasis network. The collector current of Q9 should be adjusted to 650 milliamps to potentiometer R47 as indicated by a reading of 0.65 volts at metering jack J442-1. This adjustment should be made with the VOLUME control fully counterclockwise and the squelch control fully clockwise (unsquelched). Thermistor RT1 keeps the output current constant over wide variations in temperature after R47 has been set.

Following Q9 is a Darlington circuit, which consists of compound-connected transistors Q10 and Q341. The Darlington circuit provides a higher input impedance than is normally encountered in transistor amplifiers. Also, this circuit has a more linear operation, with less distortion at maximum power output.

The output of the amplifier stage is coupled by audio transformer T341 to the loudspeaker. Audio high and low are present at the centralized metering jack (J442). When the General Electric Test Set is connected to J442, these leads are connected to the black and green jacks for sensitivity, frequency response, distortion, power output and other measurements.

SQUELCH

Noise from audio amplifier Q6 is used to operate the squelch circuit. When no carrier is present in the receiver, noise is coupled to the base of noise amplifier Q7. The gain of the noise amplifier is determined by the SQUELCH control, which varies the bias on the base of Q7.

The noise amplifier output is fed through a high-pass filter which attenuates frequencies below 3 kHz. Thermistor RT2 keeps the critical squelch constant over wide variations in temperature.

Noise from the high-pass filter is rectified by CR3 and CR4, and the negative DC output of the noise rectifiers is fed to the base of DC amplifier Q8.

DC amplifier Q8 acts as a squelch switch. A negative output from the noise rectifiers cuts off the DC amplifier. When cut off, the collector is at the +10 volt supply potential. This positive voltage is fed to the base of Q9, a PNP transistor, cutting it off. Since audio stages Q9, Q10 and Q341 are DC coupled, Q10 and Q341 are cut off also. The positive voltage from the collector circuit of the DC amplifier is used as feedback through R33 to the base of noise amplifier Q7, causing it to conduct more heavily. This feedback helps to sharply cut off Q8, providing sharp, rapid switching action.

When the receiver is quieted by a signal, noise voltage from the noise rectifiers is reduced and the DC amplifier conducts. While conducting, the collector potential of Q8 is negative and negative feedback to the base of noise amplifier Q7 causes it to conduct less.

This negative voltage is applied to the base of PNP transistor Q9 and causes it to conduct. Now, all the audio stages are turned on and sound is heard at the loudspeaker.

With the receiver squelched, the final audio amplifiers are cut off; and the receiver drain is less than 50 milliamps in 12-volt systems.

It should be noted that a hysteresis effect exists in the squelch circuit and, as a result, the squelch does not operate in the same manner as other conventional squelch circuits. The circuit is designed so that a weak signal will open the squelch. The signal may be reduced by 3 to 5 db without the squelch closing. This limits squelch "flutter" or "picket-fence" operation.

MAINTENANCE

DISASSEMBLY

To service the receiver from the top —

1. Pull locking handle down and pull radio about one inch out of mounting frame.
2. Pry up cover at rear of receiver.
3. Slide cover back and lift off.

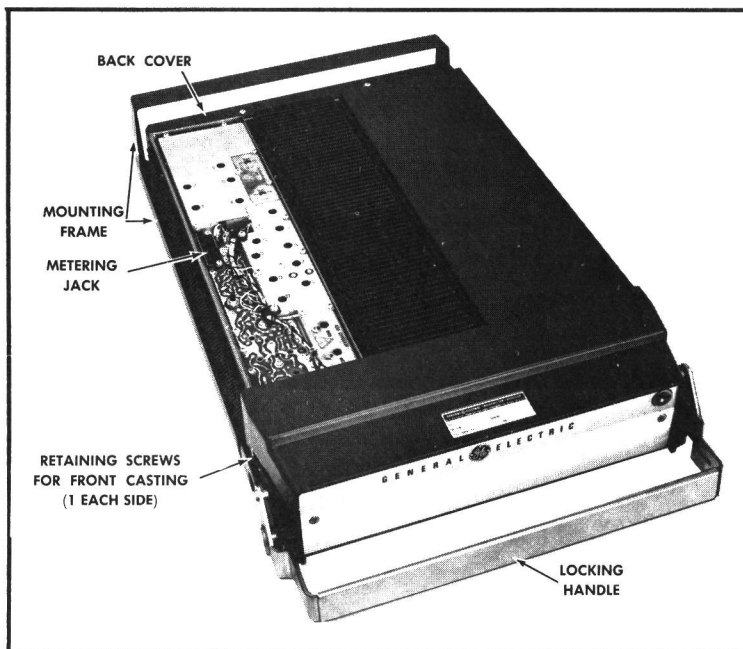


Figure 2 - Removing Top Cover

To service the receiver from the bottom—

1. Pull locking handle down. Pull radio out of mounting frame.
2. Remove screws in bottom cover. Pry up cover at back of receiver.
3. Slide cover back and lift off.

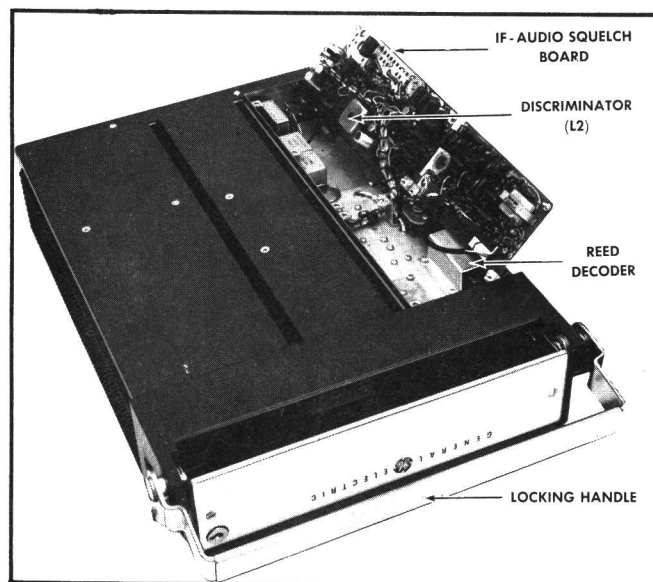


Figure 3 - Removing Bottom Cover

To remove the receiver from the system frame—

1. Loosen the two Phillips-head retaining screws in front casting (see Figure 2), and pull casting away from system frame.
2. Remove the four screws in the back cover.
3. Remove the two screws holding the receiver at each end of the system frame.
4. Disconnect the antenna jack and the 20-pin connector from the front of the receiver, and slide the unit out of the system frame.

FRONT END ALIGNMENT

EQUIPMENT REQUIRED

- GE Test Set Model 4EX3A10 (or 20,000 ohms-per-volt Multimeter with a 1-volt scale).
- A 455 kHz and 25-50 MHz signal source. Connect a one-inch piece of insulated wire no larger than .065-inch diameter to generator output probe.

PRELIMINARY CHECKS AND ADJUSTMENTS

- Connect Test Set Model 4EX3A10 to Receiver Centralized Metering Jack J442 and set meter sensitivity switch to the TEST 1 position.
- With VOLUME control fully counterclockwise and squelch control fully clockwise (receiver unsquelched) and Test Set in Position G, adjust R47 on IF-AUDIO & SQUELCH board for a reading of 0.55 volts. If using Multimeter, connect leads to J442-1 (AUDIO-PA) and J442-8 (System Negative).

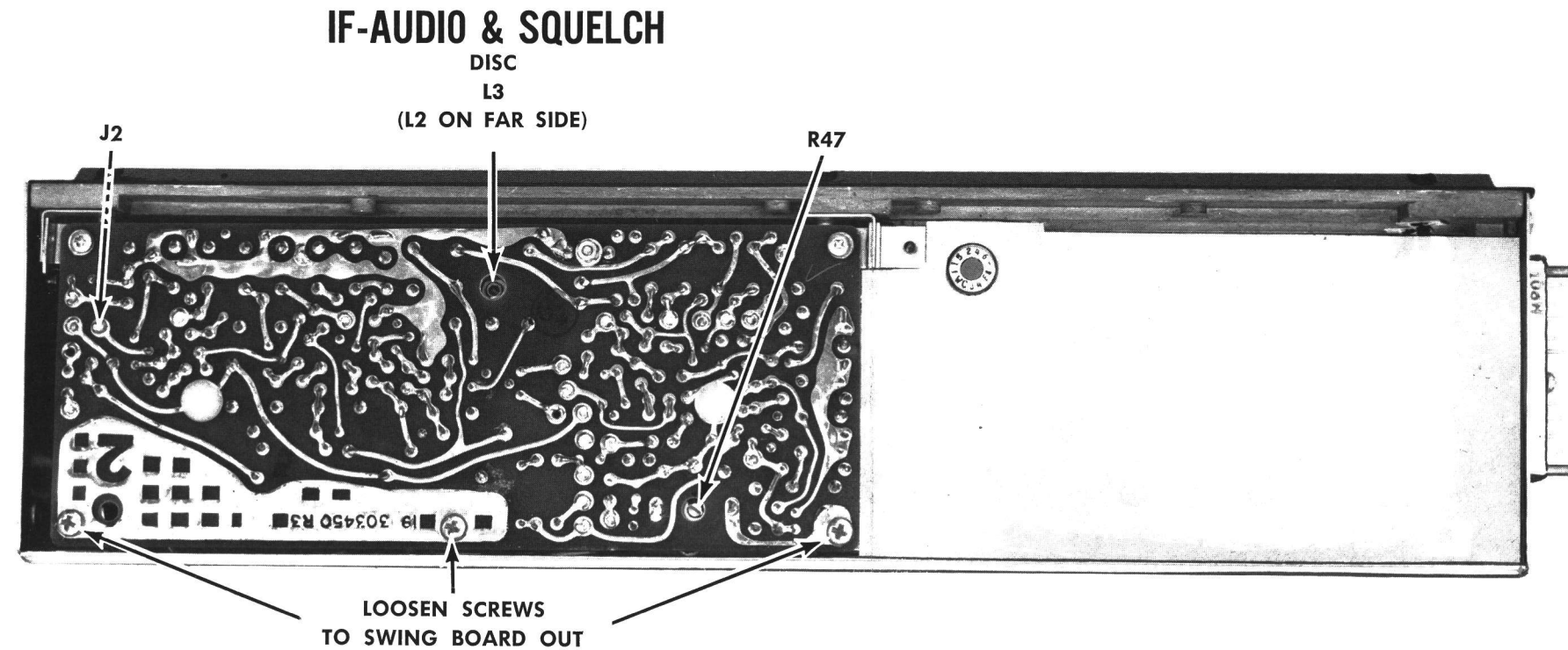
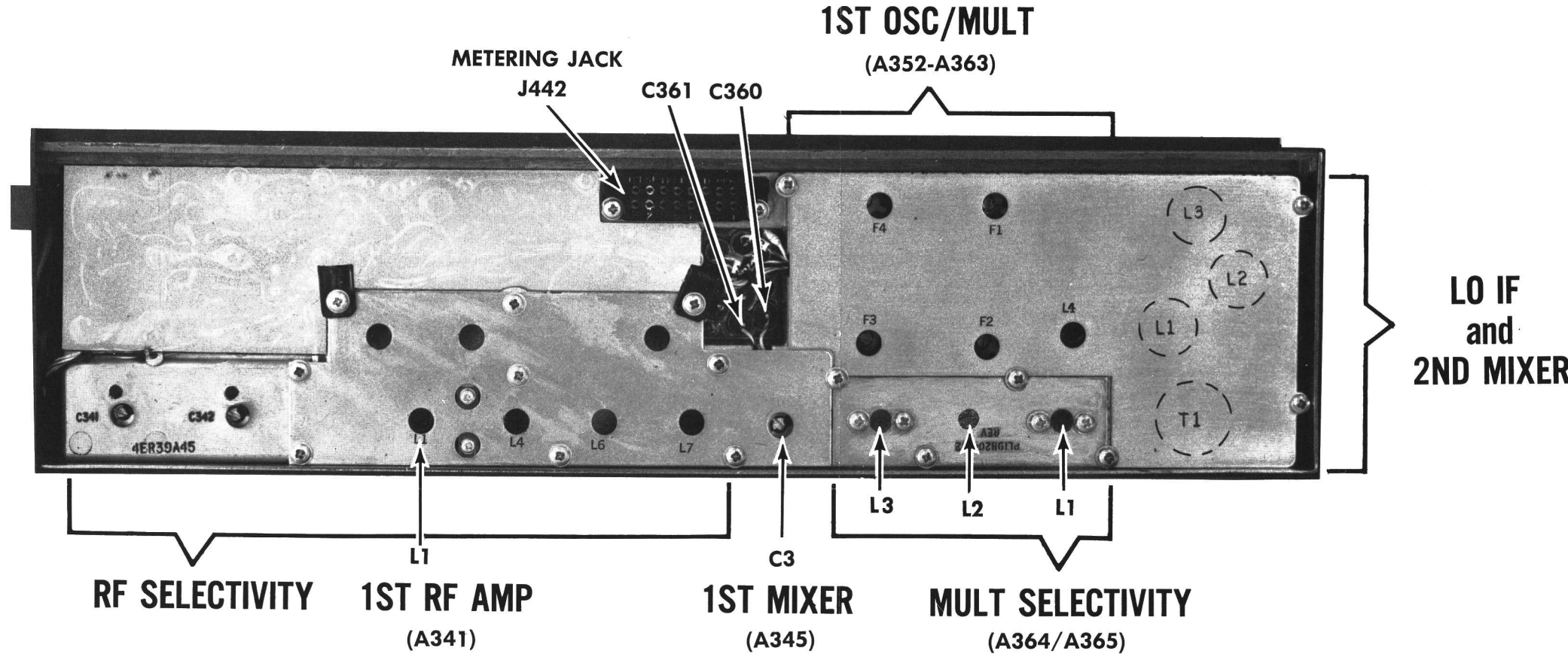
NOTE

The adjustment of R47 should be made within 20 seconds after power is applied to the receiver. This will result in a reading of approximately 0.65 volts after the unit is fully warmed up.

- With Test Set in Position J, check for regulated +10 volts. If using Multimeter, measure from C360 to C361.
- If using Multimeter, connect the positive lead to J442-16 (Ground).
- Disable Channel Guard (Models 4ER39A19-27 & 46-54 only).

ALIGNMENT PROCEDURE

STEP	METERING POSITION		TUNING CONTROL	METER READING	PROCEDURE
	4EX3A10	Multimeter - at J442			
OSCILLATOR/MULTIPLIER					
1.	D (MULT-1)	Pin 4	L4 (on 1st OSC/ MULT) and L1, L2 (on MULT SELEC- TIVITY)	See Proce- dure	Tune L4 on 1st OSC/MULT and L1 on MULT SELEC- TIVITY for maximum meter reading. Then tune L2 for minimum meter reading. Change voltage scale if necessary.
RF AMPLIFIER & SELECTIVITY					
2.	A (DISC)	Pin 10		Zero	Apply an on-frequency signal adjacent to L7. Adjust the signal generator for discriminator zero.
3.	B (2nd IF Amp)	Pin 2	L1 (1st RF Amp), L6, L7, C341 and C342 (RF SELEC- TIVITY)	Maximum	Apply an on-frequency signal to the antenna jack, keeping below saturation. Tune L1, L5, L7, C341, and C342 for maximum meter reading.
4.	"	"	L4 (1st OSC/MULT) and L1 and L2 (MULT SELECTIVITY)	Maximum	Apply an on-frequency signal as above, keeping below saturation. Tune L4 on 1st OSC/MULT and L1 and L2 on MULT SELECTIVITY for maximum meter reading.
FREQUENCY ADJUSTMENT					
5.	A (DISC)	Pin 10	C9 on 1st OSC (C10, C11 or C12 for multi- frequency)	Zero	Apply an on-frequency signal to the antenna jack. Tune C9 for zero discriminator reading. In multi-frequency units, tune C10, C11 or C12 as required. ———— NOTE ———— For proper frequency control of the re- ceiver, it is recommended that all fre- quency adjustments be made when the equipment is at a temperature of ap- proximately 75°F. In no case should frequency adjustments be made when the equipment is outside the tem- perature range of 50° to 90°F.



COMPLETE RECEIVER ALIGNMENT

EQUIPMENT REQUIRED

- GE Test Set Model 4EX3A10 (or 20,000 ohms-per-volt Multimeter with a 1-volt scale).
- A 455 kHz and 25-50 MHz signal source. Connect a one-inch piece of insulated wire no larger than .065-inch diameter to generator output probe.
- Two 33,000-ohm resistors for tuning low IF coils.*

PRELIMINARY CHECKS AND ADJUSTMENTS

- Connect Test Set Model 4EX3A10 to Receiver Centralized Metering Jack J442 and set meter sensitivity switch to the TEST 1 position.
- Set crystal trimmer C9 to mid-capacity. In multi-frequency receivers, set C10, C11 or C12 to mid-capacity as required. Where the maximum frequency spacing is less than 200 kHz, align the unit on channel F1. If the frequency spacing is greater than 200 kHz, align the receiver on the center frequency.
- With VOLUME control fully counterclockwise and squelch control fully clockwise (receiver unsquelched) and Test Set in Position G, adjust R47 on IF-AUDIO & SQUELCH board for a reading of 0.55 volts. If using Multimeter, connect leads to J442-1 (AUDIO-PA) and J442-8 (System Negative).

NOTE

The adjustment of R47 should be made within 20 seconds after power is applied to the receiver. This results in a reading of approximately 0.65 volts after the unit is fully warmed up.

- With Test Set in Position J, check for regulated +10 volts. If using Multimeter, measure from C360 to C361.
- If using Multimeter, connect the positive lead to J442-16 (Ground).
- Disable the Channel Guard.

ALIGNMENT PROCEDURE

STEP	METERING POSITION		TUNING CONTROL	METER READING	PROCEDURE	
	4EX3A10	Multimeter - at J442				
DISCRIMINATOR						
1.	A (DISC)	Pin 10	L3 (Bottom slug on IF-AUDIO & SQUELCH board)	Zero	Apply a 455 kHz signal to J2 on IF-AUDIO & SQUELCH board and adjust L3 (disc secondary) for zero meter reading.	
2.	A (DISC)	Pin 10	L2 (top) and L3 (bottom slug on IF-AUDIO & SQUELCH board)	1.7 volts (2.1 v. maximum)	Switch Test Set to TEST 3 position. Then alternately apply a 445-kHz and 465-kHz signal while adjusting L2 and L3 for readings of at least 1.7 volts, but not more than 2.1 volts. Both readings must be within 0.1 volt.	
3.	D (MULT-1)	Pin 4	L4 (on 1st OSC/MULT) and L1, L2 & L3 (on MULT SELECTIVITY)	See Procedure	Tune L4 on 1st OSC/MULT and L1 on MULT SELECTIVITY for maximum meter reading. Tune L2 for minimum meter reading. Change voltage scale if necessary. Then tune L3 for maximum meter reading. Repeat step 3.	
RF AMPLIFIER & SELECTIVITY						
4.	A (DISC)	Pin 10		Zero	Apply an on-frequency signal adjacent to L7. Adjust the signal generator for discriminator zero.	
5.	B (2nd IF AMP)	Pin 2	L7 and L6 (RF SELECTIVITY)	Maximum	Apply an on-frequency signal and tune for maximum meter reading as shown below, keeping signal below saturation. Apply Signal Generator Probe to: L6 L1	Tune: L7 L6
6.	"	"	C341, C342 and L1 (1st RF Amp)	Maximum	Apply an on-frequency signal to the antenna jack. Tune C341, C342 and L1 for maximum meter reading, keeping signal below saturation.	
7.	"	"	L1 (1st RF Amp) L6, L7, C341, and C342 (RF SELECTIVITY)	Maximum	Apply an on-frequency signal as above, keeping below saturation. Tune L1, L6, L7, C341 and C342 for maximum meter reading.	
8.	"	"	L3 (MULT SELECTIVITY)	Maximum	Apply an on-frequency signal as above, keeping below saturation. Tune L2 & L3 (on MULT SELECTIVITY) for maximum meter reading.	
LO IF & 2ND MIXER						
9.	"	"	C3 (1st MIXER)		C3 does not peak, but provides impedance matching for the crystal filter input and should only be tuned when observing IF trace on oscilloscope.	
10.	B (2nd IF AMP)	Pin 2	T1 (2nd MIXER)	Maximum	Apply an on-frequency signal as in Step 8, and tune T1 for maximum meter reading, keeping signal below saturation.	
11.	"	"	L1, L2 and L3 (LO IF)	Maximum	With one end of the 33,000-ohm resistors to ground load and peak as follows: Load L2 at point B—Peak L1 and L3. Load L1 and L3 at Points A and C—Peak L2.	
FREQUENCY ADJUSTMENT						
12.	A (DISC)	Pin 10	C9 on 1st OSC (C10, C11 or C12 for multi-frequency)	Zero	Apply an on-frequency signal to the antenna jack. Tune C9 for zero discriminator reading. In multi-frequency units, tune C10, C11 or C12 as required. — NOTE — For proper frequency control of the receiver, it is recommended that all frequency adjustments be made when the equipment is at a temperature of approximately 75°F. In no case should frequency adjustments be made when the equipment is outside the temperature range of 50° to 90°F.	

* NOTE — The low IF coils have been aligned at the factory and will normally require no further adjustment. If alignment is necessary, refer to the RECEIVER OUTLINE DIAGRAM for location of resistor loading points A, B and C.

ALIGNMENT PROCEDURE

25 — 50 MHz MASTR RECEIVER
MODELS 4ER39A10-18

TEST PROCEDURES

These Test Procedures are designed to help you service a receiver that is operating---but not properly. The problems encountered could be low power, poor sensitivity, distortion, limiter not operating properly, and low gain. By following the sequence of test steps starting with Step 1, the defect can be quickly localized.

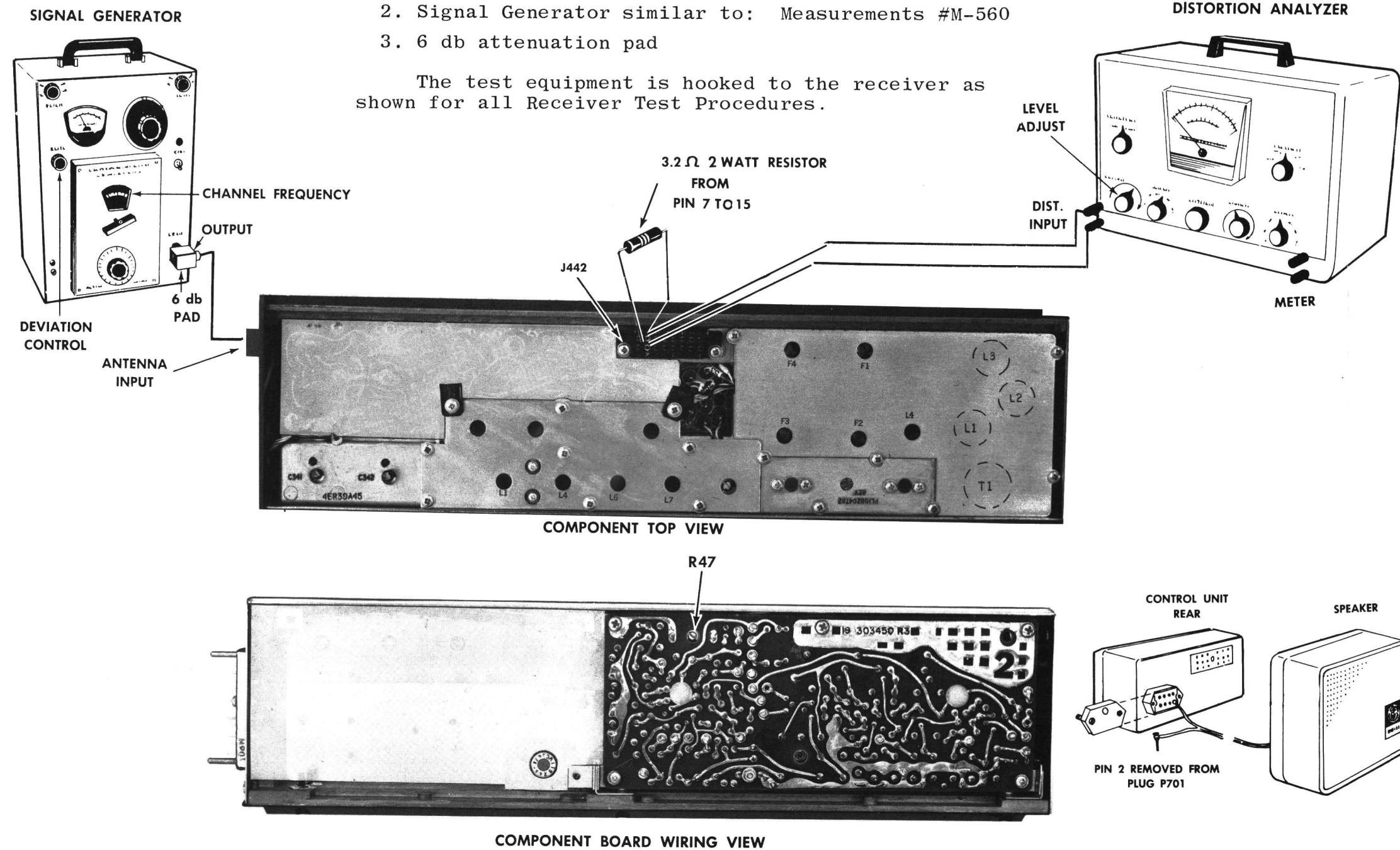
Once the defective stage is pin-pointed, refer to the "Service Check" listed to correct the problem. Additional corrective measures are included in the Troubleshooting Procedure. Before starting with the Receiver Test Procedures, be sure the receiver is tuned and aligned to the proper operating frequency.

TEST EQUIPMENT REQUIRED

for test hookup shown:

- 1. Distortion Analyzer similar to: Heath #1M-12
- 2. Signal Generator similar to: Measurements #M-560
- 3. 6 db attenuation pad

The test equipment is hooked to the receiver as shown for all Receiver Test Procedures.



STEP 1

AUDIO POWER OUTPUT AND DISTORTION

TEST PROCEDURE

Measure Audio Power Output as follows:

- 1. Connect a 1,000-microvolt test signal modulated by 1,000 hertz ± 3.3 kHz deviation to the antenna jack J441.
- 2. Two-Watt Speaker: When speaker is used, disconnect speaker lead pin from J701-2 (on rear of Control Unit). Hook up a 3.2-ohm load resistor from J442-15 to J442-7.

OR

Handset: When handset is used, lift handset off of hookswitch.

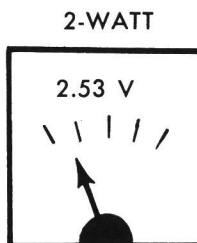
- 3. Two-Watt Speaker: Connect Distortion Analyzer input across the 3.2-ohm resistor as shown

OR

Handset: Connect Distortion Analyzer input from J442-15 to J442-7.

- 4. Two-Watt Speaker: Set volume control for two-watt output (2.53 VRMS):

VOLTMETER SCALE ON DISTORTION ANALYZER



- 5. Make distortion measurements according to manufacturer's instructions. Reading should be less than 10% (5% is typical).

SERVICE CHECK

If the distortion is more than 10%, or maximum audio output is less than two watts (for two-watt speaker) make the following checks:

- 1. Battery and regulator voltage---low voltage will cause distortion. (Refer to Receiver Schematic Diagram for voltages.)
- 2. Audio Bias Adjust (R47)---should be adjusted for 0.65 volts. (Refer to Receiver Alignment on reverse side of page.)
- 3. Audio Gain (Refer to Receiver Troubleshooting Procedure).
- 4. Discriminator Alignment (Refer to Receiver Alignment on reverse side of page).

STEP 2

USABLE SENSITIVITY (12 db SINAD)

TEST PROCEDURE

Measure sensitivity of the receiver modulated at the standard test modulation as follows:

- 1. Be sure Test Step 1 checks out properly.
- 2. Reduce the Signal Generator output from setting in Test Step 1.
- 3. Adjust Distortion Analyzer LEVEL control for a +2 db reading.
- 4. Set CONTROL from LEVEL to DISTORTION reading. Repeat Steps 1, 2 and 3 until difference in reading is 12 db (+2 db to -10 db).
- 5. The 12-db difference (Signal plus Noise and Distortion to noise plus distortion ratio) is the "usable" sensitivity level. Reading should be less than 0.35 microvolts with audio output at least one watt (1.83 volts RMS across the 3.2-ohm receiver load).

SERVICE CHECK

If the sensitivity level is more than 0.35 microvolts, make the following checks:

- 1. Alignment of RF stages (Refer to RF Alignment in Receiver Alignment on reverse side of page.)
- 2. Gain measurements as shown on the Receiver Troubleshooting Procedure.

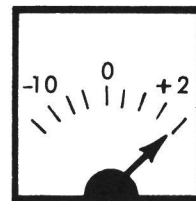
STEP 3

MODULATION ACCEPTANCE BANDWIDTH (IF BANDWIDTH)

TEST PROCEDURE

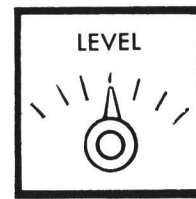
- 1. Be sure Test Steps 1 and 2 check out properly.
- 2. Set Signal Generator output for twice the microvolt reading obtained in Test Step 2-4.
- 3. Increase Signal Generator frequency deviation.
- 4. Adjust LEVEL Control for +2 db.

DB SCALE ON DISTORTION ANALYZER



- 5. Set CONTROL from LEVEL to DISTORTION reading. Repeat Steps 3, 4 and 5 until difference between readings becomes 12 db (from +2 db to -10 db).

LEVEL DISTORTION ON DISTORTION ANALYZER



- 6. Deviation control reading for the 12-db difference is the Modulation Acceptance Bandwidth of the receiver. It should be more than ± 7 kHz (but less than ± 9 kHz).

SERVICE CHECK

If the Modulation Acceptance Bandwidth test does not indicate the proper width, make gain measurements as shown on the Receiver Troubleshooting Procedure.

STEP 1 - QUICK CHECKS

SYMPTOM	PROCEDURE
NO SUPPLY VOLTAGE	Check power connections and continuity of supply leads, and check fuse in power supply. If fuse is blown, check receiver for short circuits.
NO REGULATED 10 VOLTS	Check the 12-volt supply. Then check regulator circuit (See Troubleshooting Procedure for Power Supply).
LOW 2ND LIM READING	Check supply voltages and then check oscillator reading at J442-4 as shown in STEP 2. Make SIMPLIFIED VTVM GAIN CHECKS from 2nd Mixer through 2nd Limiter stages as shown in STEP 2.
LOW OSCILLATOR READING	Check alignment of Oscillator (Refer to Front End Alignment Procedure). Check voltage and resistance reading of 1st Oscillator/Multiplier Q1/Q2. Check crystal Y1.
LOW RECEIVER SENSITIVITY	Check Front End Alignment (Refer to Receiver Alignment Procedure). Check antenna connections, cable and relay. Check voltage and resistance readings of RF Amp and 1st and 2nd Mixers. Make SIMPLIFIED GAIN CHECKS (STEP 2).
LOW AUDIO	Check Audio PA (Q341) output current at J442-1. If reading is low-- a. Refer to Receiver Alignment Procedure for BIAS ADJ. b. Check Q341. Check unquieted voltage readings in Audio section (Refer to Receiver Schematic Diagram). Check voltage and resistance readings on Channel Guard receiver.
IMPROPER SQUELCH OPERATION	Check voltage and resistance readings of Squelch circuit (Refer to Receiver Schematic Diagram).
DISCRIMINATOR IDLING TOO FAR OFF ZERO	See if discriminator zero is on 455 kHz.

STEP 3- VOLTAGE RATIO READINGS

EQUIPMENT REQUIRED:

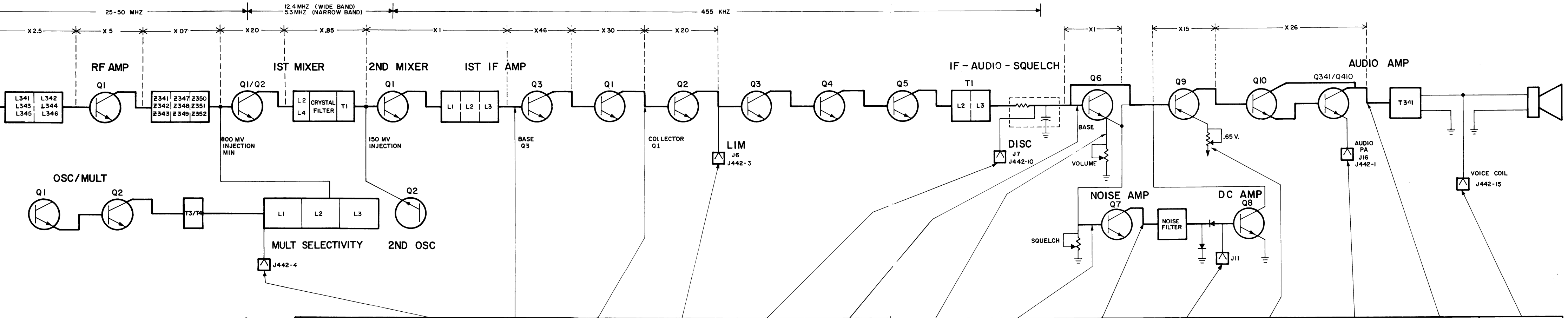
- RF VOLTMETER (SIMILAR TO BOONTON MODEL 91-CA OR MILLIVAC TYPE MV-18 C).
- SIGNAL ON RECEIVER FREQUENCY (BELOW SATURATION). CORRECT FREQUENCY CAN BE DETERMINED BY ZEROING THE DISCRIMINATOR. USE 1,000 HERTZ SIGNAL WITH 3.3 KHZ.

PROCEDURE:

- APPLY PROBE TO INPUT OF STAGE (FOR EXAMPLE, BASE OF RF AMP). PEAK RESONANT CIRCUIT OF STAGE BEING MEASURED AND TAKE VOLTAGE READING (E₁).
- MOVE PROBE TO INPUT OF FOLLOWING STAGE (1ST MIXER*). REPEAT FIRST RESONANT CIRCUIT THEN PEAK CIRCUIT BEING MEASURED AND TAKE READING (E₂).
- CONVERT READINGS BY MEANS OF THE FOLLOWING FORMULA.

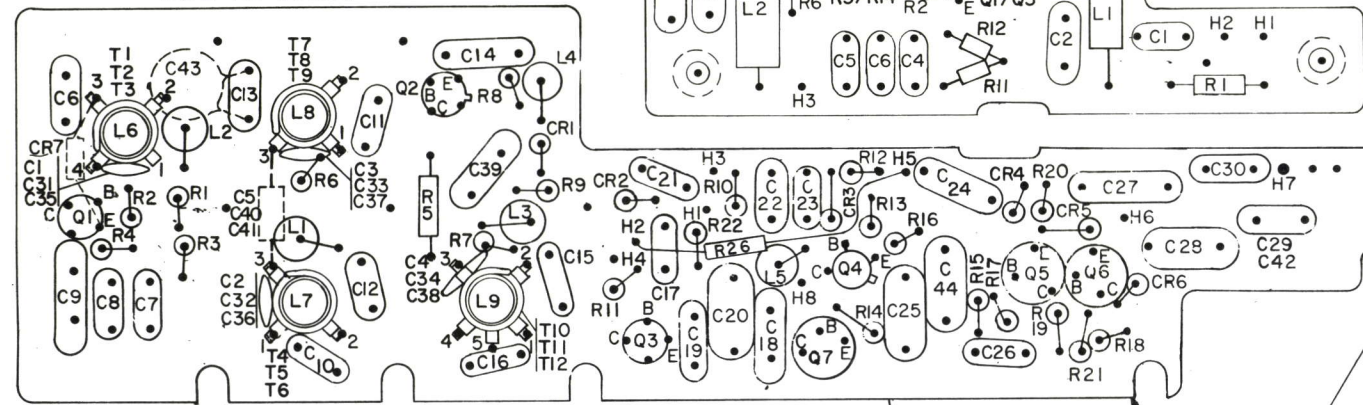
$$\text{VOLTAGE RATIO} = \frac{E_2}{E_1}$$

- CHECK RESULTS WITH TYPICAL VOLTAGE RATIOS SHOWN ON DIAGRAM.
- * NOTE: ON 1ST MIXER, REMOVE CRYSTAL BEFORE MEASURING BASE VOLTAGE. REPLACE CRYSTAL TO MEASURE COLLECTOR VOLTAGE.
ON 2ND MIXER, INCREASE SIGNAL INPUT TO APPROX. 0.3 V TO OVERRIDE INJECTION VOLTAGE.



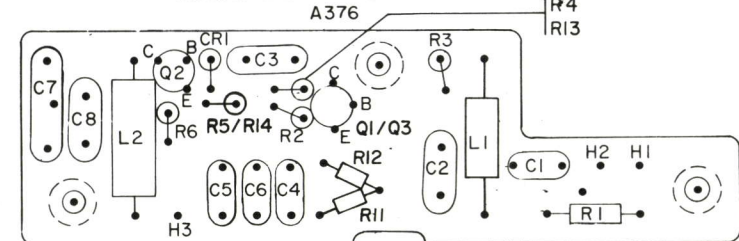
NOISE BLANKER

A370 (25-33 MHz)
A371 (33-42 MHz)
A372 (42-50 MHz)



NOISE LEVEL SHUT-OFF

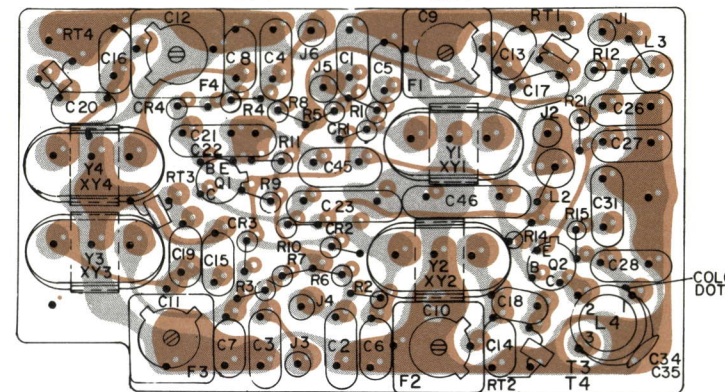
A376



1ST OSCILLATOR/MULTIPLIER

25-33 MHz 33-50 MHz

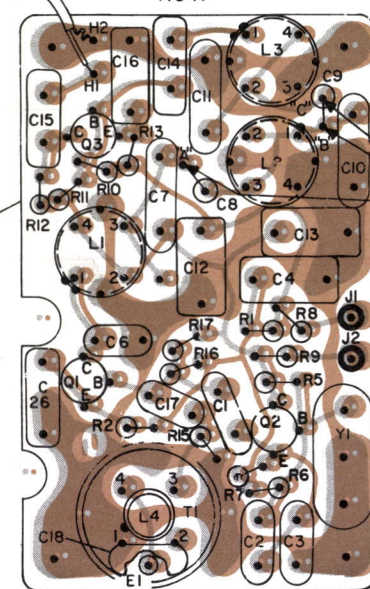
A352 — 1 FREQ — A353
A354 — 2 FREQ — A355
A356 — 4 FREQ — A357



(19B204412, Sh. 1, Rev. 5)
(19B204412, Sh. 2, Rev. 5)

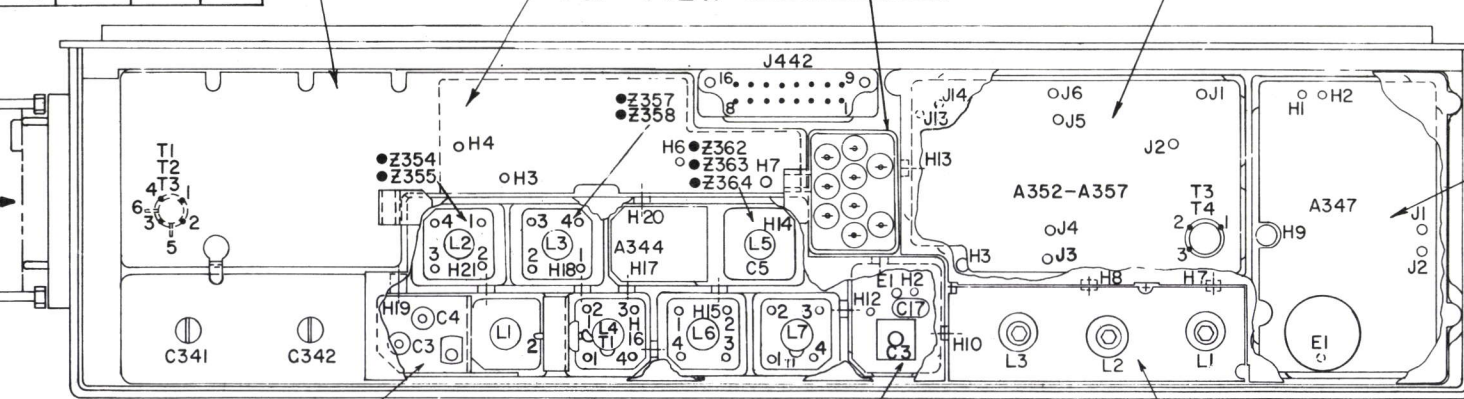
2ND MIXER

A347



(19B205441, Sh. 1, Rev. 0)
(19B205441, Sh. 2, Rev. 0)

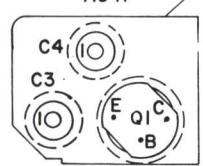
TOP VIEW



•NOISE BLANKER ONLY

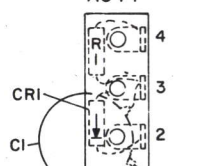
1ST RF AMP

A341



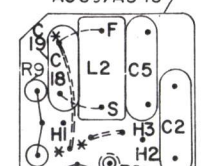
2ND RF AMP

A344

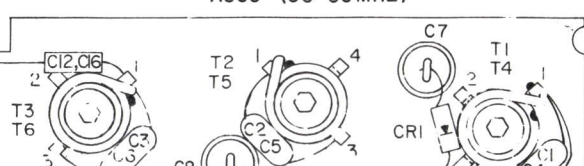


1ST MIXER

A339/A345



MULTIPLIER-SELECTIVITY

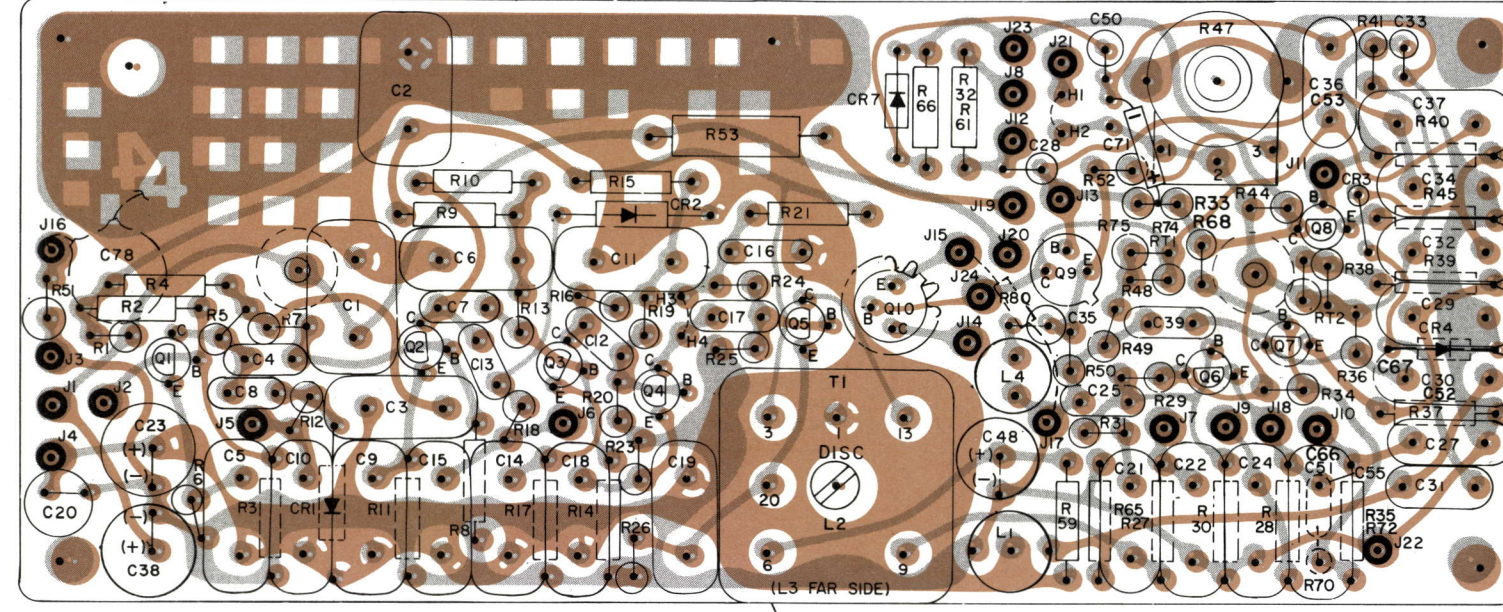
A364 (25-33 MHz)
A365 (33-50 MHz)

OUTLINE DIAGRAM

25 — 50 MHz, MASTR RECEIVER
MODELS 4ER39A10-18

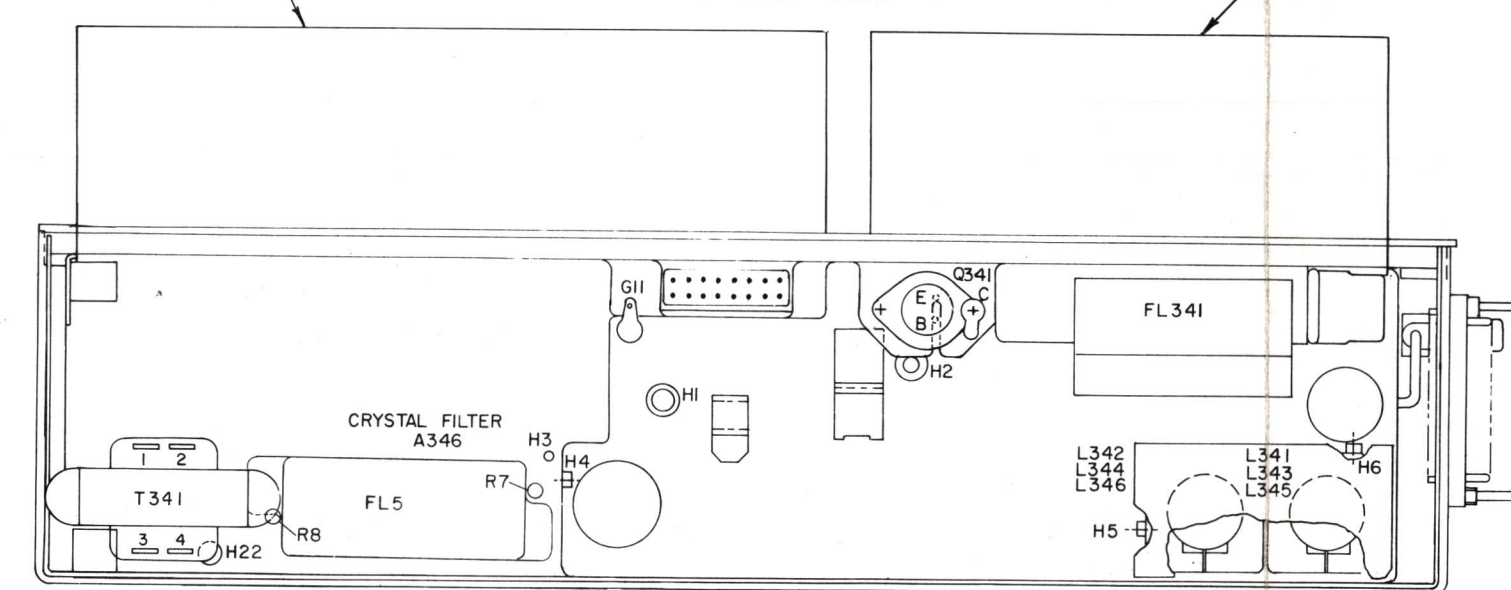
IF-AUDIO & SQUELCH BOARD

A348(WITHOUT CHANNEL GUARD) A373 (NOISE BLANKER)
A349(WITH CHANNEL GUARD) A374 (NOISE BLANKER & CHANNEL GUARD)



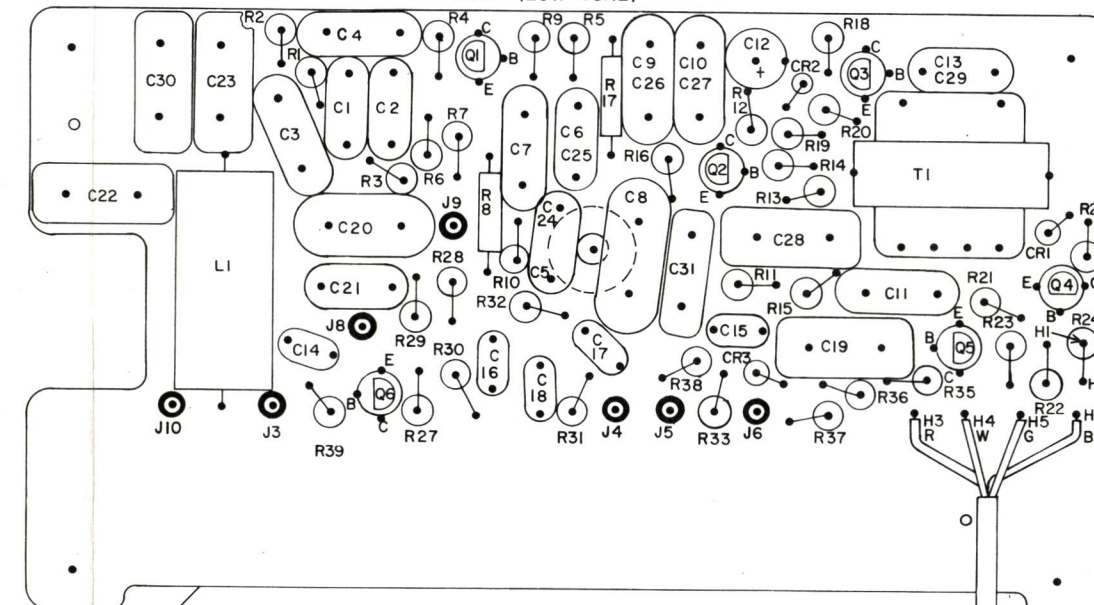
(19C303451, Sh. 1, Rev. 4)
(19C303451, Sh. 2, Rev. 4)

BOTTOM VIEW



CHANNEL GUARD

A350 (HI TONE)
A351 (LOW TONE)



TO DECODER
REED FL341

TRANSISTOR	EMITTER	BASE	COLLECTOR
A350/351	+	+	+
Q1	56Ω	56Ω	8.3K
Q2	270Ω	270Ω	8K
Q3	1K	1K	75K
Q4	0	0	14K
Q5	0	0	14K
Q6	20Ω	20Ω	4.5K

RESISTANCE READINGS

ALL READINGS ARE TYPICAL READINGS MEASURED WITH A 20,000 OHM-PER-VOLT METER, AND WITH CONTROL CABLE DISCONNECTED (OR IN STATIONS, PLUG TO J443 DISCONNECTED). READINGS ARE MADE WITH A SHORTING JUMPER CONNECTED FROM C361 (+10V) TO C360 (-10V), AND ARE MEASURED FROM TRANSISTOR PINS TO C361. +OR- SIGNS SHOW METER LEAD TO C361.

CAUTION

ALWAYS REMOVE THE SHORTING JUMPER AFTER MAKING RESISTANCE READINGS. APPLYING POWER WITH THE SHORTING JUMPER CONNECTED MAY DAMAGE THE UNIT.

FOR READINGS OF: USE SCALE:

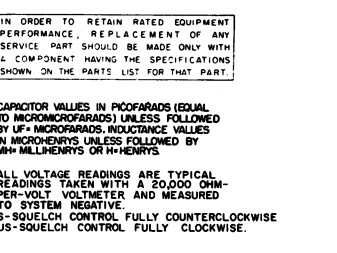
1-100Ω X 1
100-1K Ω X 10
1K-50K Ω X 1,000
50K Ω X 100,000

(19B620741, Rev. 2)

RUNS ON SOLDER SIDE

RUNS ON BOTH SIDES

RUNS ON COMPONENT SIDE



PARTS LIST		
LBI3537E		
25-50 MHz RECEIVER MODELS 4ER39A10 - 4ER39A18 19E500809G1-G9		
SYMBOL	GE PART NO.	DESCRIPTION
A341	19B204772G1	RF AMPLIFIER ASSEMBLY
		19B204772G1
		----- CAPACITORS -----
		C1 5434481P12 Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C2	5494481P14	Ceramic disc: 2000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C3 and C4	5493392P7	Ceramic, feed-thru: 1000 pf +100% -0%, 500 VDCW; sim to Allen-Bradley Type FASC.
Q1	19A116859P1	----- TRANSISTORS -----
		Silicon, NPN; sim to Type 2N5032 or 2N3570.
		----- RESISTORS -----
		R1 3R152P123K Composition: 12K ohms ±10%, 1/4 w.
R2	3R152P302J	Composition: 3K ohms ±5%, 1/4 w.
R3	3R152P102K	Composition: 1K ohms ±10%, 1/4 w.
R4	3R152P391K	Composition: 390 ohms ±10%, 1/4 w.
XQ1	5490277P5	----- SOCKETS -----
		Transistor: 3 contacts rated at 1 amp at 400 VRMS; sim to Alcon 1213LL2.
A345	19B204430G2	FIRST MIXER ASSEMBLY
		19B204430G2
		----- CAPACITORS -----
		C2 5494481P114 Ceramic disc: 2000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C3	5491271P106	Variable, sub-miniature: approx 2.1-12.7 pf, 750 v peak; sim to EF Johnson 189.
C4*	5496218P247	Ceramic disc: 22 pf ±5%, 500 VDCW, temp coef -80 PPM. Deleted by REV S.
C5	5494481P114	Ceramic disc: 2000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C6	5494481P12	Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C18*	5496218P247	Ceramic disc: 22 pf ±5%, 500 VDCW, temp coef -80 PPM.
5496218P244		In Models of REV R and earlier:
		Ceramic disc: 15 pf ±5%, 500 VDCW, temp coef -80 PPM. Added by REV N.
		----- TERMINALS -----
		E1 4038104P1 Lug: solder dipped brass.
L2	19A121082G2	----- INDUCTORS -----
		Toroidal coil.
Q2	19A115245P1	----- TRANSISTORS -----
		Silicon, NPN.
		----- RESISTORS -----
		R1 3R152P563J Composition: 56K ohms ±5%, 1/4 w.
R2	3R152P822J	Composition: 8.2K ohms ±5%, 1/4 w.
R3	3R152P202J	Composition: 2K ohms ±5%, 1/4 w.
R4*	3R152P102J	Composition: 1K ohms ±5%, 1/4 w. Deleted by REV K.
R5	3R152P390J	Composition: 39 ohms ±5%, 1/4 w.
R9*	3R152P471K	Composition: 470 ohms ±10%, 1/4 w. Added by REV K.

SYMBOL	GE PART NO.	DESCRIPTION
A346*		CRYSTAL FILTER ASSEMBLY 19B204618G3 Used in Units of REV C. Units of REV B or earlier used Crystal Filter Assembly 19B204618G1.
----- FILTERS -----		
FL5*	19B206892G1	Bandpass.
		In Models of REV R and earlier:
FL6*	19C304094G4	Bandpass.
	19C304094G4	Bandpass. Deleted by REV S.
----- RESISTORS -----		
R1*	3R152P432K	Composition: 4.3K ohms $\pm 10\%$, 1/4 w. Deleted by REV S.
R2*	3R152P102K	Composition: 1K ohms $\pm 10\%$, 1/4 w. Deleted by REV S.
R7*	3R152P562K	Composition: 5.6K ohms $\pm 10\%$, 1/4 w. Added by REV S.
R8*	3R152P103K	Composition: 10K ohms $\pm 10\%$, 1/4 w. Added by REV S.
		In Models of REV B or earlier:
A346*		CRYSTAL FILTER ASSEMBLY 19B204618G1 (REV B and earlier)
----- FILTERS -----		
FL1 and FL2	19C304094G1	Bandpass.
----- RESISTORS -----		
R1	3R152P432K	Composition: 4.3K ohms $\pm 10\%$, 1/4 w.
R2	3R152P102K	Composition: 1K ohms $\pm 10\%$, 1/4 w.
A347		SECOND MIXER ASSEMBLY 19B204438G1
----- CAPACITORS -----		
C1	5490008P9	Silver mica: 18 pf $\pm 5\%$, 500 VDCW; sim to Electro Motive Type DM-15.
C2 and C3	5490008P35	Silver mica: 220 pf $\pm 5\%$, 500 VDCW; sim to Electro Motive Type DM-15.
C4*	19A116080P7	Polyester: 0.1 μ f $\pm 20\%$, 50 VDCW.
	5491189P106	In Models of REV K and earlier: Polyester: 0.1 μ f $\pm 20\%$, 50 VDCW.
C5*	19A116090P4	Polyester: 0.033 μ f $\pm 20\%$, 50 VDCW. Deleted by REV R.
	5491189P103	In Models of REV K and earlier: Polyester: 0.033 μ f $\pm 20\%$, 50 VDCW.
C6	19A116656P22J0	Ceramic disc: 22 pf $\pm 5\%$, 500 VDCW, temp coef 0 PPM.
C7*	19A116656P180J1	Ceramic disc: 180 pf $\pm 5\%$, 500 VDCW, temp coef 150 PPM.
	5496219P566	In Models of REV K: Ceramic disc: 130 pf $\pm 5\%$, 500 VDCW, temp coef -330 PPM.
	5496219P666	In Models of REV J and earlier: Ceramic disc: 130 pf $\pm 5\%$, 500 VDCW, temp coef -470 PPM.
C8* and C9*	5491601P140	Phenolic: 3.6 pf $\pm 5\%$, 500 VDCW.
	5491601P28	In Models of REV K and earlier: Phenolic: 2.7 pf $\pm 10\%$, 500 VDCW.
C10* and C11*	19A116656P180J1	Ceramic disc: 180 pf $\pm 5\%$, 500 VDCW, temp coef 150 PPM.
	5496219P566	In Models of REV K: Ceramic disc: 130 pf $\pm 5\%$, 500 VDCW, temp coef -330 PPM.
	5496219P666	In Models of REV J and earlier: Ceramic disc: 130 pf $\pm 5\%$, 500 VDCW, temp coef -470 PPM.

SYMBOL	GE PART NO.	DESCRIPTION
C12* and C13*	19A116080P7	Polyester: 0.1 μ f \pm 20%, 50 VDCW.
C14* and C15*	5491189P106	In Models of REV K and earlier: Polyester: 0.01 μ f \pm 20%, 50 VDCW.
	19A116080P1	Polyester: 0.01 μ f \pm 20%, 50 VDCW.
	5491189P101	In Models of REV K and earlier: Polyester: 0.01 μ f \pm 20%, 50 VDCW.
	5491189P104	Polyester: .047 μ f \pm 20%, 50 VDCW.
C16*	19A116080P5	Polyester: .047 μ f \pm 20%, 50 VDCW.
C17	5491189P104	In Models of REV K and earlier: Polyester: .047 μ f \pm 20%, 50 VDCW.
	*19A116655P20	Ceramic disc: 1000 pf \pm 10%, 1000 VDCW; sim to RMC Type JF Discap.
C26*	19A116080P1	Polyester: 0.01 μ f \pm 20%, 50 VDCW. Added by REV L.
----- TERMINALS -----		
E1	4038104P1	Lug: solder dipped brass.
----- JACKS AND RECEPTACLES -----		
J1 and J2	4033513P4	Contact, electrical: sim to Bead Chain L93-3.
----- INDUCTORS -----		
L1*	19A115711P4	Transformer, freq: 455 KHz; sim to Automatic Mfg EX12673.
L2*	19C303464G1	In Models of REV K and earlier: Coil. Includes tuning slug 7160519P2.
	19A115711P3	Transformer, freq: 455 KHz; sim to Automatic Mfg EX12672.
	19C303464G2	In Models of REV K and earlier: Coil. Includes tuning slug 7160519P2.
	19A115711P5	Transformer, freq: 455 KHz; sim to Automatic Mfg EX12673.
L3*	19C303464G3	In Models of REV K and earlier: Coil. Includes tuning slug 7160519P2.
	19A115711P5	Transformer, freq: 455 KHz; sim to Automatic Mfg EX12673.
	19C303464G3	In Models of REV K and earlier: Coil. Includes tuning slug 7160519P2.
	19A115711P5	(Part of L3). Deleted by REV L.
----- PLUGS -----		
P1	4029840P2	Contact, electrical; sim to Amp 42827-2.
P2	4029840P1	Contact, electrical; sim to Amp 41854.
----- TRANSISTORS -----		
Q1	19A115245P1	Silicon, NPN.
Q2	19A115889P1	Silicon, NPN.
Q3	19A115123P1	Silicon, NPN.
----- RESISTORS -----		
R1	3R152P152K	Composition: 1.5K ohms \pm 10%, 1/4 w.
R2	3R152P392K	Composition: 3.9K ohms \pm 10%, 1/4 w.
R3*	3R152P103K	Composition: 10K ohms \pm 10%, 1/4 w. Deleted by REV D.
R4*	3R152P333K	Composition: 33K ohms \pm 10%, 1/4 w. Deleted by REV D.
R5 and R6	3R152P103K	Composition: 10K ohms \pm 10%, 1/4 w.
R7	3R152P512J	Composition: 5.1K ohms \pm 5%, 1/4 w.
R8 and R9	3R152P201J	Composition: 200 ohms \pm 5%, 1/4 w.
R10	3R152P302J	Composition: 3K ohms \pm 5%, 1/4 w.
R11	3R152P622J	Composition: 6.2K ohms \pm 5%, 1/4 w.
R12	3R152P302J	Composition: 3K ohms \pm 5%, 1/4 w.
R13	3R152P202J	Composition: 2K ohms \pm 5%, 1/4 w.
R15*	3R152P153K	Composition: 15K ohms \pm 10%, 1/4 w. Added by REV D. Deleted by REV S.
R16*	3R152P104K	Composition: 100K ohms \pm 10%, 1/4 w. Added by REV D.
R17*	3R152P394K	Composition: 390K ohms \pm 10%, 1/4 w. Added by REV D.

SYMBOL	GE PART NO.	DESCRIPTION
T1		----- TRANSFORMERS -----
		COIL ASSEMBLY 19B204414G1
		----- CAPACITORS -----
		C18 19C301540P261 Ceramic disc: 82 pf ±5%, 200 VDCW, temp coef -80 PPM.
5491798P3		----- MISCELLANEOUS -----
		Tuning slug.
		----- CRYSTALS -----
		Y1* 19A110192P3 Quartz: freq 4845 KHz ±100 Hz at 25°C, temp range -30°C to +75°C.
19A110192P1		In Models of REV B or earlier:
		Quartz: freq 4805 KHz ±100 Hz at 25°C, temp range -30°C to +75°C.
		IF/AUDIO ASSEMBLY 19B402327G3
		----- CAPACITORS -----
C1	19A115028P116	Polyester: 0.22 µf ±20%, 200 VDCW.
C2	19A116080P9	Polyester: 0.22 µf ±20%, 50 VDCW.
C3	19A115028P111	Polyester: 0.047 µf ±20%, 200 VDCW.
C4	5494481P112	Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C5	19A115028P109	Polyester: 0.022 µf ±20%, 200 VDCW.
C6	19A115028P111	Polyester: 0.047 µf ±20%, 200 VDCW.
C7	5494481P112	Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C8	5496219P717	Ceramic disc: 47 pf ±10%, 500 VDCW, temp coef -750 PPM.
C9	19A115028P109	Polyester: 0.022 µf ±20%, 200 VDCW.
C10	19A115028P114	Polyester: 0.1 µf ±20%, 200 VDCW.
C11	19A115028P111	Polyester: 0.047 µf ±20%, 200 VDCW.
C12	5494481P112	Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C13	5496219P717	Ceramic disc: 47 pf ±10%, 500 VDCW, temp coef -750 PPM.
C14	19A115028P109	Polyester: 0.022 µf ±20%, 200 VDCW.
C15	19A115028P114	Polyester: 0.1 µf ±20%, 200 VDCW.
C16	596219P421	Ceramic disc: 100 pf ±10%, 500 VDCW, temp coef -220 PPM.
C17	5494481P112	Ceramic disc: 1000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C18 and C19	19A115028P109	Polyester: 0.022 µf ±20%, 200 VDCW.
C20*	19A115680P103	Electrolytic: 20 µf +150% -10%, 25 VDCW; sim to Mallory Type TMX.
5496267P14		In REV S and earlier:
		Tantalum: 15 µf ±20%, 20 VDCW; sim to Sprague 150D.
		Polyester: 0.22 µf ±20%, 50 VDCW.
		Polyester: 0.01 µf ±20%, 200 VDCW.
C22	19A115028P107	Polyester: 0.01 µf ±20%, 200 VDCW.
C23	5491000P1	Electrolytic: 30 µf +75% -10%, 25 VDCW; sim to Sprague D25379.
C24	19A115028P107	Polyester: 0.01 µf ±20%, 200 VDCW.
C25	5494481P112	Ceramic disc: 1030 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
C27	19A116080P7	Polyester: 0.1 µf ±20%, 50 VDCW.
C28	5496267P17	Tantalum: 1 µf ±20%, 35 VDCW; sim to Sprague Type 150D.
C29	19A116080P9	Polyester: 0.22 µf ±20%, 50 VDCW.
C31	19A116080P5	Polyester: 0.047 µf ±20%, 50 VDCW.
C32	19A116080P9	Polyester: 0.22 µf ±20%, 50 VDCW.
C33	5496267P28	Tantalum: 0.47 µf ±20%, 35 VDCW; sim to Sprague Type 150D.
C34	19A116080P9	Polyester: 0.22 µf ±20%, 50 VDCW.
C35	5496267P6	Tantalum: 33 µf ±20%, 10 VDCW; sim to Sprague Type 150D.

SYMBOL	GE PART NO.	DESCRIPTION
C37*	19A115028P305	Polyester: 0.0068 µf ±10%, 200 VDCW.
19A115028P303		In Models of REV D and earlier:
		Polyester: 0.0033 µf ±10%, 200 VDCW.
		Electrolytic: 100 µf +150% -10%, 15 VDCW; sim to Mallory Type TTX.
		Electrolytic: 35 µf +75% -10%, 15 VDCW; sim to Sprague 30D.
C38	19A115680P107	Electrolytic: 100 µf +150% -10%, 15 VDCW; sim to Mallory Type TTX.
C39	5490308P143	Silver mica: 470 pf ±10%, 300 VDCW; sim to Electro Motive Type DM-15.
C48	5495670P9	Electrolytic: 35 µf +75% -10%, 15 VDCW; sim to Sprague 30D.
C50	5496267P14	Tantalum: 15 µf ±20%, 20 VDCW; sim to Sprague Type 150D.
C52	4029003P16	Silver mica: .0022 µf ±5%, 500 VDCW; sim to Electro Motive Type DM-20.
C53*	19A115028P315	Polyester: 0.15 µf ±10%, 200 VDCW.
19B209243P1		In Models of REV D and earlier:
		Polyester: 0.01 µf ±20%, 50 VDCW.
		Polyester: 0.01 µf ±20%, 50 VDCW.
		Polyester: 0.01 µf ±20%, 50 VDCW.
C55	19A116080P1	Tantalum: 0.47 µf ±20%, 35 VDCW; sim to Sprague Type 150D. Added by REV J.
C71*	5496267P28	Ceramic disc: 2000 pf ±10%, 1000 VDCW; sim to RMC Type JF Discap. Added by REV V.
C78*	5494481P114	Ceramic disc: 2000 pf ±10%, 1000 VDCW; sim to RMC Type JF Discap. Added by REV V.
----- DIODES AND RECTIFIERS -----		
CR1 and CR2	4038056P1	Germanium, fast recovery, Reverse voltage 20 volts, Fwd. current 40 mA.
CR3 and CR4	19A115250P1	Silicon, fast recovery, 225 mA, 50 PIV.
CR7	19A115250P1	Silicon, fast recovery, 225 mA, 50 PIV.
J1 thru J24	4033513P4	Contact, electrical: sim to Bead Chain L93-3.
L1	4031476G1	Choke.
L4	5491736P8	Choke: 3.5 mh ±10% ind at 1 KHz, 2.5 ohms DC res max; sim to Aladdin 33-494.
----- TRANSISTORS -----		
Q1 thru Q3	19A115123P1	Silicon, NPN.
Q4* and Q5*	19A115552P1	Silicon, NPN; sim to 2N2714.
19A115123P1		In Models of REV H and earlier:
		Silicon, NPN.
		Silicon, NPN.
		Silicon, NPN.
Q6	19A115123P1	Silicon, NPN.
Q7	19A115889P1	Silicon, NPN.
Q8	19A115123P1	Silicon, NPN.
Q9	19A115247P1	Silicon, PNP; sim to Type 2N1024.
Q10	19A115300P2	Silicon, NPN; sim to Type 2N3053.
----- RESISTORS -----		
R1	3R77P330K	Composition: 33 ohms ±10%, 1/2 w.
R2	3R77P473K	Composition: 47K ohms ±10%, 1/2 w.
R3	3R77P183J	Composition: 18K ohms ±5%, 1/2 w.
R4	3R77P101K	Composition: 100 ohms ±10%, 1/2 w.
R5	3R77P472K	Composition: 4.7K ohms ±10%, 1/2 w.
R6	3R77P202J	Composition: 2K ohms ±5%, 1/2 w.
R7	3R77P473K	Composition: 47K ohms ±10%, 1/2 w.
R8	3R77P183J	Composition: 18K ohms ±5%, 1/2 w.
R9	3R77P101K	Composition: 100 ohms ±10%, 1/2 w.
R10	3R77P472K	Composition: 4.7K ohms ±10%, 1/2 w.
R11	3R77P202J	Composition: 2K ohms ±5%, 1/2 w.
R12	3R77P103K	Composition: 10K ohms ±10%, 1/2 w.
R13	3R77P473K	Composition: 47K ohms ±10%, 1/2 w.

SYMBOL	GE PART NO.	DESCRIPTION
R14	3R77P183J	Composition: 18K ohms ±5%, 1/2 w.
R15	3R77P101K	Composition: 100 ohms ±10%, 1/2 w.
R16	3R77P472K	Composition: 4.7K ohms ±10%, 1/2 w.
R17	3R77P202J	Composition: 2K ohms ±5%, 1/2 w.
R18	3R77P103K	Composition: 10K ohms ±10%, 1/2 w.
R19	3R77P473K	Composition: 47K ohms ±10%, 1/2 w.
R20	3R77P183J	Composition: 18K ohms ±5%, 1/2 w.
R21	3R77P472K	Composition: 4.7K ohms ±10%, 1/2 w.
R23	3R77P202J	Composition: 2K ohms ±5%, 1/2 w.
R24	3R77P682K	Composition: 6.8K ohms ±10%, 1/2 w.
R25	3R77P183J	

SYMBOL	GE PART NO.	DESCRIPTION
C3	5496218P251	Ceramic disc: 33 pf ±5%, 500 VDCW, temp coef -80 PPM.
C4 and C5	5496218P248	Ceramic disc: 24 pf ±5%, 500 VDCW, temp coef -80 PPM.
C6	5496218P247	Ceramic disc: 22 pf ±5%, 500 VDCW, temp coef -80 PPM.
C7 and C8	5493392P107	Ceramic feed-thru: 470 pf +100% - 0%, 500 VDCW.
C9	5491601P123	Phenolic: 1.5 pf ±5%, 500 VDCW.
C10	5491601P117	Phenolic: 0.68 pf ±5%, 500 VDCW.
C11	5491601P118	Phenolic: 0.75 pf ±5%, 500 VDCW.
C12	5491601P132	Phenolic: 4.7 pf ±5%, 500 VDCW.
C13	5491601P137	Phenolic: 0.91 pf ±5%, 500 VDCW.
C14	5491601P114	Phenolic: 0.51 pf ±5%, 500 VDCW.
C15	5491601P115	Phenolic: 0.56 pf ±5%, 500 VDCW.
C16	5491601P130	Phenolic: 3.3 pf ±5%, 500 VDCW.
----- DIODES AND RECTIFIERS -----		
CR1	4038056P1	Germanium, fast recovery, 20 Reverse volts, Fwd. current 40 mA.
----- RESISTORS -----		
R1	3R152P473K	Composition: 47K ohms ±10%, 1/4 w.
----- TRANSFORMERS -----		
T1	19B205325G2	Coil. Includes:
	5491798P4	Tuning slug.
T2 and T3	19B205325G1	Coil. Includes:
	5491798P4	Tuning slug.
T4	19B205325G2	Coil. Includes:
	5491798P4	Tuning slug.
T5 and T6	19B205325G1	Coil. Includes:
	5491798P4	Tuning slug.
A364* and A365*		In Models of REV J and earlier: MULTIPLIER SELECTIVITY ASSEMBLY A364 19B204782G1 (4ER39A10, 13 and 16) A365 19B204782G2 (4ER39A11,12, 14,15,17,18)
----- CAPACITORS -----		
C5 and C6	5493392P7	Ceramic feed-thru: 1000 pf +100% -0%, 500 VDCW; sim to Allen-Bradley Type FASC.
C7 and C8	5491601P115	Phenolic: 0.56 pf ±5%, 500 VDCW.
C10 and C11	5491601P117	Phenolic: 0.68 pf ±5%, 500 VDCW.
C13 and C14	5491601P130	Phenolic: 3.3 pf ±5%, 500 VDCW.
----- DIODES AND RECTIFIERS -----		
CR1	7777146P3	Germanium; sim to Type 1N90.
----- RESISTORS -----		
R1	3R152P332K	Composition: 3.9K ohms ±10%, 1/4 w.
----- TRANSFORMERS -----		
T1 thru T4		COIL ASSEMBLY T1 19B204780G1 (4ER39A10, 13 and 16) T2 19B204780G2 (4ER39A10, 13 and 16) T3 19B204780G3 (4ER39A11,12,14,15,17,18) T4 19B204780G4 (4ER39A11,12,14,15,17,18)
----- CAPACITORS -----		
C1 and C2	5496218P252	Ceramic disc: 36 pf ±5%,500 VDCW, temp coef -80 PPM.
C3 and C4	5496218P248	Ceramic disc: 24 pf ±5%, 500 VDCW, temp coef -80 PPM.

SYMBOL	GE PART NO.	DESCRIPTION
----- INDUCTORS -----		
L1	19A121510P1	Coil. Includes:
	5491798P4	Tuning slug.
L2	19A121510P2	Coil. Includes:
	5491798P4	Tuning slug.
----- CAPACITORS -----		
C349	5491601P114	Phenolic: 0.51 pf ±5%, 500 VDCW; sim to Quality Components Type MC.
C350 and C351	5491601P110	Phenolic: 0.36 pf ±5%, 500 VDCW; sim to Quality Components Type MC.
C358 thru C363	5493392P7	Ceramic feed-thru: 1000 pf +100% -0%, 500 VDCW; sim to Allen-Bradley Type FASC.
C335*	7774750P4	Ceramic disc: .001 pf +100% -0%, 500 VDCW. Added by REV G.
----- DIODES AND RECTIFIERS -----		
CR301*	4037822P1	Silicon, 1000 mA, 400 PIV. Added by REV V.
----- JACKS AND RECEPTACLES -----		
J441	19B209122P1	Connector, coaxial: includes cable (W441), approx 5 inches long.
J442	19B205689G2	Connector: 18 contacts rated at 5 amps min at 1000 VDC max.
J443	19C303426G1	Connector: 20 pin contacts.
----- INDUCTORS -----		
L341 thru L346		COIL ASSEMBLY L341 19B204820G5 (4ER39A10, 13 and 16) L342 19B204820G6 (4ER39A10, 13 and 16) L343 19B204820G1 (4ER39A11, 14 and 17) L344 19B204820G2 (4ER39A11, 14 and 17) L345 19B204820G3 (4ER39A12, 15 and 18) L346 19B204820G4 (4ER39A12, 15 and 18)
----- CAPACITORS -----		
C341	19B209159P3	Variable, sub-miniature: approx 1.70-6.9 pf, 750 v peak; sim to EF Johnson 189. (Used in L341, L343 and L345).
C342	19B209159P3	Variable, sub-miniature: approx 1.70-6.9 pf, 750 v peak; sim to EF Johnson 189. (Used in L342, L344 and L346).
----- INDICATING DEVICES -----		
DS301*	19B209067P1	Lamp, glow: 0.3 ma; sim to GE NE-2T. Added by REV H.
----- PLUGS -----		
P304 thru P309	4029340P2	Contact, electrical; sim to Amp 42827-2.
P310	4029840P1	Contact, electrical; sim to Amp 41854.
P311 thru P320	4029840P2	Contact, electrical; sim to Amp 42827-2.
P321	4029840P1	Contact, electrical; sim to Amp 41854.
P325	4029840P2	Contact, electrical; sim to Amp 42827-2.
P329	4029840P2	Contact, electrical; sim to Amp 42827-2.
P337	4029840P2	Contact, electrical; sim to Amp 42827-2.
----- TRANSISTORS -----		
Q341*	19A115527P1	Silicon, NPN. (Replacement for 19A115246P1 used in some revisions).
	19A115246P1	Silicon, NPN.
	19A115527P1	In Models of REV E and earlier: Silicon, NPN.
----- RESISTORS -----		
R343* and R344*	3R152P101K	Composition: 100 ohms ±10%, 1/4 w. Added by REV K.

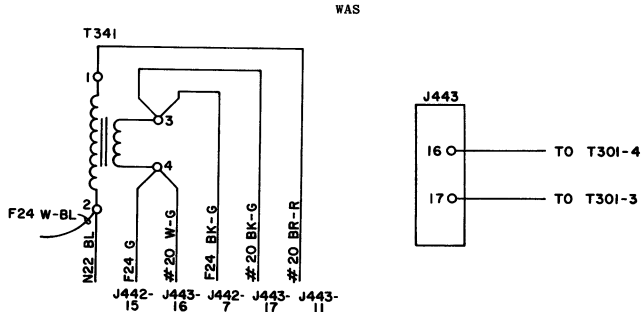
SYMBOL	GE PART NO.	DESCRIPTION
----- TRANSFORMERS -----		
T341*	19B209083P2	Audio freq: Pri 1: 19 ohms ±10% imp at 3 w. Sec 1: 3.5 ohms ±10% imp at 3 w.
	19B209083P1	In Models of REV E and earlier: Audio freq: Pri 1: 19 ohms ±10% imp at 3 w. Sec 1: 3.5 ohms ±10% imp at 3 w.
----- TERMINAL BOARDS -----		
TB1	7487424P7	Miniature, phen: 4 terminals.
----- CABLES -----		
W441		(Part of J441).
W442	19B205634G6	Coaxial: approx 5 inches long.
W443	19B205634G3	Coaxial: approx 5 inches long.
----- TUNED CIRCUITS -----		
Z341 thru Z343		COIL ASSEMBLY Z341 19B204786G1 (4ER39A10, 13 and 16) Z342 19B204786G2 (4ER39A11, 14 and 17) Z343 19B204786G3 (4ER39A12, 15 and 18)
----- CAPACITORS -----		
C1*	5496218P254	Ceramic disc: 43 pf ±5%, 500 VDCW, temp coef -80 PPM.
	5496218P257	In Models of REV J and earlier: Ceramic disc: 56 pf ±5%, 500 VDCW, temp coef -80 PPM.
C2	5496218P250	Ceramic disc: 30 pf ±5%, 500 VDCW, temp coef -80 PPM.
C3	5496218P245	Ceramic disc: 18 pf ±5%, 500 VDCW, temp coef -80 PPM.
C4	5494481P14	Ceramic disc: 2000 pf ±10%, 500 VDCW; sim to RMC Type JF Discap.
----- MISCELLANEOUS -----		
	5491798P1	Tuning slug. (Used in Z341).
	5491798P4	Tuning slug. (Used in Z342).
	5491798P5	Tuning slug. (Used in Z343).
Z347 thru Z349		COIL ASSEMBLY Z347 19B204767G1 (4ER39A10, 13 and 16) Z348 19B204767G2 (4ER39A11, 14 and 17) Z349 19B204767G3 (4ER39A12, 15 and 18)
----- CAPACITORS -----		
C1*	5496218P254	Ceramic disc: 43 pf ±5%, 500 VDCW, temp coef -80 PPM.
	5496218P257	In Models of REV J and earlier: Ceramic disc: 56 pf ±5%, 500 VDCW, temp coef -80 PPM.
C2	5496218P250	Ceramic disc: 30 pf ±5%, 500 VDCW, temp coef -80 PPM.
C3	5496218P245	Ceramic disc: 18 pf ±5%, 500 VDCW, temp coef -80 PPM.
----- MISCELLANEOUS -----		
	5491798P1	Tuning slug. (Used in Z347).
	5491798P4	Tuning slug. (Used in Z348).
	5491798P5	Tuning slug. (Used in Z349).
----- CAPACITORS -----		
Z350* thru Z352*		COIL ASSEMBLY Z350 19B204784G4 (4ER39A10, 13 and 16) Z351 19B204784G5 (4ER39A11, 14 and 17) Z352 19B204784G6 (4ER39A12, 15 and 18)
C7 and C8	5496218P248	Ceramic disc: 24 pf ±5%, 500 VDCW, temp coef -80 PPM.
C9	5496218P244	Ceramic disc: 15 pf ±5%, 500 VDCW, temp coef -80 PPM.
C10	5494481P7	Ceramic disc: 470 pf ±20%, 1000 VDCW; sim to RMC Type JF Discap.

SYMBOL	GE PART NO.	DESCRIPTION
----- MISCELLANEOUS -----		
	5491798P1	Tuning slug. (Used in Z350).
	5491798P4	Tuning slug. (Used in Z351).
	5491798P5	Tuning slug. (Used in Z352).
		In Models of REV J and earlier: COIL ASSEMBLY Z350 19B204784G1 (4ER39A10, 13 and 16) Z351 19B204784G2 (4ER39A11, 14 and 17) Z352 19B204784G3 (4ER39A12, 15 and 18)
----- CAPACITORS -----		
C1 and C2	5496218P262	Ceramic disc: 91 pf ±5%, 500 VDCW, temp coef -80 PPM.
C3 and C4	5496218P254	Ceramic disc: 43 pf ±5%, 500 VDCW, temp coef -80 PPM.
C5 and C6	5496218P231	Ceramic disc: 33 pf ±5%, 500 VDCW, temp coef -80 PPM.
----- MISCELLANEOUS -----		
	5491798P4	Tuning slug.
----- MECHANICAL PARTS (SEE RC1199) -----		
2	19B204583G3	Hinge.
3	4035439P1	Transistor heat sink; sim to Birtcher 3AL-635-2R. (Used with Q10 in A348).
4	4036555P1	Insulator, washer: nylon. (Used with Q9 and Q10 in A348).
5	4032187P1	Can. (Used with T1 in A348).
6	19B204583G1	Hinge.
7	19A115784P1	Mica insulator. (Used with Q341).
8	19A121989P1	Bushing. (Used with Q341).
9	19E500812P1	Chassis. (Used in Models 4ER39A11, 12, 14, 15, 17 and 18).
10	19E500812P2	Chassis. (Used in Models 4ER39A10, 13 and 16).
11	19A121229G1	(Not Used).
12	19B204583G2	(Not Used).
13	19A121676P1	Guide pin.
14	19B204673P1	Cover.
15	19C303495G4	Station Receiver, Bottom Cover.
	19C303385P1	Mobile Receiver, Bottom Cover.
16	19A121297P1	Angle.
17	7160861P4	Nut, spring clip: sim to Tinnerman C6452-8Z-157.
18	4029851P6	(Not Used).
19	N529P27C	Plug button.
20	19A115461P2	Spring washer; sim to Shakeproof 3597-04-00.
21	4034252P5	Can; sim to Hudson Tool and Die HV-1236-2. (Used with T1 in A347).
22	19B204672P1	Cover.
23	7162414P1	Mounting ring, transistor socket; sim to Elco 757. (Used with Q1 in A341).
24	19B204917P1	Support. (Used with A341).
25	19B204719P1	Plate.
26	19C303495G3	Station Receiver Top Cover (except Repeaters and VM Stations).
	19C303676G2	Station Receiver Top Cover (Repeaters and VM Stations).
	19C303385P2	Mobile Receiver Top Cover.
27	4029851P3	Cable clamp: nylon; sim to Weckesser 1/8-4.
28	19A121383P1	Support.
29	4033089P1	Clip. (Part of XY1-4 in A352-357).
30	19B200525P9	Rivet. (Part of XY1-4 in A352-357).
31	19A115793P1	Electrical contact; sim to Methode 752 V (PB). (Part of XY1-4 in A352-357).
32	19C311172P1	Crystal socket. (Part of XY1-4 in A352-357).
33	19C303547P1	Cover.
34	19C303394G1	Heat sink.
35	19C303389G1	Chassis.

PRODUCTION CHANGES

Changes in the equipment to improve performance or to simplify circuits are identified by a "Revision Letter", which is stamped after the model number of the unit. The revision stamped on the unit includes all previous revisions. Refer to the Parts List for descriptions of parts affected by these revisions.

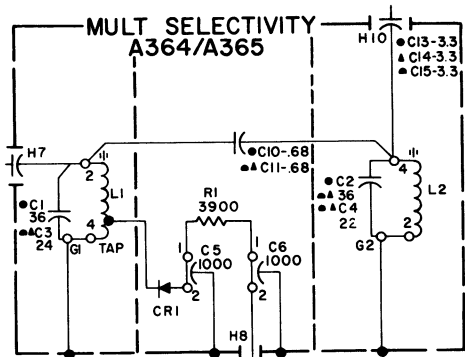
- REV. A & B - These revisions were incorporated into initial shipments.
- REV. C - To minimize chance of interference, IF frequency changed from 5.26 MC to 5.30 MC. Changed crystal filter A346 and A347-Y1.
- REV. D - To improve receiver performance in areas of high signal level. On 2nd Mixer Board A347, replaced R3 and R4 with R16 and R17, and added R15.
- REV. E - To improve audio response. Changed C37 and C53.
- REV. F - On single frequency circuits deleted CR1, R5 and added R19 to 1st Oscillator Board A352/A357. To incorporate improved transistor and transformer. Changed Q341 and T341.



- REV. G - To eliminate audio howling caused by feedback within the receiver cabling. Added C385.
- REV. H - To improve RF burnout protection of front-end air capacitor and RF transistor. Added neon lamp D8301 across L341/L343/L345 in 1st RF Amplifier A341.
- REV. J - To provide better temperature compensation for low IF circuitry. To reduce variation in discriminator output and reduce audio rumble produced when volume control is at minimum and squelch near critical. Changed Q4 and Q5, deleted R46, added R74, R75 and C71 on IF/Audio Board A348. Changed C7, C10, and C11 on 2nd Mixer Board A347.
- REV. K - To improve spurious rejection. Changed C1 on coil assemblies Z341 and Z347. Changed core on coil assemblies Z341, Z342, Z347, and Z348. Changed coil assemblies Z350, Z351, and Z352. Added R343 and R344. Changed R4 to R9 on 1st Mixer Board A345. Changed C1 to C48, C2 to C49, C3 to C50, C4 to C51, C24 to C45, C25 to C46, R13 to R21, T3 and T4 on 1st Oscillator Assembly A352/A357. Changed Multiplier Selectivity Board A364/A365.

Multiplier Selectivity Was

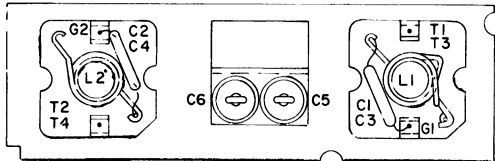
Schematic



Outline

MULTIPLIER-SELECTIVITY

A364 (25-33 MC)
A365 (33-50 MC)

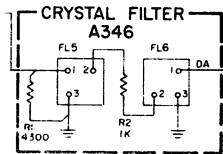


Changed RF Alignment. Front End Alignment Step 1 and Receiver Alignment Step 3. Tuning Control was "L4 (on 1st OSC/MULT) and L1 and L2 (on MULT SELECTIVITY)", and Procedure was "Tune L4 on 1st OSC/MULT and L1 on MULT SELECTIVITY for maximum meter reading. Tune L2 for minimum meter reading. Change voltage scale if necessary". Front End Alignment Step 4 and Complete Receiver Alignment Step 8 Tuning Control was "L4 (1st OSC/MULT) and L1 and L2 (MULT SELECTIVITY)", and Procedure was "Apply and on-frequency signal as above, keeping below saturation. Tune L4 (on 1st OSC/MULT) and L1 and L2 (on MULT SELECTIVITY) for maximum meter reading.

- REV. L - To improve temperature characteristics. Changed C4, C5, C7 through C16, L1, L2, L3, deleted L4, and changed L5 to L4 on 2nd Mixer Board.
- REV. M - To improve squelch performance. Deleted R71 and added R33 to the IF/Audio Board A348.
- REV. N - To provide mid-range tuning of tank circuit on First Mixer Assembly A343. Deleted C4 and added C18.
- REV. P - To improve circuit D.C. bias stability of AUDIO AMP Q10. Added R80.
- REV. R - To reduce receiver lock-up in areas of high RF signal level. Deleted C5 and added C26 on the 2nd Mixer Board A347.
- REV. S - To simplify manufacturing and provide better selectivity. Changed C18 in 1st Mixer A345. Deleted R15 in 2nd Mixer A347. Changed FL5; deleted FL6, R1 and R2; and added R7 and R8 in Crystal Filter A346.

Crystal Filter Schematic

WAS



- REV. T - To improve performance in positive ground applications. Changed C20.
- REV. U - To increase reliability of audio circuit. Added CR301.
- REV. V - To improve operation. Added C78 to A348.

