This addendum updates the Outline and Schematic Diagrams, Parts List, and adds the Production Change information, and a functional block diagram.

Add the following text to page 5 following the paragraph titled " POWER UP RESET"

RESET TIMER

The reset timer assures that the Rockwell modem will always uses the external clock supplied by the Control Point equipment. to maintain system synchronization and insure that the modem is properly configured. It consists of U4, U5, Q1 and associated circuitry. U5 is a free running timer that provides a trigger to monostable FF U4 approximately once every 50 seconds. U4 pulses Q1 which drives Q1B high and provides a negative going 3.0 usec pulse to the WRITE line at P1A-11.

PRODUCTION CHANGE INFORMATION

REV. A - MODEM INTERFACE BOARD. 19D902442G1

Added reset timer to insure proper operation of modem. Deleted R12. Added C13- C15, R31-R34, Q1, U4, and U5.

C12 was: 19A703314P10: Electrolytic: 10uF, 50 VDCW.

C13 is: T644ACP210K, Polyester: 0.001 uF ± 10%, 50VDCW.

C14 is: T644ACP310K, Polyester: 0.01 uF.±10%, 50VDCW.

C15 is: 5496267P12, Tantalum: 150 uF.±20%, 15 VDCW; sim to Sprague Type 150.

R31/R32 is: H212CRP410C, Carbon film: 100K ohms, 0.2W.

R33 is: H212CRP227C, Carbon film: 2.7K ohms, 0.2W.

R34 is: H212CRP210C, Carbon film: 1.0K ohms, 0.2W.

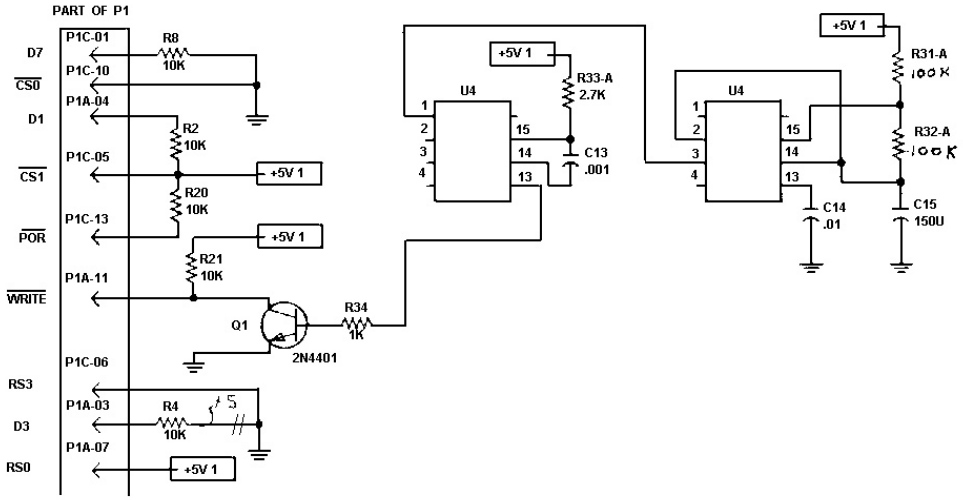
Q1 is: 19A702503P1, Transistor, silicon, NPN; sim to 2N4401.

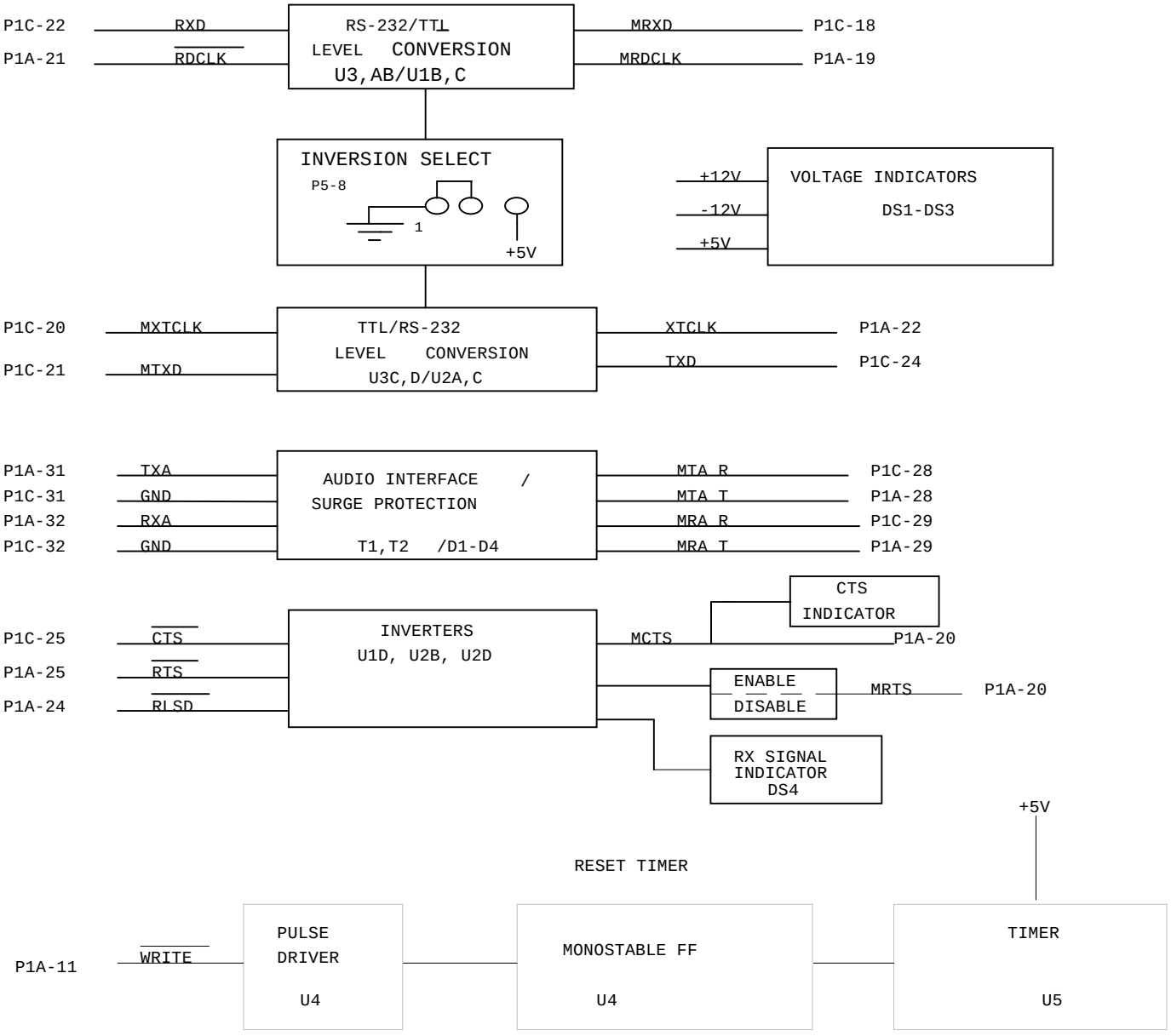
U4 is: 19A700037P354, Dual retriggerable monostable FF with clear clock; sim to 74LS123.

U5 is: 19A130827P1, Dual in-line timer; sim to NE555P.

REV. B - To improve operation of Rockwell modem by providing pre-training tone for level set and quiet tone for fast train sequence.. Connected R1 between P1C-5 and +5V. Connected R4 between P1A-3 A and +5V.

The Schematic is revised to show the Reset Timer as shown below.




P1C-22
RXD
P1A-21
RDCLK
RS-232/TTL
LEVEL CONVERSION
U3,AB/U1B,C
MRXD
MRDCLK
P1C-18
P1A-19
INVERSION SELECT
P5-8
1
+5V
+12V
-12V
+5V
VOLTAGE INDICATORS
DS1-DS3
P1C-20
MXTCLK
P1C-21
MTXD
TTL/RS-232
LEVEL CONVERSION
U3C,D/U2A,C
XTCLK
TXD
P1A-22
P1C-24
P1A-31
TXA
P1C-31
GND
P1A-32
RXA
P1C-32
GND
AUDIO INTERFACE /
SURGE PROTECTION
T1,T2 /D1-D4
MTA R
MTA T
MRA R
MRA T
P1C-28
P1A-28
P1C-29
P1A-29
P1C-25
CTS
P1A-25
RTS
P1A-24
RLSD
INVERTERS
U1D, U2B, U2D
MCTS
CTS
INDICATOR
P1A-20
ENABLE
DISABLE
MRTS
P1A-20
RX SIGNAL
INDICATOR
DS4
+5V
RESET TIMER
P1A-11
WRITE
PULSE
DRIVER
U4
MONOSTABLE FF
U4
TIMER
U5

OUTLINE DIAGRAM