LBI-38637C



MAINTENANCE MANUAL FOR MASTR III T/R SHELF 19D902839G1

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SPECIFICATIONS*

POWER

Input Voltage 13.8 Vdc nominal (20%)
Current Drain 4 Amperes maximum

AUDIO RESPONSE

Receiver To Line +1, -3 dB from -6 dB per octave response for 300 to 3000 Hz referenced

to $1\ kHz$

Line to Transmitter +1, -3 dB from -6 dB per octave response for 300 to 3000 Hz referenced

to 1 kHz

Receiver To Speaker +2, -8 dB from -6 dB per octave response for 300 to 3000 Hz referenced

to 1 kHz

Line Output Level -19 dBm to +11 dBm

Line Input Level -19 dBm to +11 dBm

LINE LOOP IMPEDANCE 11K ohm maximum (8k ohm line, and 3K ohm matching)

LINE TERMINATING IMPEDANCE 600 ohms

NOTCH FILTER RESPONSE -45 dB @ 2175 Hz

CARRIER CONTROL TIMER Programmable from zero to 10 minutes

DROP-OUT DELAY TIMER Programmable from zero to 10 seconds

OPERATING TEMPERATURE -30° C to $+60^{\circ}$ C $(-22^{\circ}$ F to 140° F)

DISTORTION Less Than 2%

SERVICE SPEAKER 1 watt into 8 ohms

PANEL DIMENSIONS (H x W) 8.75 x 19.0 inches (5 Rack Units)

DESCRIPTION

The MASTR III station control electronics are designed for dc/tone remote,remote/repeater, or repeater only applications. The station control electronics, also referred to as the Control Section, consists of a Backplane Board, Power Module, System Module, and an Interface Board. The backplane also connects the RF Section which consists of the Receiver Synthesizer Module, Receiver Front End Module, Receiver IF Module, and the Transmit Synthesizer Module. The Control Section and the RF Section combine into one assembly to form the T/R Shelf.

The Power Module, System Module, and the Interface Board connect to the backplane and thus to one another via 96 pin connectors. The Control Section contains five backplane slots with 3 presently unused. The Interface Board provides interconnection for a local microphone or handset, RS-232 programming or diagnostics, transmitter PA control, transmitter PA fan, auxiliary function relays, optional antenna switch, and optional circulator. Two connectors (terminal block and modular phone) are provided for telephone line connections to the MASTR III Station. Additional connectors are provided on the backplane for connection to GETCs used with systems such as EDACS, VOICE GUARD, GE-MARC, etc.

The Control Section uses programmable microcomputer technology to control the base station's transmitter, receiver, and audio processor. The System Module contains a Digital Signal Processor (DSP) Module used for audio processing and tone generation and detection. The basic Control Section can provide one or two transmit and receive frequencies in DC control applications, and up to four transmit and receive frequencies in tone control applications. Options pro-vided by the Control Section include a transmitter drop-out delay (DOD) timer, Carrier Control Timer (CCT), Channel Guard, and Squelch Operated Relay output (SOR). Additional station options include:

- Battery alarm tone
- Type 90 or DTMF tone decoding
- 2/4 wire audio
- Morse code station identification
- Auxiliary control

BACKPLANE BOARD

The Backplane Board (A1), 19D902947G1 (see Assembly Diagram 19D902839 sheet 1), is a purely passive printed wiring board (pwb) that mounts to the T/R shelf 19D902839G1. The backplane is functionally and physically segmented into two sections. When viewed from the front, the four slots to the left connect the RF Modules. The five slots on the right connect the Control Section modules. The horizontal slot above the five

Control Section slots is occupied by the Interface Board (A2). The slots are assigned as follows from left to right (as viewed from the front of the station):

- Transmitter Synthesizer Module (19D902780)
- Receiver Synthesizer Module (19D902781)
- Receiver Front End Module (19D902782)
- Receiver IF Module (19D902783)
- System Module (19D902590)
- Aux 1
- Aux 2
- Aux 3
- Power Module (19D902589)

INTERFACE BOARD

The Interface Board (A2), 19D902975G1 (see assembly Diagram 19D902839 sheet 1), mounts horizontally above the 5 backplane slots of the Control Section. The Interface Board provides the following functions:

- Rx and Tx Synthesizer loading
- Telephone line interface with current level detection for remote control
- Audio PA for local speaker
- Transmitter power output level and control
- Manual adjustment with front panel access of receiver squelch and local speaker volume
- LED indication of PA Alarm
- Various connectors including RS232 programming port and Mic/Handset port.

SYSTEM MODULE

The System Module 19D902590G3 contains all audio processing and control electronics. The System Module is equipped with a DSP board that rides "piggyback" on the 19D903771G1 System Board. Refer to Maintenance Manual LBI-38764 for complete information on the System Module.

POWER MODULE

The Power Module 19D902589G2 contains switching regulators for the +5V, +12V, and -12V DC supplies. The output of the +12V and -12V supplies are further regulated to provide +5V and

^{*} These specifications are intended primarily for use by service personnel. Refer to the appropriate Specification Sheet for complete specifications.

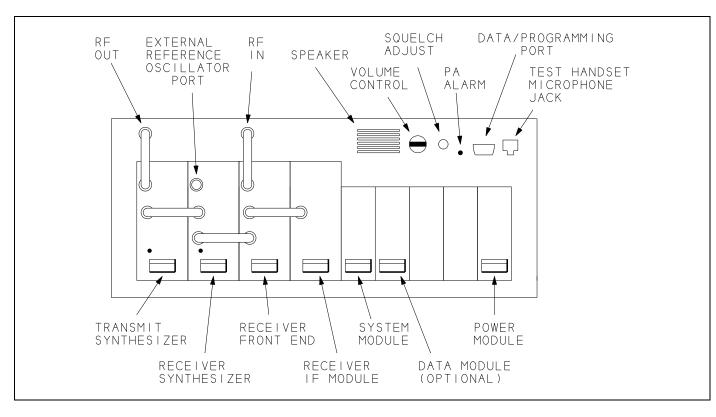


Figure 1 - T/R Shelf Layout

-5V required by the analog components. See Maintenance Manual LBI-38752 for complete information on the power module.

HARNESS

Station wiring for the MASTR III Base Station has been minimized due to the modular architecture. However, a small amount of wiring is necessary for interconnection of some station components. All cables connecting to the Control Section of the T/R Shelf terminate at the Interface Board. See LBI-38636 for Application Drawings and Interconnection Drawings for identification of these cables.

SYSTEM OPERATION

The MASTR III T/R Shelf can be programmed for operation as a DC remote, tone remote, remote re-peater, or repeater only application.

The T/R Shelf control section is equipped with control and status indicators for test purposes. The controls allow the service technician to disable the transmit function, simulate a remote PTT to open up the line, select the station Channel Guard monitor function, and reset the T/R Shelf.

Status indicators available in the T/R Shelf control section include transmit, transmit disable, and CG mon-itor indicators.

There are several common options available for use in the T/R Shelf that are applicable to DC remote control, tone remote control or repeater applications. These options are described in the following paragraphs.

CHANNEL GUARD

There are two types of Channel Guard (CG) available: tone and digital. The T/R Shelf can decode either tone or digital CG information from received audio, and can generate CG tones or digital codes for transmission.

One of many CG tones can be programmed into the T/R Shelf through the personality EEPROM. Different CG tones can be used for decode and encode. The T/R Shelf can be programmed for encode only CG, decode only CG, or to both encode and decode CG.

In addition, both digital codes and tone codes can be used in a station. For example, the station receiver can be programmed for tone codes, and the transmit frequency programmed for a digital code. Prior to any transmission, the CG monitor function can unmute the receiver when any on-frequency signal is received, allowing all on-frequency activity to be mon-itored.

When in the MONITOR mode, the transmitter is activated only if programmed as a repeater, and the proper CG information (tone or digital CG) is present. The monitor function is activated by the local CG Mon-itor switch, or by a remote console.

Tone Channel Guard

Standard CG tone frequencies range from 67 Hz to 210.7 Hz. Extended CG tones are available, but can cause some degradation in specifications.

The T/R Shelf detects a 135-degree phase shift in the CG tone to determine when to mute the receiver in order to eliminate the squelch tail (STE). In addition, the T/R Shelf generates a 135-degree phase shift in the CG tone, and continues to send the phase shifted CG tone for 160 milliseconds after the transmitter is unkeyed (PTT button released).

Digital Channel Guard

The T/R Shelf also encodes and decodes digital CG. There are 83 digital codes available. Any of the digital codes can be assigned to any of the transmit or receive channels. A list of the octal codes (and their equivalent codes) is shown in Table 1.

The encoding function provides continuous, repetitive digital word modulation to the transmitter. The decode function controls receiver muting to eliminate all calls that are not digitally coded with the assigned CG code.

BATTERY ALARM TONE

Whenever the station is operating on battery stand-by power, the station power supply applies a battery standby signal to the T/R Shelf. The T/R Shelf then generates a 1200 Hz alert tone and adds it to the transmit audio for transmission. The 1200 Hz tone is also sent down the line to any remote control unit in the system.

NOTE

The station has to be keyed or unsquelched for the alarm tone to be heard at the remote control unit.

The repetition rate and on-time rate are programmable through the personality **EEPROM**. The repetition rate sets the time from the beginning of a tone to the beginning of the next tone, and is programmable from zero (0) to 25 seconds in increments of 1 second. The on-time rate sets the duration of the tone burst, and is programmable from zero (0) to one (1) second in increments of 0.1 second.

If the battery standby signal is not connected to the input of the T/R Shelf, the option must be disabled in the T/R Shelf personality to prevent alarm tones from being generated.

MORSE CODE ID

Morse code identification can be programmed into the T/R Shelf personality. The code is transmitted according to FCC publication 47 CFR, Chapter 1 (10-1-87 Edition), paragraph 90.425 for non-trunked communications; and paragraph 90.380 for trunked communications. Up to 12 characters in only one word can be programmed into the T/R Shelf. This feature can be enabled or disabled in the programming, as required.

A 5 second transmitter quiet time is required before starting the Morse code sequence. A 1000 Hz tone is used, with an element time of 50 milliseconds for 20 word-per-minute transmissions. The Morse code ID is sent every interval time. The interval time is programmable, but defaults to every thirty minutes. The ID may be programmed to be transmitted either with or without Channel Guard.

DC REMOTE CONTROL

The T/R Shelf can be remotely controlled by DC control currents. A Block Diagram of a T/R Shelf with a remote interface is shown in Figure 2. Refer to the INSTALLATION section as listed in the Table of Contents of this Manual for the different methods of connecting a DC remote control unit to the T/R Shelf.

A DC remote control unit can initiate a transmission, listen to received audio, and select or deselect certain T/R Shelf functions. The different current levels used and the control functions are described below.

Table 1 - Primary and Equivalent Octal Codes

PRIM. CODE	EQUIVALENT CODE	PRIM. CODE	EQUIVALENT CODE	PRIM. CODE	EQUIVALENT CODE
023	340, 766	131	572, 702	235	611, 671, 723
025		132	605, 634, 714	236	251, 704, 742
026	566	133	413, 620	237	464, 642, 772
031	374, 643	134	273	243	267, 342
032		135	205, 610	246	542, 653
036	137	136	502, 712	252	661
037	560, 627	142	174, 270	255	425
043	355	143	333	262	316, 431, 730
047	375, 707	144	466, 666	266	655
051	520, 771	145	525	271	427, 510, 762
053		147	303, 306, 761	274	652
054	405, 675	150	256, 703	276	326, 432
056	465, 656	152	366, 415	307	362, 565
057	172	153	606, 630	311	330, 456, 561
060	116, 737	155	233, 660	312	515, 663, 743
065	301	156	517, 741	315	321, 673
066	734	157	322, 503	317	546, 614, 751
067	516, 720	161	345, 532	324	343, 570
071	603, 717, 746	162	416, 553	325	550, 626
072	470, 701	163	460, 607, 654	331	372, 507
073	640	164	207, 732	332	433, 552
074	360, 721	165	354	344	471, 664, 715
075	501, 624	171	265, 426	346	616, 635, 724
076	203, 754	176	244, 417	351	353, 435
104	226, 557	212	253	356	521
107	365	213	263, 736	363	436, 443, 444, 662
114	327, 615	217	371, 453, 530	446	467, 511, 672
115	534, 674	222	445, 457, 575	447	473, 474, 731, 744
117	411, 756	223	350, 475, 750	452	524, 765
122	535	224	313, 506, 574	454	513, 545, 564
123	632, 657	225	536	455	533, 551
125	173	227	261, 567	462	472, 623, 725
127	412, 441, 711	231	504, 631, 636, 745	523	647, 726
130	364, 641	234	423, 563, 621, 713	526	562, 645
131	572, 702	245	370, 554		

Control Current Signalling

Control current signalling from a DC remote control unit consists of applying different current levels on a wire pair having DC continuity. The six control current levels used in the remote T/R Shelf are:

- 11 milliamperes
- 6 milliamperes
- -2.5 milliamperes
- 0 milliamperes

Station functions which can be controlled by these control currents are:

- Repeater Disable
- Channel Guard Monitor
- Transmit Frequency Selection
- Receive Frequency Selection
- Scan
- Receiver Selection (Auxiliary Receiver selection)

See Table 2 for a list of DC Control Currents and their corresponding functions.

Transmit Functions

When a transmit frequency select control current is received from a remote, the T/R Shelf initiates a transmission of received line audio on the selected transmit frequency. The transmission continues until the transmit control current is no longer detected.

Channel Guard Monitor

When the CG Monitor function control current is received from a remote control unit, the T/R Shelf does not require the correct CG before unmuting the receiver.

The requirement for correct CG tones to initiate a repeat of received signals is **not** removed when the CG Monitor function is activated. This allows received audio to be passed down the line to a remote control unit regardless of CG content, allowing the remote operator to monitor all frequency activity prior to transmitting. This function is automatically reset when a remote control unit keys the transmitter.

Repeat Function

When the T/R Shelf receives a repeater disable control current, it disables the repeater function if the repeater function was previously enabled. If the repeater functiomnhas been disabled, a repeater disable control current will enable the repeater. When the repeat function is enabled, the base station re-transmits the

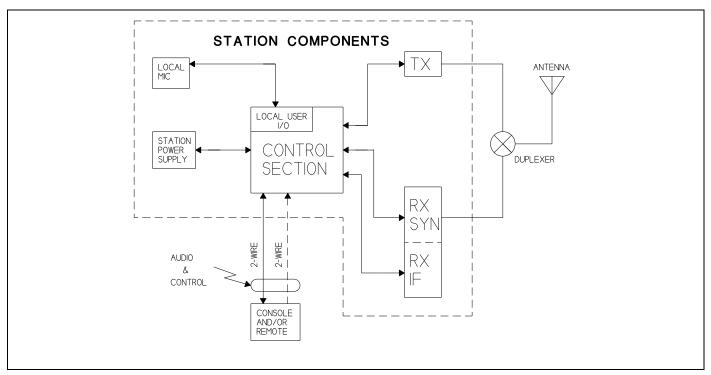


Figure 2 - DC/Tone Remote, DC/Tone Remote/Repeat

Table 2 - DC Control Currents and Functions

FUNCTION		CONTROL CURRENT IN MILLIAMPS				
1 FREQ TX 1 FREQ RX	-11	-6	-2.5	0 RECEIVE	+6 TRANSMIT	+11
2 FREQ TX 2 FREQ RX		RX-F2		RX-F1	TX-F1	TX-F2
2 FREQ TX 2 FREQ RX WITH SCAN	RX-F2	RX-F1		SCAN	TX-F1	TX-F2
1 FREQ TX 1 FREQ RX WITH CHANNEL GUARD DISABLE			CG DISABLE	RECEIVE WITH CG	TRANSMIT	
2 FREQ TX 2 FREQ RX WITH CHANNEL GUARD DISABLE	RX-F2 CG DISABLE	RX-F2 WITH CG	RX-F1 CG DISABLE	RX-F1 WITH CG	TX-F1	TX-F2
REPEATER DISABLE		REPEATER DISABLE		RECEIVE	TRANSMIT	
REPEATER DISABLE & CHANNEL GUARD DISABLE	REPEATER DISABLE & CG DISABLE	REPEATER DISABLE	CG DISABLE	RECEIVE WITH CG	TRANSMIT	
1 FREQ TX 2 SEPARATE RECEIVERS (AUX RX)	RX-F2	RX-F1		RX-F1 & RX-F2	TRANSMIT	
2 FREQ TX 2 SEPARATE RECEIVERS (AUX RX)	RX-F2	RX-F1		RX-F1 & RX-F2	TX-F1	TX-F2

received (incoming) signal when a valid CG tone or code is present. When the repeat function is disabled, the T/R Shelf does not initiate transmission of received signals.

Auxiliary Receiver

With an auxiliary receiver connected to the T/R Shelf using wiring harness 19B802398P1, audio from this auxiliary (second) receiver may be routed to the telephone line connecting a remote control unit. A separate 600 ohm balanced output is also provided by the second receiver for applications requiring audio at a second remote location.

A remote control unit may apply DC control currents to select which receiver audio is heard at the remote as listed below:

- 1. Main receiver audio only,
- 2. Auxiliary receiver audio only, or
- 3. Both main receiver and auxiliary receiver audio.

For Channel Guard applications, CG Monitor monitors the traffic on the auxiliary receiver frequency and the main receiver frequency.

TONE REMOTE CONTROL

In tone remote applications, the T/R Shelf uses its Digital Signal Processor (DSP) to interface with a tone remote control unit through a two- or four-wire phone line. A Block Diagram of the T/R Shelf remote interface is shown in Figure 2.

A tone remote control unit can initiate a transmission, listen to received audio, and select or deselect T/R Shelf functions. Functions selected by the different available tones can be programmed so that a 1450 Hz "**Function**" tone, for example, can be used for different functions in different control shelves.

Signalling from a tone remote control unit consists of a high level "Secur-it" tone, followed by the appro-priate medium level "Function" tone (as well as a "Hold" tone if the transmitter is keyed). The tone control sequence is shown in Figure 3.

The "**Secur-it**" tone is a +10 dB, 2175 Hz tone that is present for 125 milliseconds. The "**Secur-it**" tone is followed by a 40 millisecond, 0 dB "**Function**" tone. The "**Function**" tone can be followed by a -20 dB, 2175 Hz "**Hold**" tone if PTT is selected. The "**Hold**" tone is present as long as the PTT is pressed.

Function Tones

The frequency of the "**Function**" tone determines the function selected by a tone remote control unit. "**Function**" tones range from 1050 Hz to 2050 Hz, and are spaced 100 Hz apart.

Tone Remote Functions

Station functions that can be controlled by tone signalling from a remote control unit are:

- Repeater Enable (disable)
- Channel Guard Decode Enable (disable)
- Channel Guard Monitor
- Transmit Frequency Selection
- Receive Frequency Selection
- Scan
- Receiver Selection (Auxiliary Receiver selection)
- Auxiliary Output Enable (disable) (Auxiliary Control)

See Table 3 for a list of "**Function**" tones and their corresponding function.

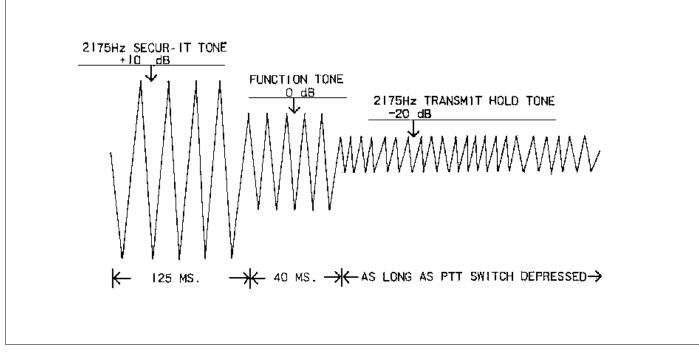


Figure 3 - Tone Control Sequence

Table 3 - Tone Control Function and Frequency

FUNCTION	TONE
RX Channel Guard Disable (Reset by PTT)	2050 Hz
TX-Freq. No. 1	1950 Hz
TX-Freq. No. 2	1850 Hz
TX-Freq. No. 1 or Receiver No. 1	1750 Hz
TX-Freq. No. 2 or Receiver No. 2	1650 Hz
Channel Guard Decode On or Repeater Enable*	1550 Hz
Channel Guard Decode Off or Repeater Disable*	1450 Hz
TX-Freq. No. 3 or Aux. Function 1 On	1350 Hz
TX-Freq. No. 4 or Aux. Function 1 Off	1250 Hz
Repeater Enable*	1150 Hz
Repeater Disable* or Scan or Simultaneous Monitor	1050 Hz
*D	

^{*} Repeater Enable (disable) is 1150/1050 only when Channel Guard On/Off is present.

Repeat Enable (disable)

When a repeater enable (disable) "**Function**" tone is received on the line from a remote, the T/R Shelf enables (disables) the repeater function. When the repeat function is disabled, the T/R Shelf will not initiate a re-transmission of received signals. However, the audio is still routed to the remote control unit if the transmitter is not keyed.

Channel Guard Monitor

When a CG Monitor "Function" tone is received from a remote control unit, received audio is sent down the line to a remote control unit and the local speaker regardless of CG content. This allows the operator to monitor all frequency activity prior to transmitting. The requirement for a correct CG tone or code to initiate a repeat of received signals is **NOT** removed.

The monitor function is disabled when a remote control unit keys the transmitter.

Channel Guard Enable (disable)

This function is the same as CG Monitor except that the Monitor function is not deselected by a remote PTT. The Monitor function is deselected only by a CG enable "**Function**" tone.

One-Four Frequencies

The T/R shelf receives "**Function**" tones to select one of four channels (frequencies). The Control Section then loads the Tx and Rx synthesizers with a 32 bit serial word that contains the appropriate frequency information.

Transmit Functions

When a transmit frequency select "**Function**" tone is received from a remote, the T/R Shelf filters out the "**Hold**" tone and initiates a transmission of received line audio. The transmission continues until the "**Hold**" tone is no longer detected.

Intercom Function

The T/R Shelf intercom function allows a service technician at the station to communicate with a remote control unit without keying the transmitter.

When no valid signal is present, the T/R Shelf routes the line audio to the local speaker. A remote control unit can then select the intercom function and send audio (no control tones) over the line. This remote audio will be heard only at the station speaker, and will not be transmitted.

The service technician can communicate with the remote control unit by placing the T/R Shelf transmit disable switch in the disable position. The local microphone at the station can then be keyed and audio sent <u>only</u> down the remote lines to the remote control unit. This audio is not transmitted by the station.

While in the intercom mode, receiver audio will continue to have priority over line audio to the local speaker, and local (station) mic audio will have priority over receiver audio to the remote line.

Auxiliary Receiver

A remote control unit can control the state of the **RX 2 MUTE** output line using "**Function**" tones. The "**Function**" tones allow the T/R Shelf to send the main receiver audio only, the auxiliary receiver audio only, or both the main receiver and auxiliary receiver audio output to a remote control unit.

Scan Function

The scan function allows the user to scan multiple frequencies using the station receiver.

When no signal is being received on any channel, the scan function sequentially selects and monitors each channel. If a signal is detected, the T/R Shelf locks onto the channel for the duration of the message and discontinues scanning. The default sample time for each channel is 80 milliseconds. A channel with the receiver unsquelched will be locked on.

REPEAT FUNCTION

The T/R Shelf performs a basic repeat function in which received signals are re-transmitted after filtering and level adjustments. Figure 4 is a block diagram of the T/R Shelf interface in a repeat only system.

Received signals are applied to the $VOL/SQ\ HI$ line from the receiver, and are routed to the transmitter on the $TX\ AUDIO\ OUT$ line for re-transmission. If Channel Guard is present, the received Channel Guard information is filtered out and the transmit Channel Guard , if enabled, is encoded and summed with received audio and then re-transmitted.

Some repeater stations have timing restraints mandated by the FCC. Two timing circuits are available for use in these applications. The timing circuits are a Carrier Control Timer (CCT), and a Drop-out Delay Timer (DOD).

Carrier Control Timer

The Carrier Control Timer (CCT) limits the time the station transmitter remains keyed for a single transmission. The time limit can be preprogrammed from zero (0) seconds to 600 seconds (10 minutes) in one-second steps. All control shelves equipped with the CCT are shipped with the timer programmed for three minutes

— NOTE -

Timing restraints apply to local and remote transmissions as well as the repeat function. Local, remote, and repeat PTT timers are each programmed separately and are completely independent timers.

The timing cycle begins when the transmitter is keyed by pressing the PTT button on the local microphone, or the PTT button of a remote or mobile radio generating the signal, activating the repeater. If the station is equipped with Channel Guard, the remote signal must contain the proper Channel Guard tone. Timing ends and the timer is reset when the transmitter is unkeyed.

If the timing limit is exceeded, the T/R Shelf will turn off the transmitter through the **ANT RELAY** and **TX OSC CONTROL** outputs. The Carrier Control Timer function is reset whenever a PTT switch is released, whether it is at the remote control unit or other keying source.

Whenever the timing cycle is exceeded by a repeat PTT, the stations will not activate another repeat until the PTT is released from any source. However, the T/R Shelf will re-transmit from another source (such as a remote control unit) whenever the time limit has expired on a repeat PTT.

Drop-Out Delay Timer

In repeater applications, the Drop-Out Delay Timer (DOD) is designed to decrease the number of transmitter on/off cycles. This is achieved by keeping the transmitter keyed for a predetermined period after a repeat transmission has ended. This period can be programmed for zero (0) to ten (10) seconds in 100-millisecond (0.1 second) steps. All stations equipped with the DOD are shipped from the factory with the timer set for three seconds.

— **NOTE** ———

The Drop-Out Delay Timer is used primarily for repeater functions. Other transmissions, including those originating from the local microphone, typically do not use a DOD timer.

The timer starts whenever a repeat transmission ends. The transmitter is not de-energized through the TX OSC CONTROL and ANT RELAY outputs until the timer runs out.

If a new transmission is initiated before the timer runs out, the transmitter remains energized and the new transmission completed. If no new transmission is initiated, the transmitter will remain on until the DOD times out.

PROGRAMMING

All input and output levels to/from the Control Section are adjusted by electronic potentiometers. These potentiometers are adjusted by the Utility Handset SPK9024 connected to the Mic/Handset port or by a personal computer (PC) connected to the Programming/Diagnostic port, both accessible from the front of the T/R shelf.

The T/R shelf contains an Electrically Erasable Programmable Read Only Memory (EEPROM) whose contents define the personality of the station. The contents of this EEPROM may only be modified through the handset or by running the appropriate software and a PC connected to the programming port.

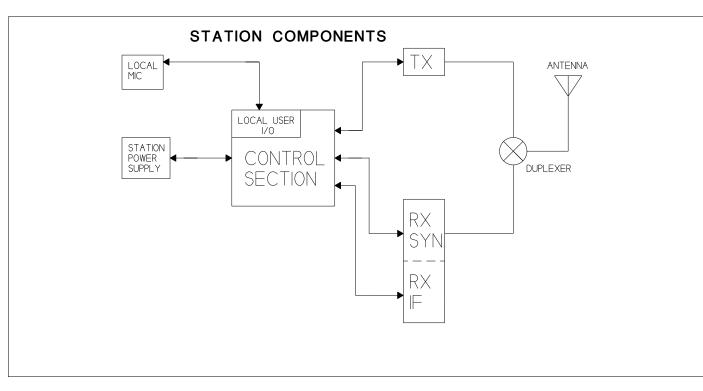


Figure 4 - Repeat Only

For complete instructions on the station personality definitions see TQ-3353, Programming Manual and software for MASTR III. If the T/R Shelf has not been programmed or has lost its personality, it must be reprogrammed.

PROGRAMMABLE FEATURES

The T/R Shelf personality programming parameters include:

Enable (disable) Parameters

- Channel Guard Encode (per channel)
- Channel Guard Decode (per channel)
- STE (Encode) (per channel)
- Repeat Function (per channel)
- CCT (per channel)
- DOD Timer (per channel)
- Simplex/Duplex (per channel)
- Auxiliary Control
- Scan
- Battery Alarm Tone
- DC Remote

- Tone Remote
- 2nd Receiver Simplex (per channel)
- Morse code parameters (per channel)
- Circulator
- Logic Standby
- Intercom
- External Reference Oscillator
- Sidetone

System Parameters

- DSP Line In
- DSP Line Cancellation
- DSP Compressor Gain
- Two/Four Wire Remote
- Battery Alarm Tone "on time"
- Battery Alarm Tone
- Battery Alarm Repetition Rate
- Morse Code ID (per channel)
- Morse Code ID Interval (per channel)

- Morse Code Wait Time (per channel)
- Morse Code Transmit Level
- Line Out level
- Line In level
- Transmit Audio output level (per channel)
- Channel Guard output level (per channel)
- Channel Guard Encode Frequency (per channel)
- Channel Guard Decode Frequency (per channel)
- Type 90 Decode
- Carrier Control Time (per channel)
- Drop-out Delay Time (per channel)
- DTMF Decode
- Transmitter Frequency (per channel)
- Receiver Frequency (per channel)
- Repeat Audio Output Level
- Reference Frequency
- DSP Repeater Gain
- DSP Compressor Threshold
- PA Power Level (per channel)
- Auxiliary 1 Relay Power Up Default State

SYSTEM INTERFACES

This section contains a description of the interfaces to the Control section, transmitter, receiver and operator. Also, interfaces to the GETC board, programming/diagnostics serial port and other miscellaneous interconnections are described.

TRANSMITTER SYNTHESIZER

TRANSMIT AUDIO HI - The Control Section drives the Transmit Synthesizer with this AC coupled signal. The backplane provides the necessary connection.

TRANSMIT AUDIO LO - The common line for the **TRANSMIT AUDIO HI** output. It is grounded at the T/R Shelf.

TX OSC CONTROL (PA KEY) - The Control Section generates this digital control signal which keys the RF Power Amplifier. The backplane routes this signal to the PA via connections on the Interface Board.

SERIAL_CLK - This digital signal provides a clock for loading the Tx and Rx Synthesizers. This signal originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

RXF4/AUX2 - This digital signal provides DATA for loading the Tx and Rx Synthesizers. RXF4/AUX2 originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

RXF2 - This digital signal provides an ENABLE pulse to the Tx and Rx Synthesizers. RXF2 originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

TXF1,TXF2,RXF1 - These digital signals provide addresses A0, A1, and A2 for the Tx and Rx Synthesizers. These signals originate at the System Module and are routed by the backplane to the RF Section and the Interface Board.

RECEIVER SYNTHESIZER

SERIAL_CLK - This digital signal provides a clock for loading the Tx and Rx Synthesizers. This signal originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

RXF4/AUX2 - This digital signal provides data for loading the Tx and Rx Synthesizers. RXF4/AUX2 originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

RXF2 - This digital signal provides an enable pulse to the Tx and Rx Synthesizers. RXF2 originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

TXF1,TXF2,RXF1 - These digital signals provide addresses A0, A1, and A2 for the Tx and Rx Synthesizers. These signals originate at the System Module and are routed by the backplane to the RF Section and the Interface Board.

INT_OSC - This digital signal is an output from the Interface Board. The Receiver Synthesizer uses to signal to select either the internal or external reference oscillator. A logic high selects the internal reference oscillator.

RECEIVER INTERFACES

VOL SQ HI - Audio from the station receiver is output on this line. The audio range is from 0 to 1.5 Volts rms. This output can consist of audio, Channel Guard, or data.

VOL SQ LO - This is the common line for the **VOL SQ HI** input. It is grounded in the T/R Shelf.

CAS (Carrier Activity Sensor) - A TTL high on this input indicates an on-frequency signal is being received. A TTL low on this input indicates an on-frequency signal is <u>not</u> being received. This input is independent of the presence of a proper CG tone.

RX 1 MUTE - The T/R Shelf presents a low (\leq 0.3 Vdc @ \leq 30 milliamperes) on this open collector output when the audio from receiver one is muted.

AUX RX MUTE - The T/R Shelf presents a low (≤ 0.3 Vdc @ ≤ 30 milliamperes) on this open collector output when the audio from receiver two is muted. This output is only used when an auxiliary receiver is connected to the T/R Shelf through the **SECOND RCVR** input.

INTERCOM AUDIO - If an on-frequency signal is present, and the receiver is not muted (**RX 1 MUTE** = open collector), de-emphasized audio with no CG present is routed to this output.

If the receiver is muted, the local microphone not keyed, and no RF signal, "Secur-it" tone or "Function" tone is present, audio received from the line is routed to the INTERCOM AUDIO output.

RUS IN - This RUS output from the second receiver indicates to the T/R shelf that the second receiver is unsquelched with the proper channel guard.

CG MON - This output from the T/R shelf to the second receiver causes the second receiver to drive its' audio output whenever it receives an on-frequency signal of sufficient strength to unsquelch the receiver (RUS is active).

2ND RCVR - This T/R shelf input is driven by the second receiver's line driver monitor output. Using this output instead of the balanced 600 ohm output allows the audio from the second receiver to drive both the remote line pair in addition to a seperate line pair at another remote site.

GETC INTERFACES

RCVR VOL/SQ HI - Receiver audio is routed to the GETC for recovery of 9600 bps digital data and recovery of 150 bps subaudible signalling data.

LINE A, LINE B - This 600 ohm balanced pair from the GETC connects to the T/R Shelf transmit pair telephone line. This provides a 9600 bps downlink from the GETC to a second remote GETC.

DPLX LINE A, DPLX LINE B - This 600 ohm balanced pair to the GETC connects to the T/R Shelf receive pair telephone line. This provides a 9600 bps uplink from a remote GETC to the station's GETC.

DELAYED PTT IN - When active this GETC output keys the station's transmitter.

TX CG EN - This GETC output is only used in Voice Guard End-to-End stations. When a guarded transmission is done, the GETC pulses the 1950 DIS line. The station then mutes the 1950 Hz voting tone. The GETC should then activate the TX CG EN line. If it does not activate it within a second of pulsing the 1950 DIS, the voting tone will come back on.

DETECT DIS - This T/R Shelf input from the GETC signals the T/R shelf whether receive audio or high speed data should be transmitted.

REPEAT PTT IN - This GETC output causes the station to perform a RUS PTT in Voice Guard and is used in back-to-back repeater applications to key the transmitter.

 \boldsymbol{VG} \boldsymbol{PTT} \boldsymbol{IN} - This open collector output from the T/R shelf is not used.

REPEAT PTT OUT - This T/R shelf output is true when the station is repeating or doing a guarded remote PTT in Voice Guard End-to-End.

GETC DATA - This T/R shelf input from the GETC provides a path to the transmitter for high speed data transmission.

COMB PTT OUT - This T/R shelf output signals to the GETC that the transmitter is keyed by any PTT except for Morse Code.

LOCAL PTT - This signal is an input to the T/R shelf and the GETC that indicates that PTT on the local mic port is true.

REMOTE PTT OUT - This T/R shelf output is true when a remote PTT function is being executed. However, turning on the REM PTT switch on the front of the System Module will not activate this output.

 \boldsymbol{CAS} - This T/R shelf output is driven true when the receiver is unsquelched.

COMB PTT IN - This T/R shelf input is currently not used.

RUS IN - This T/R shelf input is driven true by the GETC's RUS OUT or by an auxiliary receiver when it becomes unsquelched

CG MONITOR - This T/R shelf output signals to the GETC that the station is operating in Channel Guard Monitor state.

EXT LSD - This T/R shelf input provides a path for subaudible signalling data from the GETC to the transmitter.

1950 DIS - This T/R shelf input from the GETC signals the T/R shelf to mute the 1950 Hz voting tone in Voice Guard End-to-End applications. In Voice Guard Encrypt/Decrypt stations, the 1950 DIS is used to toggle the station between guarded and clear modes.

RX 1 MUTE (SYS RUS OUT) - This T/R shelf output is true when CAS is true along with a valid CG or CG Monitor. In the case of a simplex station, this signal is false during a transmit.

VG MIC HI - This T/R shelf audio output provides a path from the station's mic to the VG-9600 used in Voice Guard applications.

SYS VOL SQ HI - This signal is normally hardwired to RCVR VOL SQ HI and is the signal routed to the System Module in the T/R shelf. In Voice Guard Encryp/Decrypt applications, the printed wire trace JP1 on the T/R shelf backplane is cut and SYS VOL SQ HI is driven by the VG-9600 Module.

VG PTT OUT - This T/R shelf output is true during a remote or local PTT, morse code ID, or drop out delay. Active for Voice Guard Encrypt/Decrypt applications only.

VG ALERT - This T/R shelf audio input from the VG-9600 provides a path for an alert tone to be heard at the station's local speaker and on the remote line.

VG SQ DSBL - This T/R shelf input is used in Voice Guard Encrypt/Decrypt repeater applications. The VG-9600 activates this input when it detects a valid key.

TXF3/DATA (VG CLR SEL) - This T/R shelf output signals the VG-9600 Module that clear voice is being transmitted. This is done only in Voice Guard Encrypt/Decrypt stations.

TXF4/ENBL (VG GRD SEL) - This T/R shelf output signals the VG-9600 Module that guarded (encrypted) voice is being transmitted. This is done only in Voice Guard Encrypt/Decrypt stations.

STATION POWER SUPPLY

Power Supply Inputs

13.8VDC (A+) - The station power supply generates a nominal 13.6 Vdc @ 33 Amps, 4 amperes of which are budgeted to the T/R Shelf. 13.8

Vdc is used by the Power Module to provide the regulated voltages for the T/R Shelf. Power is connected to the T/R shelf at the Interface Board which supplies a connector to mate with the station's power supply cable.

Power Supply Module Outputs

+12V VDC - Supplies a +12 Vdc 0.6 Vdc output rated

at 100 milliamperes.

+5 VDC - Supplies a +5 Vdc 0.25 Vdc output rated

at 1000 milliamperes.

-12 VDC - Supplies a -12 Vdc 0.6 Vdc output at 100

milliamperes.

-5 VDC - Supplies a -5 Vdc 0.25 Vdc output rated at

40 milliamperes for T/R Shelf operation

only.

+**5VDC** Supplies a +5 Vdc 0.25 Vdc output rated

at 40 milliamperes for analog circuitry.

CONTROLS AND INDICATORS

Controls

TX DISABLE -

Activating this switch disables the transmitter by turning off the **TX OSC CONTROL** output, and de-energizing the antenna relay. When the transmitter is disabled, the station operates in the intercom mode.

REMOTE PTT -

Activating this switch causes the station to react as though a PTT command has been received from a remote.

CG MONITOR -

This switch selects the station Channel Guard Monitor function. When activated, all CG requirements on the receiver portion of the station are removed. This means all received transmissions will be heard regardless of their CG contents. However, the transmitter still requires the proper CG to be present before it will repeat the audio.

When the CG Monitor function is not activated, the receiver requires the proper CG to be present prior to unmuting and the transmitter requires the proper CG to be present prior to repeating any transmission.

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Indicators

TX -This LED indicates the transmitter is on.

CG MONITOR -This LED indicates the station is in the **CG** MONITOR mode.

TX DISABLE -This LED indicates the T/R Shelf is in the **TX DISABLE** mode, and cannot initiate a

transmission.

PA ALARM -This LED indicates that the PA has detected

an Alarm condition.

Local MIC Interface

LOCAL PTT -

A low (1 volt or less) on this input indicates the local microphone is keyed. The T/R Shelf establishes an audio path from the LOCAL MIC HI input to the LINE and **TX AUDIO** outputs. The T/R Shelf also activates the transmitter oscillator and energizes the antenna relay if the transmitter has not been disabled by the **TX DISABLE** switch.

Normally, **LOCAL PTT** is the highest priority PTT function. Local PTT will preempt all other PTT functions including REPEAT and REMOTE PTT, and will continue to transmit on the currently selected frequency.

LOCAL MIC HI - This input line is DC biased at +12 Vdc by the station T/R Shelf to supply power to the microphone. The microphone AC couples a nominal 100 millivolt rms audio signal into the T/R Shelf's 600 ohm input impedance through this line.

LOCAL MIC LO - This is the AC reference for the LOCAL **MIC HI** audio. It is grounded in the System Module.

GND -

This is the ground supply to the microphone.

Line Interface

LINE -

Receive audio is sent on this output pair to the remote control device. Transmit audio is also received from the remote control on this line pair if the station is configured for two wire audio. The T/R Shelf has an out-

put impedance of 600 ohms, and can drive a 600-ohm line with an adjustable signal level from -19 to 11 dBm.

DUPLEX AUDIO - Transmit audio is received from the remote

control on this wire pair in a four wire

Programming/Diagnostics Serial Port

The programming/diagnostics RS-232 serial port is a multipurpose port that is used to communicate with a personality programmer, automated test equipment during manufacture and other system components. When the Utility Handset is connected, the T/R Shelf must be reset while depressing a volume button. This provides communication from handset to shelf. The handset uses 300 baud data and the PC programmer uses 9600 baud data. After using the handset, toggle the RESET switch on the Power Module to reset the serial port to 9600 baud.

PGM TXD -

The T/R Shelf transmits 300 or 9600 baud RS-232 data on this line. When the Utility Handset is connected to the auxiliary Interface Board, the T/R Shelf must be reset to perform the autobaud function.

PGM RXD -

The T/R Shelf receives 300 or 9600 baud RS-232 data on this line.

Miscellaneous Interfaces

ANT RELAY - This digital output controls the antenna switch in stations so equipped. This output becomes active 15 milliseconds before the PA is keyed to allow time for the mechanical switch to operate before RF power is applied. This output also functions as a Tx Synthesizer power switch control. The Tx Synthesizer oscillator power is switched off when the station is not transmitting and back on when the station is transmitting. The ANT RELAY signal originates at the System Module and is routed by the backplane to the RF Section and the Interface Board.

TX OSC CONTROL (PA KEY) - This digital output gates the RF Power Amplifier on and off. TX OSC CONTROL will not become active unless the Tx Synthesizer indicates it is locked onto the programmed frequency. TX OSC CONTROL originates at the System Module and is routed by the backplane to the RF

MASTR III STATUS - This digital output provides data that indicates the status of the RF modules' fault flags. Each of the RF modules routes its fault status indicator (FLAG 0-FLAG 4) to the Interface Board. This data is then transmitted to the System Module over the same serial bus that loads the synthesizers.

BATT STBY - A high (22-23 Vdc) on this input indicates the station AC power supply is powering the station. A low on this input indicates the battery backup system is supplying power to the station, and that power should be conserved.

When the transmitter is energized and operating from the battery backup system, the T/R Shelf provides an alert tone in the TX AUD output signal. The alert tone is also heard at the remote control unit.

CIRCUIT ANALYSIS

INTERFACE BOARD

Line Interconnect

Audio and control currents from a remote unit are connected to the T/R shelf via TB101 or J101 located on the Interface Board. TB101 is a terminal board and J101 is a 6 pin modular phone jack. TB101 and J101 carry identical pin assignments and are connected in parallel on the pwb.

Line audio from the base station to a remote unit is coupled onto the line via transformer T101 over signals LINE_A and LINE B. T101 is designed for a termination impedance of 600 ohms, which should be provided by the remote unit. In two wire applications, line audio from the remote unit is coupled to the System Module by T101, also, over signals LINE_A and LINE_B. For 4 wire systems, line audio from the remote is coupled to the System Module by transformer T102 over signals DPLX_LINE_A and DPLX_LINE_B. The T/R shelf provides the appropriate 600 ohm line termination for T101 and T102.

DC Control

The current detection electronics indicate the following conditions:

- No current
- 2. Negative current (in excess of 2 mA)
- 3. Current magnitude in excess of 5 mA
- 4. Current magnitude in excess of 10 mA.

Control current passes through a full wave bridge rectifier consisting of diodes D109, D110, D111, and D112. Negative current is directed through U101, D111, through the 6 mA and 12 mA detectors and then out through D110. This negative current causes the photo-transistor of U101 to saturate and thus pull the output DC_CNTRL_3 low.

Positive current flows through D112 of the bridge into the current level sense portion of the circuit which consists of O101, U102, Q102, U103, D113, and D114. At current levels below 6 mA, Q101 and Q102 are "on" and act as current hogs preventing optoisolators U102 and U103 from turning "on". As the current level approaches 6 mA, the voltage developed across the parallel combination of R117 and R118 exceeds the sum of the zener voltage across D113 and the base-emitter voltage of Q102 which forces Q102 into cutoff. With no current flow through Q102, the 6 mA is forced through U103 which turns it on. As the current level continues to increase toward 11 mA, the same switching action occurs with O101 and U102 but at a point set by D114. With 11 mA of current DC_CNTRL_1 is true (active low) as well as DC CNTRL 2 (because 11 mA is greater than 6 mA).

Table 4 - Decoding Truth Table

CONTROL CURRENT (mA)	DC CTRL 1	DC CTRL 2	DC CTRL 3
0	High	High	High
-11	Low	Low	Low
+11	Low	Low	High
-2.5	High	High	Low
-6	High	Low	Low
+6	High	Low	High

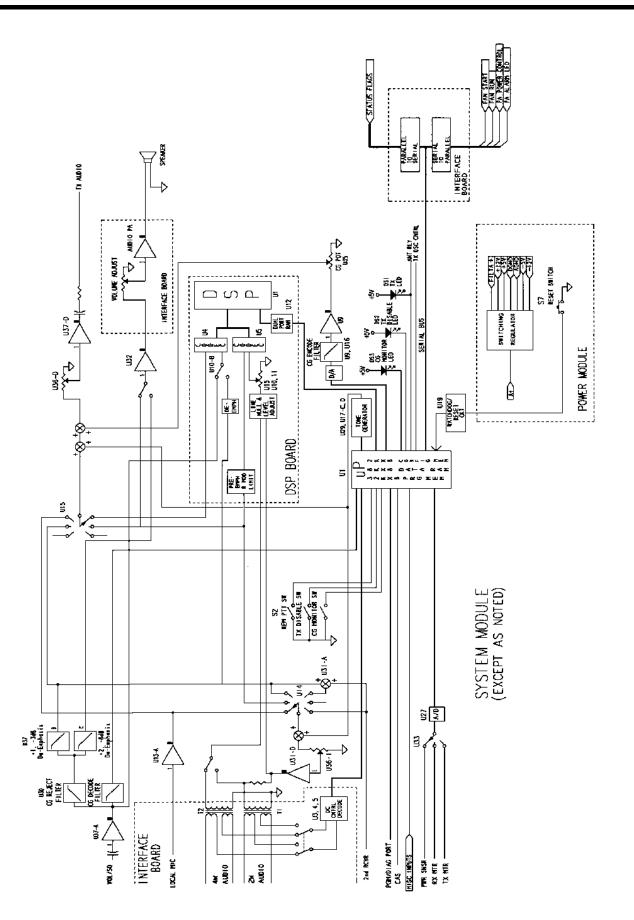
E & M Signalling

For E & M signalling applications, the E & M voltage is applied at TB101 pins 1 and 6 (or J101) which connect to the DC current detection circuit. For 24V signalling, about 8 mA of current is detected as 6 mA by U103 and output on DC_CNTRL_2. For 48V signalling, R105 and R106 should be removed in order to maintain 8 mA of E & M current. Remove P104 and P105 jumpers located on the Interface Board.

Audio Amplifier

Audio power amplifier U104 provides 1 watt of audio to the local service speaker. For convenience, the volume adjust pot R101 is accessible from the front of the station. Resistors R126 and R127 form the gain setting feedback network and C109 and R128 provide compensation for loop stability.

Station Control Section Block Diagram



Serial Communications

The T/R shelf may be connected to a PC through the front panel connector J103 or it may be connected to the Utility Handset, also front panel accessible through J102. Serial TTL data from the handset (KEYPAD_SERIAL) is converted to RS-232 levels by comparator U110.2 and zener diodes D117 and D118. RS-232 data transmitted from the PC (PC_PGM_RXD) and the level converted data from the handset are switched by D101, D103, and R123 to form signal PGM_RXD. This signal is routed to a RS-232 receiver on the System Module where the UART is located.

Serial data transmitted by the T/R shelf to the PC through J103 (PGM_TXD) is RS-232 compatible and requires no processing. Data transmitted by the T/R shelf to the handset is first inverted and level shifted by Q103 then connected to the handset by signal DISPLAY SERIAL.

PA Control Functions

The PA control harness connects to the Interface Board at P103. Flag_4 from the PA is the status bit for indicating a PA fault condition. This signal is connected to shift register U105 where it will be read by the System Module. PA_KEY (TX_OSC_CNTRL) is routed directly from the System Module to the PA and is used to key the PA on and off. PA_PWR_CNTRL is a dc voltage from 4 to 8 volts that sets the power output of the PA. This voltage is developed by electronic potentiometer U108 and is level shifted and buffered by U110.1 and U110.3. Electronic pot U108 is controlled by the System Module using RXF1,TXF2,TXF1 (A2,A1,A0), SERIAL_CLK (Clock), and RXF2 (Enable). The System Module first outputs address bits A2,A1, and A0. Then chooses whether to increment or decrement the pot by the logic state of ENABLE. With this done, the pot voltage is incremented or decremented on pulses from CLOCK.

PA Fan Controller

The DC fan mounted onto the RF Power Amplifier is rated at 12 Vdc and draws about 600 mA of current. Amplifier U110.4 with its associated feedback network along with Q108 and Q109 provide a closed loop current regulator. Since motor torque is proportional to current and motor speed is proportional to torque, we have a fan speed regulator. The circuit provides a constant 600 mA of current for the fan which is drawn from supply A+. This current is maintained constant as A+ varies form 15.6V to below 13.5V (as occurs when the station is transmitting). Two fan speeds are provided, one for start-up (to overcome any friction due to dust, aging, cold, etc.) and another for normal operation. Upon reset or power-up, the System Module sets the fan speed at high (FAN_RUN=1, FAN_START=1), then after 10 seconds sets the speed to normal (FAN_RUN=1, FAN_START=0).

Flag Status Register

Shift register U105 acts as a parallel to serial converter that holds the flag status of the 5 RF modules (TX SYN, RX SYN, RXFE, IF, and PA). U105 is controlled by the System Module using RXF1,TXF2,TXF1 (A2,A1,A0), SERIAL_CLK (Clock), RXF2 (Enable), and M3_STATUS. The System Module first outputs A2,A1, and A0. Then the flag bits are loaded into the shift register by setting ENABLE high followed by a low to high to low pulse on CLOCK. The flag bits are then shifted out (with ENABLE low) on M3_STATUS on succeeding CLOCK pulses. Since the register shifts right (LSB first), the first three bits out are don't cares with the fourth bit out being FLAG_4 and the eighth bit out being FLAG_0.

Output Register

Shift register U106 acts as a serial to parallel converter that expands the System Module's output bits. U105 is controlled by the System Module using RXF1,TXF2,TXF1 (A2,A1,A0), SE-RIAL_CLK (Clock), RXF2 (Enable), and RXF4/AUX2 (DATA). The System Module first outputs A2,A1, and A0. Next, data is presented by the System Module on the signal DATA and is shifted into U106's buffer on the leading edge of CLOCK. After 8 bits of data have been shifted into U106's buffer, ENABLE is driven high and with the next CLOCK pulse, the 8 bits are loaded into U106's output register. The bits are shifted out of the System Module with the MSB first. The bits are defined as:

D7: NOT USED

D6: NOT USED

D5: NOT USED

D4: NOT USED

D3: PA ALARM - drives PA ALARM LED through inverter Q104

D2: FAN_START - input to PA fan current regulator. Provides max fan speed.

D1: FAN_RUN - input to PA fan current regulator. Provides normal fan speed.

D0: INT_OSC - input to Receiver Syntehsizer. Selects internal or external reference.

Microphone/Handset Interface

J102 provides an interface for either Microphone option SXMC3B (19B801398P11) or Utility Handset SPK9024. The Utility Handset not only provides serial communication with the T/R shelf, but also provides audio into MIC_HI from the handset mic and audio from the T/R shelf (INTRCM_AUDIO) to the handset speaker. Thus, when using the handset a local mic and service speaker are not required.

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Circulator Connector

SMA type connector P108 connects the optional circulator power sense signal to the Interface Board and through the backplane to the System Module via signal PWR_SNSR. This signal is sampled by the System Module while the station is transmitting and if the voltage exceeds a predefined limit (indication of fault in antenna system) the System Module will unkey the transmitter and the PA ALARM LED will flash. The transmitter will be disabled until a system reset occurs.

Squelch Adjustment

The station provides for local squelch adjustment through front panel accessible R102 or remote adjustment via handset or remote/diagnostic ports. Signal RCVR_VOL_SQ_HI is connected to two separate voltage divider's formed by R102-R162 and U112-R197. U112 is a digitally programmable potentiometer that is adjusted in a manner similar to U108, the PA power adjustment potentiometer. The output of each divider is connected to analog switch U114. Thus the signal SQUELCH_WIPER, which is fed back to the IF Module, can be selected from either source. It is important to note that if digital (remote) adjustment is selected, the manual adjustment via R102 is disabled.

Relay Options (SXSU3D)

Stations equipped with REV A or higher interface boards, are designed to accept optional relays. The relays include a SOR ($\underline{\underline{S}}$ quelch $\underline{\underline{O}}$ perated $\underline{\underline{R}}$ elay) and two AUX relays, AUX1 and AUX2.

The SOR (K3) contains four form "C" contacts and is rated for 2 amps at 20 Vdc. The relay operates under control of signal RX_1_MUTE, which is derived from CAS, with the coil of K3 being picked up by transistor switch Q110.

AUX1 relay (K1) and AUX2 relay (K2) each contain two form "C" contacts and operate under remote control. When AUX1 function is started via remote control, the system microprocessor sets signal RXF3/AUX1 to logic high which turns on transistor switch Q112 picking up the coil of K1. When AUX2 function is started, the system microprocessor sets bit 4 of output register U106 high, turning on transistor switch Q112 picking up the coil of K2. When the AUX functions are stopped, the control bits are toggled, and the relay coil drops out.

T/R SHELF ALIGNMENT

Instructions for system alignment, including the T/R Shelf, are contained in LBI-38636.

MAINTENANCE

CAUTION



CMOS Integrated Circuit devices used in this equipment can be destroyed by static discharges. Before handling one of these devices, the

service person should discharge himself by touching the case of a bench test instrument that has a 3-prong power cord connected to an outlet with a known good earth ground. When soldering or de-soldering a CMOS device, the soldering iron should also have a 3-prong power cord connected to a outlet with a known good earth ground. A battery operated soldering iron may be used in place of the regular soldering iron.

MASTR III STATION T/R SHELF 19D902839G1					
SYMBOL	PART NO.	DESCRIPTION			
		ASSEMBLIES			
A1		BACKPLANE BOARD 19D902947G1			
		JACKS			
J1 thru J9	19B801587P8	Connector, DIN: 96-position; sim to AMP 650963-4.			
J10	19B801587P11	Connector, DIN: 96-position, right angle mounting; sim to AMP 650895-4.			
		PLUGS			
P1	19A704852P135	Printed Wiring Board Connector.			
P2	19A704852P146	Connector, printed wire, two part: 16 contacts; sim to Dupont Berg 22-12-2164.			
P3	19A704852P148	Connector, printed wire, two part: 16 contacts; sim to Dupont Berg 22-12-2164.			
P4	19A704852P136	Printed Wiring Board Connector.			
P5	19A704852P155	Printed Wiring Board Connector.			
P6	19A704852P145	Connector, printed wire, two part: 16 contacts; sim to Dupont Berg 22-12-2164.			
A2		INTERFACE BOARD 19D902975G1			
		CAPACITORS			
C101 and C102	7486445P5	Electrolytic, non polarized: $4\mu F$ -10 + 100%, 150 VDCW.			
C103	19A700121P106	Ceramic: 0.1 μF ±20%, 50 VDCW.			
C104	19A701225P3	Electrolytic: 220 μF, -10+50%, 25 VDCW.			
C105	19A700121P106	Ceramic: 0.1 µF ±20%, 50 VDCW.			
C106	19A701534P5	Tantalum: 2.2 μF, ±20%, 35 VDCW.			
C107	19A701225P3	Electrolytic: 220 μF, -10+50%, 25 VDCW.			
C108	19A701534P7	Tantalum: 10 μF ±20%, 16 VDCW.			
C109	19A700121P106	Ceramic: 0.1 μF ±20%, 50 VDCW.			
C112 and C113	19A701534P5	Tantalum: 2.2 μF, ±20%, 35 VDCW.			
C114	19A701534P8	Tantalum: 22 μF ±20%, 16 VDCW.			
C115 thru	19A700121P106	Ceramic: 0.1 μF ±20%, 50 VDCW.			
C119 C121	19A701534P6	Tantalum: 4.7 μF ±20%, 35 VDCW.			
C121	19A701534F6	Tantalum: 4.7 μF ±20%, 35 VDCW. Tantalum: 2.2 μF, ±20%, 35 VDCW.			
C124	19A701334F3	Electrolytic: 220 μF -10 +50%, 25 VDCW.			
0120	10/1/0122010				
D101 thru D103	19A700028P1	Silicon: 75 mA, 75 PIV; sim to 1N4148.			
D104	19A703595P10	Optoelectronic LED: Red; sim to HP HLMP-1301-010.			
D105 thru D108	344A3799P9	Zener: 6.8 volts; sim to 1N4736A.			
D109 thru	T324ADP1041	Silicon: Rectifier; sim to 1N4004.			
D112	40470000550	Olling Town on 400 million of the DEVES COLIN			
D113	19A700025P8	Silicon, zener: 400 mW max; sim to BZX55-C6V8.			
D114 D115 and	19A700025P11 T324ADP1041	Silicon, zener: 400 mW max; sim to BZX55-C12. Silicon: Rectifier; sim to 1N4004.			
D116					

SYMBOL	PART NO.	DESCRIPTION
D119	19A115250P1	Silicon, fast recovery, 225 mA, 50 PIV.
D121	19A115250P1	Silicon, fast recovery, 225 mA, 50 PIV.
D123	19A115250P1	Silicon, fast recovery, 225 mA, 50 PIV.
D125	19A700025P4	Silicon, Zener: 400 mA max; sim to BZX55-C3V9.
D126 and D127	19A700028P1	Silicon, 75 mA, 75 PIV; sim to 1N4148.
DIZI		FUSES
F101 thru F106	19A702169P3	Enclosed link, .375 Amps @ 125 volts; sim to Littlefuse 255.375.
J101	344A3288P1	Modular jack: 6-position; sim to AMP 520425-3.
J101 J102	19J706197P3	Connector: 8 contacts; sim to AMP Type 520251-4.
J102	19B209727P43	Connector: Plug.
J103	19A702104P2	Connector: Shorting Jumper, Gold Plated. (Housing
and J105	13/1/02/104/12	Color: White).
J107	19B209727P17	Connector: 25 contacts; sim to AMP 205738-2.
		PLUGS
P101	19B801587P6	Connector, DIN: 96 male contacts; sim to AMP 531796-1.
P102	19A705822P1	Power connector, 4 positions; sim to Amp Cat. #641737-1.
P103	19A704852P32	Printed wire, two part: 6 contacts, sim to Molex 22-29- 2061.
P104 and P105	19A704852P2	Connector: 3 Pin Male Header.
P106	19A700072P28	Printed wire: 2 contacts rated @ 2.5 amps; sim to Molex 22-27-2021.
P107	19A704852P29	Connector; sim to: Molex 22-29-2031.
P108	19A705512P1	Connector, RF SMB Series: sim to AMP No. 221111- 1.
P109	19A700072P28	Printed wire: 2 contacts rated @ 2.5 amps; sim to Molex 22-27-2021.
		TRANSISTORS
Q101 and Q102	19A705953P1	Silicon, NPN: sim to MPSA43.
Q103 and Q104	19A700023P2	Silicon, NPN: sim to 2N3904.
Q108	19A700023P2	Silicon, NPN: sim to 2N3904.
Q109	19A700054P1	Silicon, NPN, 60 w; sim to BD-201.
Q110 thru Q113	19A700023P2	Silicon, NPN: sim to 2N3904.
		RESISTORS
R101	19B235632P1	Variable, conductive plastic: 1000 ohms.
R102	19B235632P2	Variable, conductive plastic: 10K ohms.
R103 and R104	H212CRP022C	Deposited carbon: 22 ohms ±5%, 1/4 w.
R105 and R106	H212CRP910C	Deposited carbon: 1 ohm ±5%, 1/4 w.
R107 thru R110	19A700113P74	Composition: 3.0K ohms ±5%, 1/2 w.
R111	H212CRP410C	Deposited carbon: 100K ohms ±5%, 1/4 w.
R112	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R113 and R114	H212CRP315C	Deposited carbon: 15K ohms ±5%, 1/4 w.

SYMBOL	PART NO.	DESCRIPTION
R115 thru R118	19A700113P74	Composition: 3.0K ohms ±5%, 1/2 w.
R119	H212CRP310C	Deposited carbon: 10K ohms ±5%, 1/4 w.
R120	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R121	H212CRP310C	Deposited carbon: 10K ohms ±5%, 1/4 w.
R122	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R123	H212CRP322C	Deposited carbon: 22K ohms ±5%, 1/4 w.
R124	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R125	H212CRP356C	Deposited carbon: 56K ohms ±5%, 1/4 w.
R126	H212CRP118C	Deposited carbon: 180 ohms ±5%, 1/4 w.
R127	H212CRP015C	Deposited carbon: 15 ohms ±5%, 1/4 w.
R128	H212CRP910C	Deposited carbon: 1 ohm ±5%, 1/4 w.
R129	19A700113P162	Composition: 1.0 ohms ±5%, 1/2 w.
R130	H212CRP410C	Deposited carbon: 100K ohms ±5%, 1/4 w.
R131	H212CRP310C	Deposited carbon: 10K ohms ±5%, 1/4 w.
R132	H212CRP315C	Deposited carbon: 15K ohms ±5%, 1/4 w.
R133	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R134	H212CRP220C	Deposited carbon: 2.0K ohms ±5%, 1/4 watt.
R135	H212CRP310C	Deposited carbon: 10K ohms ±5%, 1/4 w.
R136	H212CRP322C	Deposited carbon: 22K ohms ±5%, 1/4 w.
R137	H212CRP127C	Deposited carbon: 270 ohms ±5%, 1/4 w.
R138	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R139	H212CRP210C	Deposited carbon: 1K ohms ±5%, 1/4 w.
R144 thru R150	H212CRP210C	Deposited carbon: 1K ohms ±5%, 1/4 w.
R151	H212CRP191C	Deposited carbon: 910 ohms ±5%, 1/4 w.
R152	H212CRP110C	Deposited carbon: 100 ohms ±5%, 1/4 w.
R153	H212CRP191C	Deposited carbon: 910 ohms ±5%, 1/4 w.
R154	H212CRP251C	Deposited carbon: 5.1K ohms ±5%, 1/4 w.
R155	H212CRP239C	Deposited carbon: 3.9K ohms ±5%, 1/4 w.
R156	H212CRP310C	Deposited carbon: 10K ohms ±5%, 1/4 w.
R157	H212CRP282C	Deposited carbon: 8.2K ohms ±5%, 1/4 w.
R158	H212CRP147C	Deposited carbon: 470 ohms ±5%, 1/4 w.
R162	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R163	H212CRP315C	Deposited carbon: 15K ohms ±5%, 1/4 w.
R164	H212CRP412C	Deposited carbon: 0.12M ohms $\pm 5\%$, 1/4 w.
R165	H212CRP312C	Deposited carbon: 12K ohms ±5%, 1/4 w.
R166	19A700050P11	Wirewound: $0.68 \text{ ohms } \pm 10\%, 2 \text{ w.}$
R167	H212CRP220C	Deposited carbon: 2.0K ohms ±5%, 1/4 watt.
R168	H212CRP420C	Deposited carbon: 200K ohms ±5%, 1/4 w.
R175 thru R178	H212CRP210C	Deposited carbon: 1K ohms ±5%, 1/4 w.
R179 thru R182	H212CRP147C	Deposited carbon: 470 ohms ±5%, 1/4 w.
R183	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R184	H212CRP312C	Deposited carbon: 12K ohms ±5%, 1/4 w.
R185	H212CRP147C	Deposited carbon: 470 ohms ±5%, 1/4 w.
R186	H212CRP210C	Deposited carbon: 1K ohms ±5%, 1/4 w.
R187	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R188	H212CRP210C	Deposited carbon: 1K ohms ±5%, 1/4 w.

SYMBOL	PART NO.	DESCRIPTION
R191	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R192	H212CRP356C	Deposited carbon: 56K ohms ±5%, 1/4 w.
R193	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R194	H212CRP356C	Deposited carbon: 56K ohms ±5%, 1/4 w.
R195	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R196	H212CRP156C	Deposited carbon: 560 ohms ±5%, 1/4 w.
R197	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R198	H212CRP322C	Deposited carbon: 22K ohms ±5%, 1/4 w.
R199	H212CRP324C	Ceramic film: 24K ohms, .2 w.
R200	H212CRP510C	Deposited carbon: 1M ohms ±5%, 1/4 w.
R201	H212CRP356C	Deposited carbon: 56K ohms ±5%, 1/4 w.
R202	H212CRP247C	Deposited carbon: 4.7K ohms ±5%, 1/4 w.
R203 thru R205	H212CRP051C	Ceramic film: 51 ohms, .2 w.
R206	H212CRP147C	Deposited carbon: 470 ohms ±5%, 1/4 w.
		VARSITORS
SG101 thru SG106	19A701783P3	Arrester, electrical surge (MOV): sim to V150La20A.
		TRANSFORMERS
T101 and T102	19A705947P2	Audio: 600 ohm impedance.
		TERMINAL BOARDS
TB101	19A705820P5	Terminal Block.
TP1 thru TP3	344A3367P1	Test point.
		INTEGRATED CIRCUITS
U101 thru U103	19A705952P1	Optoisolator; sim to 4N38.
U104	19A701830P1	Linear: Audio AMPLIFIER; sim to TDA 2003.
U105	19A703987P21	Digital: CMOS Shift resister with parallel I/O; sim to 74HC299.
U106	19A703987P24	Digital: CMOS 8-Bit shift register with tri-state outputs; sim to 74HC595
U107	19A704445P1	Digital: CMOS 1-of-8 Decoder/Demulti- plexer; sim to 74HC138.
U108	19A705180P2	Digitally Controlled Potentiometer: 40 - 10K ohms; sim to X9103P.
U109	19A703483P11	Digital: CMOS Quad 2-Input OR Gate; sim to 74HC32.
U110	19A701789P1	Linear: Quad Op Amp; sim to LM324.
U111 U112	19A703483P11 19A705180P2	Digital: CMOS Quad 2-Input OR Gate; sim to 74HC32. Digitally Controlled Potentiometer: 40 - 10K ohms; sim to X9103P.
U113	19A701789P1	Linear: Quad Op Amp; sim to LM324.
U114	19A700029P38	Digital: CMOS Triple 2 Channel Multiplexer.
		SOCKETS
XK1 and XK2	19A700156P9	Socket, IC: 16 Pins, Tin Plated.
XK3A	19A700156P7	Socket, IC: 14 Pins, Tin Plated.
ХК3В	19A700156P7	Socket, IC: 14 Pins, Tin Plated.

SYMBOL	PART NO.	DESCRIPTION
		MISCELLANEOUS
5	19A702917P7	Heat Sink, Transistor: Sim to Thermalloy Cat 6030B-TT.
6	19A702364P308	Machine screw, TORX Drive: No. M3-0.5 x 8.
7	19A700032P5	Lockwasher, internal tooth: No. 3MM.
8	19A700034P4	Nut, hex: No. M3 x 0.5MM.
9	19A705469P1	Insulator Plate, TO-220.
		JACKS
J1 and J2	19A115938P13	Connector, receptacle.
		LOUDSPEAKERS
LS1	344A3136P1	Speaker, permanent magnet.
		MISCELLANEOUS
2	19D902721P1	Chassis.
3	19B801732P1	Speaker cloth.
4	19B801706P1	Knob.
5	19A700032P5	Lockwasher, internal tooth: No. 3MM.
6	19A123224P10	Button plug.
7	19A702381P506	Screw, threaded.
8	19A700034P4	Nut, hex: No. M3 x 0.5MM.

PRODUCTION CHANGES

Changes in the equipment to improve performance or to simplify circuits are identified by a "Revision Letter" which is stamped after the model number of the unit. The revision stamped on the unit includes all previous revisions. Refer to the Parts List for the descriptions of parts affected by these revisions.

REV. A - INTERFACE BOARD 19D902975G1

To add new features to board including SOR and DSP. New board is backward compatible.

Added C125, D119-D127, R132-R134 and R186-R205, Q110-Q113 and I111-U114.

Changed C103, C105, C109, C115-C119, D105-D108, R111, R158, R179-R182 and R185.

C103, C105, C109 and C115-C119 were: Tantalum: 0.1 $\mu F \pm 20\%, 35$ VDCW.

D105-D108 were: 19J706030P2.

R111 was: H212CRP310C - 10K ohms \pm 5%, 1/4 w. R158 was: H212CRP247C - 4.7K ohms \pm 5%, 1/4 w.

R179-R182 were: H212CRP247C - 4.7K ohms ±5%, 1/4 w.

R185 was: H212CRP247C - 10K ohms $\pm 5\%$, 1/4 w.

REV. B - INTERFACE BOARD 19D902975G1

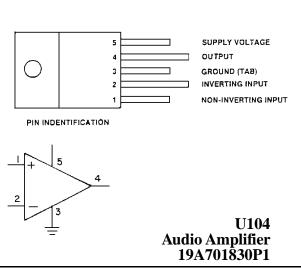
To make RX_1_MUTE Logic Level compatible with GETC level requirement. Added R206 and buffer U111 between RX_1_MUTE and the base of Q110.

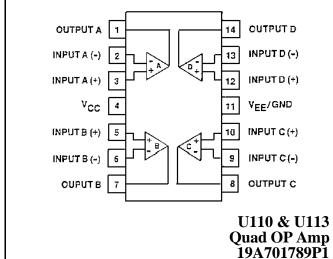
REV. A - BACKPLANE BOARD

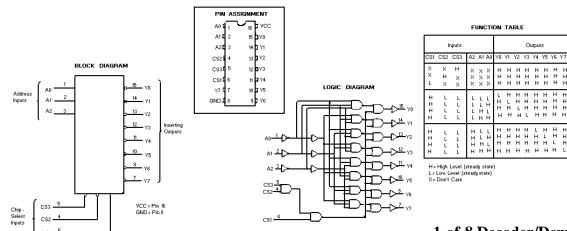
To support GETC and 2nd receiver applications, the printed wire board was changed. Also connectors P1, P3 and P6 changed.

REV. B - BACKPLANE BOARD

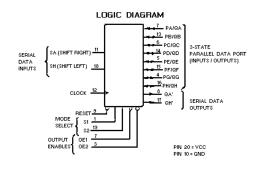
To correct errors on printed wire board, the baud was changed. Connections to J6, J7, J8 and J9, pin 7 were renamed.

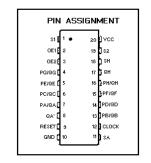






U107 1-of-8 Decoder/Demultiplexer 19A704445P1



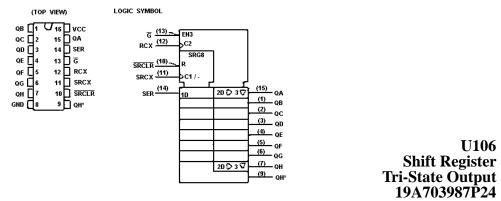


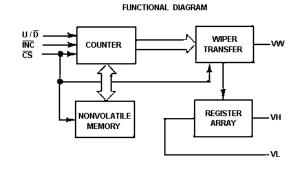
	INPUTS								RESPONSE	
Mode Reset	Sel	ode ect		tput ibles	Clock	Inn	rial uts	PA/QA PB/QB PC/QC PD/QD PE/QE PF/QF PG/OG PH/QH	QA'QH'	
		S2	Sī	OE11	OE21	0.000		DA DH		
Reset		$\times \dashv \mathtt{I}$	L×Н	L L X	L L X	×××	X X	X X	L L L L L L L L L L L L L L L L L L L	
Shift Right	I I I		III	HXL	×	ነነነ	000	X X	-Shift Right: QAthrough QH=Z; DA-FA;FA-FB;etc. -Shift Right: QAthrough QH=Z; DA-FA;FA-FB;etc. -Shift Right: DA-FA=QA;FA-FB=QB;etc.	D QG D QG D QG
Shift Left	пп	ппп		HXL	ХHГ	ነነነ	X X	000	Shift Left: QA through QH = Z ; DH - FH ; FH - FG; etc. Shift Left: QA through QH = Z ; DH - FH ; FH - FG; etc. Shift Left: DH - FH = QH; FH - FG = QG ; etc.	QB D QB D QB D
Parallel Load	I	I	I	Х	Х	١	х	Х	Parallel Load: PN - FN	PA PH
Hold	ннн	L L L	L	H X I	хн	X X X	X X	X X	Hold: QA through QH = Z;FN = FN Hold: QA through QH = Z;FN = FN Hold: QN = QN	PA PH PA PH PA PH

- Z = high impedance
 D = data on serial input
 F = filip flop (see Logic Diagram)
 tv/hen one or both output controls are high the eight input/output terminals are disabled to the high-impedance state;
 however, sequential operation or clearing of the registar is not affected.

U105 **Shift Register Parallel Output** 19A703987P21

U106





PIN CONFIGURATION

HIGH TERMINAL OF POT WIPER TERMINAL OF POT

LOW TERMINAL OF POT

SYSTEM POWER

CHIP SELECT

UP/DOWN CONTROL WIPER MOVEMENT CONTROL

PIN NAME

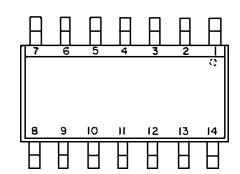
VL VSS VCC

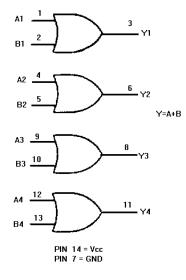
U/D

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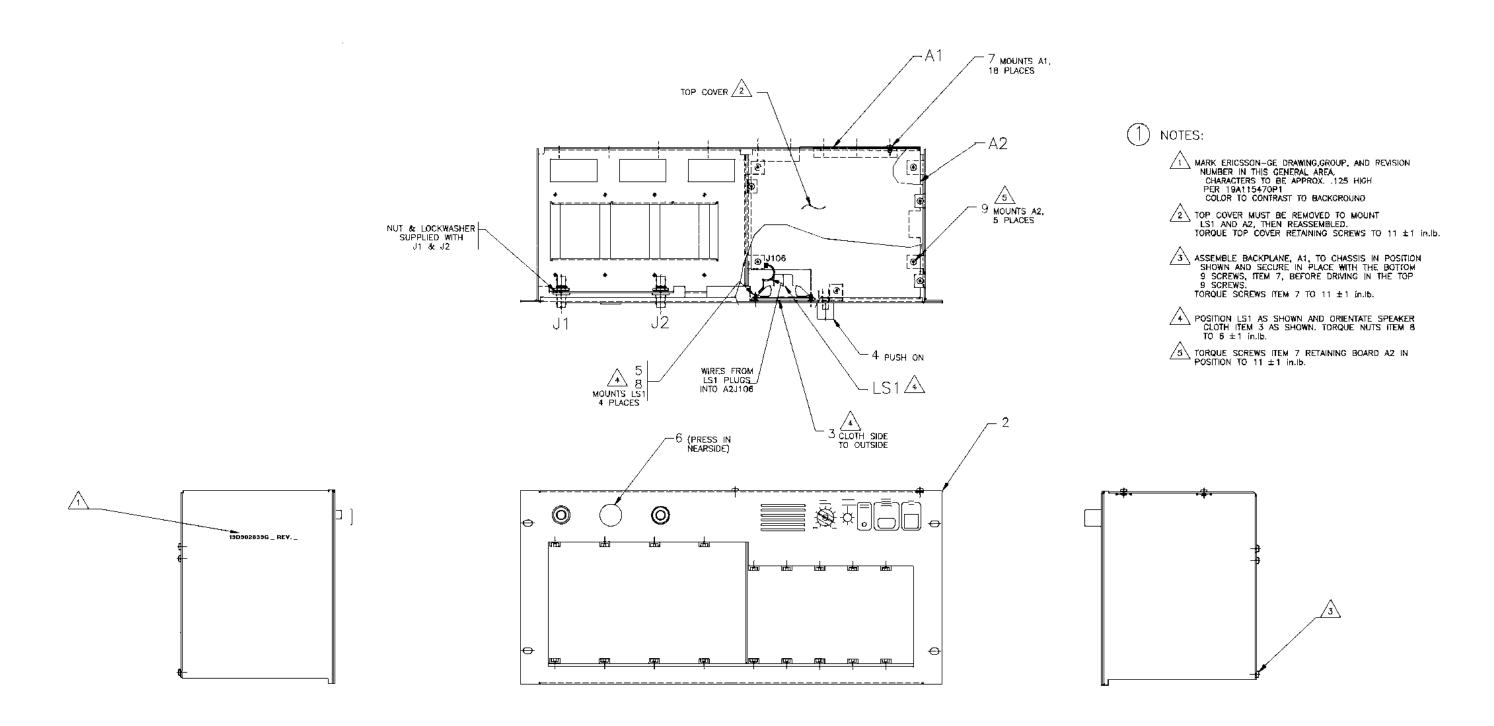
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U108 & U112 **Digital Controlled Potentiometer** 19A705180P2



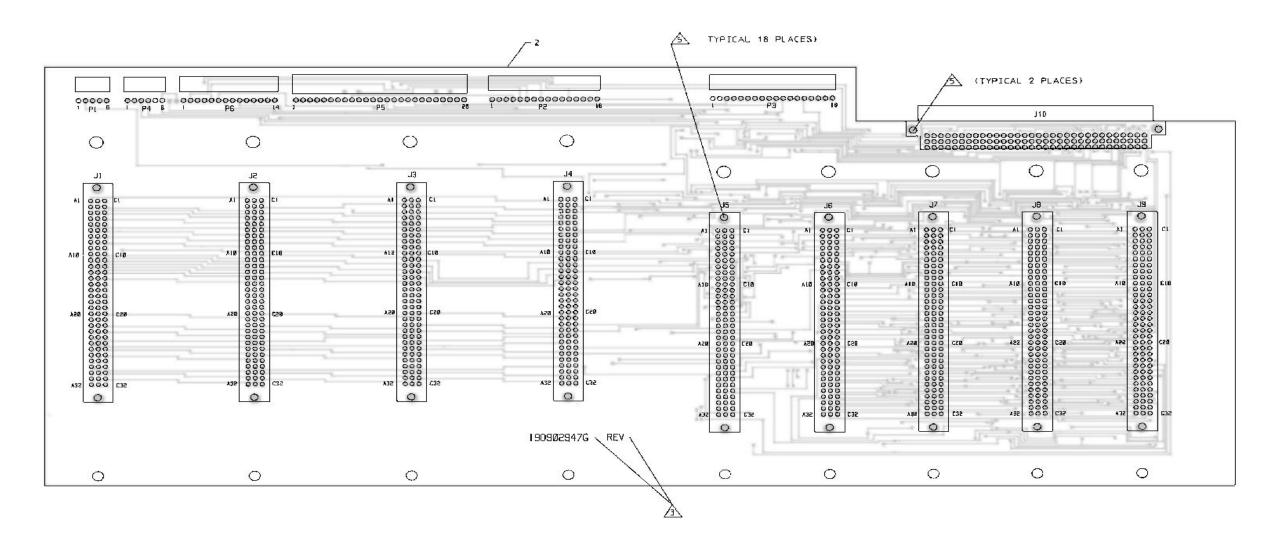


U109 & U111 Quad 2-input or Gate 19A703483P11



MASTR III T/R SHELF 19D902839G1

(19D902839 Sh.1 Rev.2)



(19D902947, Rev. 4) (19D902948, Layer 2, Rev. 5)

T/R SHELF BACKPLANE (A1) 19D902947G1

(19D902949 Sh.1 Rev.2)

LBI-38637 SCHEMATIC DIAGRAM LBI-38637

TX SYNTHESIZER

RX SYNTHESIZER

J1.1 > 1A	—♦ INT_OSC	J1.33 > 1B		J1.65 > 1C	—O AGND
.112 3 2A	—Q AGND	J1.34) 2B		J1.66 2C	→O CLOCK
11.2 N 3A	—Q LAN_1	J1.35) 3B		J1.67 > 3C	—O DATA
11.4 N 4.44	—Q AGND	J1.36) 4B		J1.68 > 4C	—O ENABLE
J1.5 > 5A	—Q LAN_2	J1.37 > 5B		J1.69) 5C	—Ò AGND
J1.6 > 6A	—O AGND	J1.38 > 6B		J1.70 > 6C	— ⊙ A0
J1.7 >-7A	—O AGND	J1.39 7B		J1.713 7C	-OA1
J1.8 > 8A	—•© AGND —•© AGND	J1.40≯ 8B	— ⊙ AGND	J1.72) 8C	OA1 OA2
J1.9 3A	—O AGND —O AGND	J1.41 > 9B	—O AGND —O AGND	J1.73) 9C	—O AGND
J1.10 > 10A	—O AGND —O AGND	J1.42 10B	—O AGND	J1.742 10C	•
J1.113 11A		J1.43) 11B	—O AGND —O AGND	J1.752 11C	—OFLAG_0
J1.12 12A	ORF_SPARE_4	12B	-	J1.763 12C	— O FLAG_1 — O FLAG_2
J1.13 13A	O RF_SPARE_5	J1.44> 13B	— ◇ AGND — ◇ AGND	J1.772 13C	— O FLAG_2 — O FLAG_3
J1.14 > 14A	O ANT_REL	JI.45 >- II.45 > 14B	•	31.77. 14C	
J1.15 > 15A	— O AGND	JI.46)	— Q AGND	J1.78> 15C J1.79> 15C	→O RF_SPARE_3
J1.16 > 16A	— O •12V	J1.47 > 16B	—-Q+12V	J1.79 > 16C	-0 +12∀
J1.16 > 17.A	─○ •13.8VF	J1.48 / 17D	─ 0+13.8VF	01.00 Z	-0 +13.8VF
J1.17) 17A	— O AGND	J1.49 > 10D	— ⊙ AGND	01.01 2	→ AGND
J1.18) 18A	 ♦ +5V	J1.50)	♦ •5٧	J1.82 #	○ + 5V
J1.19 > 1011	—O AGND	300	O AGND	J1.833	→O AGND
J1.203 20A	○ -12V	J1.52) 20B	—Q AGND	01.043	→O BXMTR+
J1.212 21A	—O AGND	J1.537	—O AGND	JI.85 22C	→Q AGND
J1.227 22A		J1.54 > 22B	—O AGND	J1.86> 22C	— ⊘ TX_AUDIO_LO
J1.233 23A		J1.55)	— Ģ AGND	J1.87≯ 24C	→O TX_AUDIO_HI
J1.24 > 24A		J1.56 > 24B	──O AGND	J1882-	→O AGND
J1.25 > 25A		J1.57) 25B	—O AGND	J1.89 > 25C	→ AGND
H 26 \ 26A		J1.58 > 26B	O AGND	J1.90 > 26C	→o cas
J127 <u>27A</u>		J1.59) 27B	—O AGND	J1.91 > 27C	♦ SQ-ARM
J1 28 1 28A	— ⊘ AGND	J1.60 > 28B	—O AGND	J1.92 > 28C	→O AGND
14 AA L 29A	Ø RF_SPARE_2	J1.612 29B	—O AGND	J1.93 > 29C	—♦ AGND
H 20 5 30 M		J1.62) 30B	—O AGND	J1.94> 30C	→ VOLISQ_LO
JI 21 N		H 62 1 510	—O AGND	J1.95> 31C	-O RCVR_VOL/SQ_HI
J1.32> 32A		J1.64) 32B	—Ç AGND	J1.96> 32C	♦ AGND

J2.1 > 1A	— ⊋ INT_OSC	J2 .33) 1B	
J2.2 > 2A	—O AGND	J2.34) 2B	
J2.3 > 3A	O LAN 1	J2.35 > 3B	
J2.4 > 4A	— ⊘ AGND	J2.36 3 4B	
J2.5 > 5A	—Ġ LAN 2	J2.37 > 5B	
J2.6 > 6A	—O AGND	J2.38 > 6B	
J2.7 > 7A	—O AGND	J2.39) 7B	
J2.8 > 8A	—O AGND	J2.40 > 8B	-O AGND
J2.9 > 9A	—O AGND	J2.41 > 9B	—O AGND
J2.10 > 10A	—O AGND	J2.42 3 10B	→O AGND
J2.11) 11A	ORF SPARE 4	J2.43) 11B	→ AGND
J2.I2 > 12A	O RF SPARE 5	J2.44 3-12B	—O AGND
J2.I3 > 13A	O ANT_REL	J2.45 > 13B	—O AGND
J2.I4 14A	—O AGND	J2.46 > 14B	—O AGND
J2.I5 > 15A	—O+12V	J2.47 > 15B	—O +12V
J2.16 > 16A	— Q + 13.8VF	J2.48 3 16B	—O +13.8VF
J2.17 3 17A	—O AGND	J2.49 7 17B	— ⊘ AGND
J2. 18 > 18A	— ○ +5V	J2.50 > 18B	— ⊘ +5V
J2.19 5 19A	—Q AGND	J2.51 3 19B	→O AGND
J2.20 20A	—O -12V	J2.52 > 20B	—O AGND —O AGND
J2.21 > 21A	— Q AGND	J2.53 21B	—O AGND
J2.22 > 22A	₩ AGND	J2.54 > 22B	—O AGND
J2.23 23A		J2.55 > 23B	—O AGND —O AGND
J2.24 > 24A	→ RF_SPARE_6	J2.56 > 24B	-
J2.25 > 25A	TO RE SPARE 1	J2.57 > 25B	— O AGND
J2.26 > 26A	O DE SEMBEL	200	— AGND
J2.27 27A		J2.58 > 27B	— ♦ AGND
J2.28 28A	A ACNID	02.03 / 20D	—O AGND
J2.29 29A	—O AGND	29B	— ⊘ AGND
J2.30 30A	— ⊘ RF_SPARE_2	J2.61 > 30B	—O AGND
J2.30 31A		J2.62)	— ♂ AGND
J2.32 32A			—O AGND
02.32		J2.64 > 32B	—O AGND

J2.65 > 1C	⇔ AGND
J2.66 > 2C	O CLOCK
J2.67 > 3C	O DATA
J2.68 4 C	O ENABLE
J2.69 > 5C	AGND
J2.70 > 6C	-O A0
J2.71) 7C	-O A1
J2.72 > 8C	-O A2
J2.73 > 9C	—O AGND
J2.74> 10C	→O FLAG 0
J2.75> 11C	O FLAG 1
J2.76> 12C	–o FLAG_2
J2.772 13C	O FLAG_3
J2.78> 14C	O RF SPARE 3
J2.79> 15C	−O + 12V
J2.80 > 16C	-0 ⋅ 13.8VF
J2.81 > 17C	−O AGND
J2.82 > 18C	-O ∙ 5V
J2.83 > 19C	O AGND
J2.84) 20C	—O BXMTB+
J2.85 21C	—O AGND
J2.86 22C	—O TX_AUDIO_LO
J2.87 > 23C	- ♦ TX AUDIO HI
J2.88 > 24C	O AGND
J2.89) 25C	→ AGND
J2.90 > 26C	-⇔ CAS
J2.91 > 27C	- ⊘ SQ-ARM
J2.92 > 28C	→ AGND
J2.93 >	◆ AGND
J2.94> 30C	→ VOLISQ_LO
J2.95> 31C	-O RCVR_VOL(SQ_HI
J2.96 > 32C	-O AGND

12.1.> 1A	10 00 1 1B	10
00.1 7 000	J3.33)	J3.65 > Q AGNU
	J3.34)	J3.66 > CLOCK
J3.3) 3A O LAN_1	33.35, 4 D	J3.67 > O DATA
03.4) 50	03.36)	J3.68 > C ENABLE
J3.5 7 CA CANAL2	03,37	J3.69 > O AGIND
J3.6 > Q AGNU	03.30	J3.70 >
J3.7 AGIND	03.38	J3./1 > OC AI
J3.8 > AGIND	J3.40) O MGND	J3,72) O A2
		J3.73) AGNU
J3.10 > 10A	J3.42) 10B AGND	33,74)
J3.11 > 12.4 RF_SPARE_4	J3.43) (O AGND	J3.75) C FLAG_I
	J3,44) (J MGND	J3,76) FLMG_2
J3.13 > 13A ANT_REL	J3.45) 13B O AGND	J3.77) MC
J3.14 > Q AGND	J3.46) (CD	J3.78) AF_SPARE_3
J3.15 > 12 V	J3.47) O +12V	J3,79 > 15C • 12V
J3.16) -17.6 P	J3.48 16B 0 +13.8VF	J3.80 > 16C
J3.17) AGIND	J3.49 > O AGNU	J3.81 > 17C
J3.18) 19 A	J3.50 > O +5V	J3.82)
	J3.51) 19B Q AGND	J3.83 > 19C
J3.20 > 20A O -12V	.12.52.3-20B	J3.84) — O HXMTH+
.13.21). 4 GND	.l3 53 > ∠ IB	J385 → 21C
.13.22 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	J3 54 > 22B AGND	J3.86 23C TX_AUDIO_LO
.12.22 <u>\ 43M</u>	J3.55 > 23B	J3.87 > 23C
J3.24) 24A O RF_SPARE_6	.13.56 > 24B	.13.99.)————• AGND
.13.25 N	J3.57.> ∠9B → AGND	J3.89 ≻ 25C Q AGND
12.26 1200	.13 58 • ^{26B} • • • • • • • • • • • • • • • • • • •	J3.90 > 26C • CAS
.13.27) -10	.13.593 <u>27B</u> AGNID	J3.91 > 27C O SQ-ARM
J3 28 ≯ ^{∠88} − − □ AGND	JORGA ZOB AND AGNID	J3.92 > 28C
10 00 x 43M — DE OD ADE O	12 61 \ 23B	J3.93 > 29C
J3.30 > 30A	J3 62 1-30D	J3.94> 30C OLVSQ_LO
.13.31 \	J3.63 > 31B - O AGND	J3.95 > 31C PROVE_VOL\SQ_HI
J3.32 > 32A	J3.64 > 32B AGND	J3.96 > 32C

RX FRONT END

RX IF

SYSTEM MODULE

J4.1 > 1A	— Ö INT_OSC	J4.33 > 1B		J4.65 > 1C	→O AGND
J4.2 > 2A	—Ò AGND	J4 34 2 2B		20	→ CLOCK
J4.3 ≻ 3A	─O LAN_1	J4.35> 3B		J4 67	→O DATA
J4.4 > 4A	— ⊙ AGND	J4 36 > 4B		J4.68 > 4C	—Q ENABLE
.14.5 > 5A	—Q LAN_2	.14.37 > 5B		J4.69 > 5C	_Ç AGND
J4.6 ≯ 6A	—O AGND	J4.38 ≻ 6B		J4.70 > 6€	— o A0
J4.7 > 7A	— ⊘ AGND	J4.39		J4.71> 7C	○ A1
.14 o \8A	— O AGND	J4.40 0 8B	—⇔ AGND	J4.72> 8C	→O A2
J4.9 > 9A	—O AGND	.14.41 > 9B	—O AGND	J4.73 ≯ 9C	→ AGND
J4.10 ≥ 10A	—O AGND	J4.42	—O AGND —O AGND	J4.74 ≥ 10C	—O FLAG_0
J4.11 > 11A	-QRF_SPARE_4	J4.43	—⊘ AGND	J4.75 > 11C	—O FLAG_1
.M. 12 12A	—O RF_SPARE_5	J4.443 12B	—O AGND	J4.76 > 12C	FLAG_2
J4.13 \ 13A	—O ANT_REL	J4.45 13B	—⊘ AGND —⊘ AGND	120	—O FLAG_3
J4.14 14A	—O AGND	.14.46 \ 14B	—O AGND	J4.77 > 14C	—O RF_SPARE_3
J4.I5 15A	— ○ 12V	M 473	—Q+12V	J4.79 > 15C	—O +12V
J4.16	—Q +13.8VF	.14.49× 16B		160	—O +13.8VF
J4.17 17A	— Q 4GND	14.403	—() +13.8VF —() AGND	J4.80 > 17C	
18A	— ○ +5V	J4.50 5		J4.81) 18C	— ⊙ AGND ÷ .5∨
J4. 18 > 18A		* 19B	— ⊙ • 5∀	J4.82 > 19C	— ○ +5V — ○ AGND
J4.19 20A	→ AGND	J4.51 > 20B	→O AGND	J4.83 > 20C	V
214	- 0.12V	J4.52 > 21B	→O AGND	J4.84 > 21C	~
	—Ф _{AGND}	J4.53 > 22B	—O AGND	J4.85 > 22C	
J4.22 > 22A		J4.54) 23B	—O AGND	J4.86 <u>22C</u>	O TX_AUDIO_LO
J4.23 > 23A		J4.55 24B	— ♂ AGND	J4.87 ≽ 24C	TX_AUDIO_HI
J4.24 > 24A	—♥RF_SPARE_6	J4.56) 24B	— ♂ AGND	J4.88 > 25C	→ AGND
	→ RF_SPARE_1	J4.57 7	→O AGND	J4.89) 26C	→ AGND
J4.26 > 26A		J4.58 / 27D	—O AGND	J4.30)	— Q CAS
J4.27 > 27A		J4.59> 27B	—O AGND	J4.91 > 27C	— ○ SQ-ARM
J4.28 > 28A	— ○ AGND	J4.602	— O AGND	J4.92 > 28C 29C	→O AGND
J4,29 > 29A	→CRF_SPARE_2	J4.61 > 29B	— ♂ AGND	J4.93 > 29C	— ⊘ AGND
J4.30 30A		J4.62 > 30B	—O AGND	J4.94)	→O VOLISQ_LO
.14.31 \		J4.63 > 31B	-O AGND	J4.95) 31C	— → RCVR_VOL(SQ_HI
J4.32 32A		J4.64 > 32B	—O AGND	J4.96 > 32C	— Q AGND
CKPLAN	NE (A1)				

J5.1 > 1A	—⊘ AGND	J5.337 1B O	AGND	J5.65 > 1C	O AGND
.15.2) ZA	→ YOLISQ_LO	JE 345 20 30	SYS_VOL(SQ_HI	IE cc 1 2U	-O +13.8VF
J5.3 >	— O MIC_HI	.15.25\	MIC_LO	J5 67 > 3C	O 2ND RCVR
J5 4 2 4A	—O LINE_B	J5.365 4D	LINE_A	J5.68	-O-MDM_LINE_TXD
J5.5 > 5A	O DPLX_LINE_A	.15.37.) ³⁰	DPLX_LINE_B	J5.69) 5C	O TX_AUDIO_HI
J5.6 > 6A	──O VG_ALERT	J5.38 > 00 (2)	CG_HI	J5.70) 6C	TX_AUDIO_LO
J5.7 > 7A		.15.39 \(\frac{7B}{A}\)	VG_MIC_HI	J5.713 7C	-○ +12V
J5.8 > 8A	— ⊙ -5∀	.15 40 > O	-5V	J5.72 SC	- ○ - 12V
J5.9 >9A	•	JS 41 \	•	J5.73 > 9C	- ○ -12V
J5.10 > 10A	— ○ +5∀A	J5.42 > 10B O J5.43 > 11B Q	+5VA	J5.74 > 10C	-O -12V
J5.11 > 11A	—Q MDM_LINE_RXD	J5 43 11B	EXT_JCK	J5.75) 11C	O REPEAT_PTT_IN
J5.12 > 12.A	— COMB_PTT_OUT		DETECT_DIS	J5.76 > 12C	-O TX_CG_EN
J5.13 5 13A		J5 45 13B	DGND	J5.77 > 13C	O DGND
J5.I4) 14A	— ○ DGND —•○ +5V	15.46 14B	+5V	J5.78 14C	- O +5V
150	-	15B '		J5.79 15C	•
J5.I5 > 16A	O LIM_CG		RUS_IN	100	O RX_1_MUTE
J5.16 > 16A J5.17 > 17A	REMOTE_PTT_IN	J5.48 > 16B	DELAYED_PTT_IN	J5.80 > 17C	O COMB_PTT_IN
	— ⊘ 1950_DIS	J5.49 17B O	VG PTT_OUT	JS.81) 10C	REMOTE_PTT_OUT
J5.18 >		19B	CG_MON	J5.82) 19C	RESET
	—♦ DC_CNTRL_1	J5.51 > 20B	DC_CNTRL_2	J5.83 7 30C	O DC_CNTRL_3
J5.20) 20A 21A	—◆ BATT_STDBY	J5.52 >21B	CAS	J5.84 >	O LOCAL_PTT
	—○ A+	J5.52 20B O J5.53 21B O J5.54 22B O	A+	J5.85) 22C	- O A+
J5.22 > 22A	—— ◆ SERIAL_CLK	J5.54 >22D	WALSH_1	J5.86 >	-O VG_SQ_DSBL
IE 224 42M	——	J5.55 > 23B	PA_KEY	J5.87 > 24C	O AUX_RX_MUTE
IE 24 N 24 M	— ⊘ PVR_SNSR	J5.56 > 24B	TX_MTR+	.15.88 N	O TX_MTR-
JB 25 % 40M	— ⇔ BX_MTB∙	J5.57 25B	BX_MTR-	J5.89 25C	-O - 5∀
J5 26 3 2000	—• ♦ EXT_LSD	.15.58 \$ 26B	EXT_HSD	J5.90 > 200	-⊙ - 5∀
J5.27 1 27A	— ⊘ PGM_RXD	JS 59 \ 27B	M3_STATUS	J5.91 > 27C	O PGM_TXD
.15.28 _288		J5 603-20D		J5.92 > 28C	-⊙ comm-
	—⊘ СОММ-	IE 01 % 23D		J5.93 > 29C	O RXF1
15 00 NOM	─O RXF2	J5.62 \ 30B	RXF3/AUX1	J5.9 4	O BXF4/AUX2
	──○ +13.8VF	J5 63 \\ 31B \\ -0	TXF1	J5.95 23C	→ TXF2
J5.32 > 32A	—• O TXF3		TXF4	J5.96 > 32C	•• +13.8VF

THIS SCHEMATIC DIAGRAM APPLIES TO MODEL NO. REV LETTER
PL19D902947G1 B

T/R SHELF BACKPLANE (A1) 19D902947G1

(19D902949 Sh.1 Rev.3)

LBI-38637 SCHEMATIC DIAGRAM LBI-38637

SPARE 1

J6.1 > 1A	——	J6.33) 1B	—♦ AGND	J6.65 > 1C
J6.2 > 2A	O AUX_SPARE_1	J6.34) 2B	—O SYS_VOL\SQ_HI	.16.66 > − − − ↑ +13.8VF
J6.3 > 3A	O AUX SPARE 2	J6.35 > 3D	—Ø AUX_SPARE_3	J6.67) 3C O AUX_SPARE_4
J6.4 >	—, LINE_A	JB 36 3	O LINE A	JE 683 4C CO ALIV SDADE 52
J6.5 > 5A	—_♠ POLL_SER	J6.375 5B	—O STATUS	J6.697 5C O RPT_AUD_MUTE
J6.6 > 6A	— O AUX SPARE 5	J6.385 6B	—O AUX_SPARE_6	J6 70) O IBM SEB
J6.7> 7A		J6.395 7B	—O AUX SPARE 7	J6.712 Q +12V
J6.8> 8A	—O-5V	J6.40 > 8B	—o -5V	J6 723-8C O +121/
J6.9 > 9A	O AUX_SPARE_8	J6.41 > 9B	—Ø AUX_SPARE_9	J6.73> 9C Q -12V
J6.10> 10A		J6.422 10B	—O +5VA	J6.74> 10C O -12V
J6.11 > 11A	—Q AUX_SPARE_10	J6.43> 11B	—	J6.75) 11C Q AUX_SPARE_12
J6.12> 12A	O AUX_SPARE_13	J6.443 12B	O AUX_SPARE_14	J6 762 12C 3 AUX SPARE 15
J6.13 > 13 A	—¢ DGND	J6.45> 13B	—O DGND	J6.77> 13C → O DGND
J6.14 > 14.A	—Q +5V	J6.46 > 14B	 Q +5V	J6.78> 14C
J6.15 > 15A	—	J6.47 > 15B	—Ø AUX_SPARE_17	J6.79) 15C Q IF/AUX-SPARE 1
J6.16) 16A	— □ IF/AUX-SPARE_2	J6.48 > 16B	-O IF∤AUX-SPARE 3	J6.80> 16C O IF/AUX-SPARE 4
J6.17 > 17A	O IF/AUX-SPARE_5	J6.49 > 17B	O IF/AUX-SPARE_6	J6.81 > 17C O JEVALIX-SPARE 7
J6.18 > 18A	O IF/AUX-SPARE_8	J6.50; 18B	-O AUX_SPARE_18	J6.823 18C O BESET
J6.19 > 19A		J6.51) 19B	-O AUX_SPARE_20	J6.835 19C O AUX SPARE 21
J6.20 > 20A	AUX_SPARE_22	J6.52> 20B	—ð IRM/SRM_CNTL	J6.84) 20C O AUX SPARE 23
J6.21 > 21A	—O A+	J6.53> 21B	—O A+	J6.85> 21C → Q A+
J6.22 > 22A	—⊙ AUX_SPARE_24	J6.542	—O AUX_SPARE_25	J6.86> 22C O AUX_SPARE_26
J6.23) 23A	—o ANT_REL	J6.55 > 23B	ØAUX_SPARE_27	J6.87) 23C O AUX SPARE 28
J6.24) 24A	—— ⊘ AUX_SPARE_29	J6.56) 24B	-O AUX SPARE 30	J6.88> Z4C O AUX SPARE 31
J6.25) 25A		J6.57> 25B	—OAUX_SPARE_33	J6.89 > 25C
J6.26) 26A	—Q EXT_LSD	J6.58> 26B	—OEXT_HSD	J6.90 > 26C O AUX SPARE 37
J6.27 27A	—_O AUX_SPARE_38	J6.59> 27B	—O AUX_SPARE_39	J6.91> 27€ O AUY CDADE 40
J6.28) 28A	—_O AUX_SPARE_41	J6.60> 28B	O AUX_SPARE_42	JB 923
J6.29) 29A	—o COMM-	J6.612 29B	O AUX_SPARE_43	J6.93> O AUX SPARE 44
J6.30) 30A	—— → AUX_SPARE_45	J6.62) 30B	—Q AUX_SPARE_46	J6.94 3 30C TO ALLY SPARE 47
J6.31≯ 31A	—— Q +13.8VF	J6.63) 31B	-O AUX_SPARE_48	J6.95 > 31C
J6.32> 32A	——	J6.64) 32B	-O AUX_SPARE_51	J6.96> 32C 0 +13.8VF

SPARE 2

07.17	O varie	J7.33 A DD	-J AGND	J7.65 Z	-O AGNO
J7.2 > 2A	O AUX_SPARE_1	J7.34 2B	-⊘ SYS_VOL\SQ_HI	J7.66 2C J7.66 3C	-Q +13.8VF
J7.3 > 3A	—Ç AUX_SPARE_2	J7.35 ≯ 3B	OAUX_SPARE_3	.17.673	-Q AUX_SPARE_4
J7.4 > ***	—¢ LINE_B	J7.36 > 4B	-Q LINE_A	J7.68>4C	O AUX_SPARE_52
J7.5 > 5A	—Ø POLL_SER	J7.37) 5B	- ÇSTATUS	J7.69) 5C	O RPT_AUD_MUTE
J7.6 > 6A	—Q AUX_SPARE_5	J7.38) 6B	O AUX_SPARE_6	.17 70 \$ 00	-O IRM_SFR
J7.7 > 7A	—Ç CG_MON (BT DSBLE)	J7.39> 7B	OAUX_SPARE_7	J7.713 7C	-O ₊12V
J7.8 > 8A	○ -5V	J7.40> 8B	-⊙ -5∀	J7.72> 8C	-O +12V
J7.9 > 9A	—O AUX_SPARE_8	J7.41) 9B	-O AUX_SPARE_9	J7.73	−O -12V
J7.10 > 10A	—∩ +5VA	J7.42> 10B	- ⊙ +5VA	J7.74 > 10C	-O -12V
J7.11 3 11A	—Ġ AUX_SPARE_10	J7.43) 11B	-O AUX_SPARE_11	J7.75) 11C	-O AUX_SPARE_12
J7.12 3 12A	O AUX_SPARE_13	J7.44> 12B	-ÇAUX_SPARE_14	J7.76> 12C	-O AUX_SPARE_15
J7.13 > 13A	—C DGND	J7.45> 13B	– ⊙ DGND	J7.77> 13C	- O DGND
J7.14 > 14 A	— ⇔ •5∨	J7.46≻ 14B	-○ +5V	J7.783 14C	-O _{•5γ}
J7.15 > 15A	O AUX_SPARE_16	J7.47 > 15B	-O AUX_SPARE_17	J7.79≯ 15C	→ IF/AUX-SPARE_1
J7.16 > 16A	—Ø IF≀AUX-SPARE_2	J7.48> 16B	-O IF/AUX-SPARE_3	J7.80 > 16C	→ IF/AUX-SPARE_4
J7.17 > 17A	O IF/AUX-SPARE_5	J7.49> 17B	-Q IF∤AUX-SPARE_6	J7.81> 17C	-O IF/AUX-SPARE_7
J7.183 18A	—Q IF∤AUX-SPARE_8	J7.50) - 18B	A) AUX_SPARE_18	J7.82) 18C	-O RESET
J7.192 19A	-O AUX_SPARE_19	J7.51 > 19B	- ⊘ AUX_SPARE_20	J7.83) 19C	-O AUX_SPARE_21
J7.20> 20A	AUX_SPARE_22	J7.52) 20B	-Q IRM/SRM_CNTL	J7.84) 20C	O AUX SPARE 23
J7.21 > 21A	—Q A+	J7.533 21B	-O A+	J7.85) 21C	- Q A+
11/2/2	—Ç AUX_SPARE_24	J7.54 > 22B	HQ AUX_SPARE_25	J7.86) 22C	O AUX_SPARE_26
J7.23> 23A 24A	—	J7.55> 23B	O AUX_SPARE_27	J7.87> 23C	AUX_SPARE_28
J7.24 > 25A	O AUX_SPARE_29	J7.56) 24B	O AUX_SPARE_30	J7.88> 24C	O AUX_SPARE_31
J7.25) 26A	-O AUX_SPARE_32	J7.57) 25B	-O AUX_SPARE_33	J7.89) 25C	-O AUX_SPARE_34
J7.26)	—O EXT_LSD	J7.58> 26B	-OEXT_HSD	J7.90> 26C	O AUX_SPARE_37
J7.273 27A	-Q AUX_SPARE_38	J7.59) 27B	-O AUX_SPARE_39	J7.91> 27C	AUX_SPARE_40
J7.28) 28A	—O AUX_SPARE_41	J7.60> 28B	O AUX_SPARE_42	J7.92 > 28C	-O COMM∙
J7.29≯ 29A	— 0 сомм₊	J7.61 > 29B	O AUX_SPARE_43	.17 923	-O AUX_SPARE_44
J7.30 > 30A	—	J7.62 > 30B	AUX_SPARE_46	J7.94 > 30C 31C	-Ø AUX_SPARE_47
J/C31 2	—O +13.8VF	J7.63> 31B	O AUX_SPARE_48	J7.95>	O AUX_SPARE_48
J7.32 5 32A	—Q AUX_SPARE_50	J7.64 ≻ 32B	O AUX_SPARE_51	J7.96 ≯ 32C	- ○ + 13.8VF

SPARE 3

J8.1 > 1A	—O AGND	J8.33) 1B	—∆ AGND	J8.65; 1C O AGND
.18.2 N 2M	—Q AUX_SPARE_1	J8.34) 2B	—O SYS VOLISQ HI	J8.66 > 2C O +13.8VF
J8.33 3A	—Ω AUX SPARE 2	J8.35) 3B	—ე AUX_SPARE_3	
J8.42 4A	— C LINE B	J8.36) 4B	—g LINE_A	4C
J8.5 > 5A	—() POLL SER	J8.37) 5B	— () STATUS	J8.68) O AUX_SPARE_52 J8.69) 6C O RPT_AUD_MUTE
J8.6 16A	—O AUX_SPARE_5	J8.37) 6B		J8.69> GC RPT_AUD_MUTE
J8.7) 7A		38.38)	—O AUX_SPARE_6	J8.70≻6C → O IRM_SFR
J8.8 > 8A	—• CG_MON (BT DSBLE)	J8.393-7B	—AUX_SPARE_7	00.71 2 0 +12 γ
J8.8 > 9A J8.9 > 49A	—;;;-5∀	J8.40 J	— ⊙ -5V	J8.72) 9C -12V
	—• AUX_SPARE_8	100	—Q AUX_SPARE_9	J8.733
J8.107	— ⊙ •5∀A	J8.42 J	— Q + 5∀A	J8.747 O -12V
J8.11 > 110	O AUX_SPARE_10	J8.437	—Ġ AUX_SPARE_11	J8.75)
J8.125 12A	—— O AUX_SPARE_13	J8.99.**	—Q AUX_SPARE_14	J8.762
J8.13 > 13 A	—O DGND	140	—o DGND	J8.77) 14C O DGND
J8.14 × 14 A	—- ◇ ∙ 5∨	JO.40)	— ೧ +5∀	J8.783———•• +5¥
J8.15 > 16A	O AUX_SPARE_16	J8.47) 15B	—₫ AUX_SPARE_17	J8.79) 15C O IF/AUX-SPARE_1
	────────────────────────────────────	J8.48 > 16B	O IF/AUX-SPARE_3	J8.80) 16C
J8.17 > 17A	——	J8.49 7 17B	—Ø IF/AUX-SPARE_6	J8.81> 17C
J8.18 > 18A	—₫ IFłAUX-SPARE_8	J8.50 > 18B	—	J8.82) O RESET
J8.19 > 19A	AUX_SPARE_19	J8.51 > 19B	O AUX SPARE 20	.18.83) 19C O ALIX SPARE 21
J8.19 20A J8.20 21A	O AUX_SPARE_22	J8.52 > 20B	- RM/SRM CNTL	J8.84) 20C O AUX SPARE 23
J8.21 >	— ○ A+	J8.53> 21B	—0 A+	J8.85) 21C Q A+
J8.22 22A	O AUX_SPARE_24	J8.54 5 22B	—♦ AUX_SPARE_25	J8 86 > 22C O ALIX SPARE 26
J8.23≽ 23A	O ANT_REL	J8.55 > 23B	—O AUX SPARE 27	10.07 1 Z3C 10.011V CDADE 20
J8.24> 24A	O AUX SPARE 29	J8.56 3 24B	—O AUX_SPARE_30	J8.88> 24C
J8.25 25A	O AUX SPARE 32	J8.573 25B	O AUX_SPARE_33	J8.89 > 25C
J8.26 > 26A	— O EXT_LSD	J8.58 > 26B	—OEXT_HSD	J8.90 > 26C
.18 27 5 47 M	-O AUX SPARE 38	J8.592 27B	O AUX_SPARE_39	270
J8.28 > 28A	—O AUX_SPARE_41	J8.60 28B		280 110112011112110
10 201 438	—0 COMM-	J8.61 29B	O AUX_SPARE_42	29C - 0011111
J8.30 30A	—Q AUX_SPARE_45	J8.62 30B	—O AUX_SPARE_43	300
10.04 - 310		J8.63 > 31B	—O AUX_SPARE_46	310
J8.31 32A		J8.64 > 32B	—O AUX_SPARE_48 —O AUX_SPARE_51	J8.95) 32C 0 +13.8VF

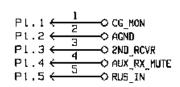
POWER MODULE

INTERFACE BOARD

J10.1 > 1A O AGND	J10.33) 1B	—Ç AGND	J10.65 > 1C	—Ò AGND
J10.2 > 2A	J10.34 > 2B	Ô RCVR_VOL\SQ_HI	.un.ee) 20	O+13.8VF
J10.3 > 3A O MIC_HI	J10.35 > 3B	→ MIC_LO	J10.67> 3C	-
J10.4 > 4A O LINE_B	J10.365 4B	—O LINE_A	J10.68> 4C	-
J10.5 > 5A O DPLX LINE A	J10.375 5B	O DPLXLINE B	J10.69 5 5C	-
J10.6 > 6A	J10.38 > 6B		J10.70> 6C	-
J10.7 > 7A O INTROM AUDIO	J10.39 7B		J10.715 7C	—O +12V
J10.8 > 8A O -5V	J10.40 > 8B	 ♦-5V	J10.72 > 8C	—○ +12V
J10.9 > 9A	J10.41 > 9B		J10.73> 9C	—O -12∀
J10.10 > 10A O +5VA	J10.42> 10B	- ⊘ +5VA	J10.74> 10C	—O -12V
J10.11 2 11A	J10.43 > 11B		J10.75 > 11C	— O +5∀A
J10.12) 12A	J10.44 > 12B		.00 763 12C	-
J10.13) 13A O DGND	J10.45> 13B	—Ò DGND	J10.77 > 13C	—Ō DGND
J10.14 > 14A O +5V	J10.46) 14B	○ - 5V	J10.78 > 14C	- 0 •5∀
J10.15 > 15A	J10.47) 15B		J10.79 > 15C	—O IF≀AUX-SPARE_1
J10.16 > 16A O IF/AUX-SPARE_2	J10.48 > 16B	O IF∤AUX-SPARE_3	J10.80 > 16C	—Ò IF/AUX-SPARE_
J10.17) 17A O IF/AUX-SPARE_5	J10.49>	—⊜ IF/AUX-SPARE_6	J10.81 > 17C	—O IF/AUX-SPARE_T
J10.18 > 18A	J10.50 > 18B		J10.82> 18C	—O RESET ¯
J10.19) 19A O DC_CNTRL_1	J10.51 > 19B	-O DC_CNTRL_2	J10.83 > 19C	—
J10.20 > 20A O BATT_STOBY	J10.52 > 20B		J10.84 > 20C	—Q LOCAL_PTT
J10.21 > 22A A PULLANTE	J10.53 > 21B	⊙ A+	J10.85> 21C	 Q A+
JIU.223 O HX I_MUTE	.110.54 >	—Q FLAG_1	J10.86> 22C	—♦ FLAG_2
J10.23) 23A O ANT_REL	J10.55> 23B	—O PA_KEY	J10.87≯ 23C	
J10.24> 25A O PWR_SNSR	J10.56 > 24B 25B	—♥ TXF1	J10.88> 24C	— ⊘ TXF2
J10.25> 26A O BXF1	.110.57.2	—O SERIAL_CLK	J10.89 > 25C	—Q RXF2
J10.262	J10.58 > 26B	—O RXF4/AUX2	J10.90 > 26C	—♦ RXF3/AUX1
J10.27>	J10.59 > 27B		J10.91 > 27C	—O PCM_TXO
J10.28 > 28A 2 COLUM	J10.60 > 28B	─O FLAG_0	J10.92 > 28C	O COMM∙
J10.29) 29A O COMM-	J10.61 > 29B	—♦ FLAG_3	.110.93 >	— ⊙ A0
J10.30 > 30A O A1	J10 62 >	—Ò A2	J10.94 > 30C	—Ç ENABLE
J10.31 >	J10.63 > 31B	—Ò DATA	J10.95 >	—⊙ с∟оск
J10.32 > 32M Q INT_OSC	J10.64 > 32B		J10.96> 32C	-

T/R SHELF BACKPLANE (A1) 19D902947G1

(19D902949 Sh.2 Rev.3)



2ND RCVR

EXTERNAL METERING/RIC

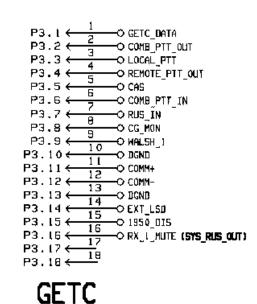
—⇔ MIC_HI

2

P4.2 ← 3

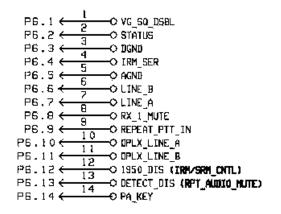
P2.3 ← 3 → 0 DGND

GETC

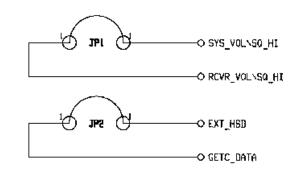


P5.1 ← 0 6	1 +
P5.2 (2 0)	AGND
	127
4	AGNE
P5.5 ← 0	-12V
	VG_MIC_HI
P5.7 ← 7	1IC_L0
	HYS VÓLNSQ HI
	/OL/SO_LO
10	RCVR VOLNSQ HI
11	KCAK_AOE /20_HI
P5.11 (12	CHR
13	IGND
14	/G_PTT_QUT
P5.14 (15	10. 14.4044
Ih.	/G_ALERT
	/G_SQ_DSBL
P5.17 ← ○ (CAS
P5.18 (19 (19	CG_MON
P5.15 ← 20 O	TXF3 (VG_CLR_SEL.)
P5.20 ← ○ F	RX_1_MUTE (sys_raus_out)
P5.21 € 22 ○ 3	(VG_GROJ_SEL)
P5.22 (23)	GETC_DATA
P5.23 (24)	EXT_HSD
P5.24 (25 0)	.950_DIS
P5.25 €** ○ F	REPEAT PIT OUT

VOICE GUARD



GE-MARC



NOTE: CUT PWB RUNS FOR VG E/D ONLY

T/R SHELF BACKPLANE (A1) 19D902947G1

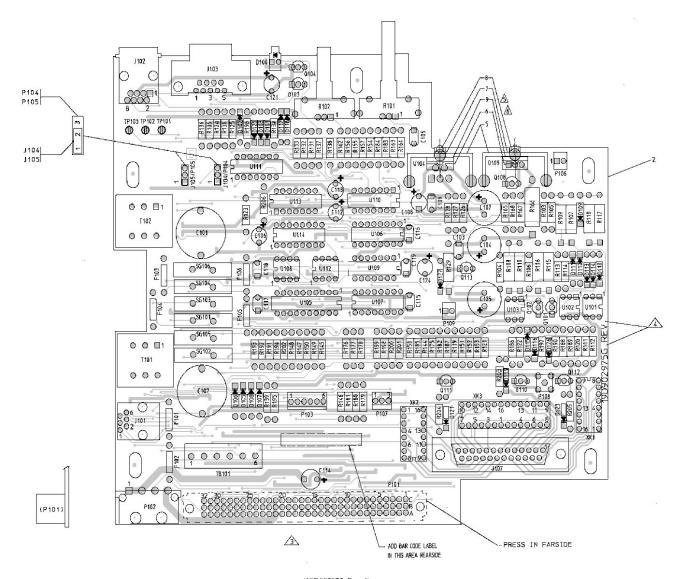
(19D902949 Sh.3 Rev.4)

COMPONENT SIDE

P104 OR P105-J104 OR J105-....... Utes SG104 @ SG102 @ ● SG1Ø3 ● 0 0 E 0 0 0 9110 8810 1 a PIE3 a a (014) 19D902975G REV 14 1 (PIBI) A - ADD BAR CODE LABEL IN THIS AREA NEARSIDE.

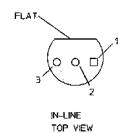
(19D902975, Sh. 1, Rev. 2) (19D902976, Rev. 1)

COMPONENT SIDE

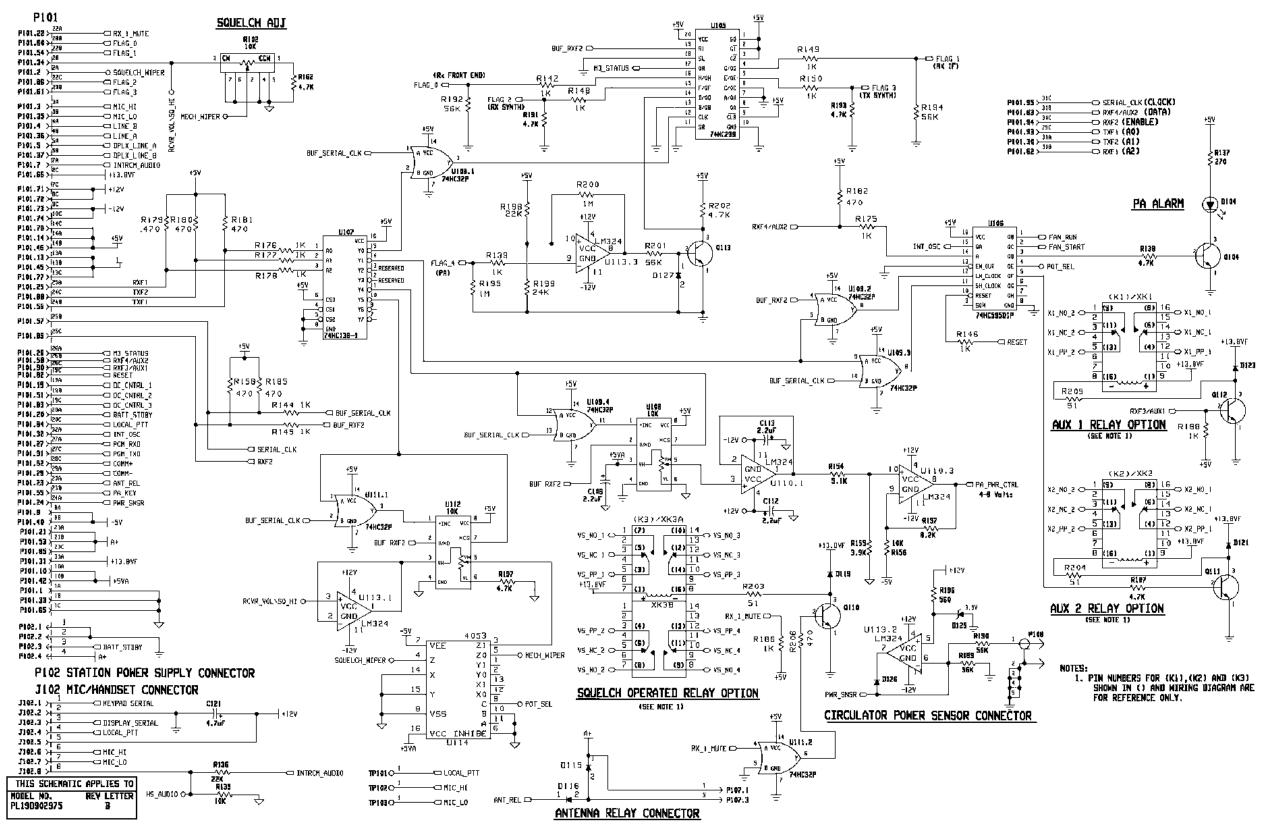


(19D902975, Rev. 4) (19D902976, Rev. 3)

LEAD IDENTIFICATION FOR Q101, Q102, Q103, Q104 AND Q108



NOTE:CASE SHAPE IS DETERMINING FACTOR FOR LEAD IDENTIFICATION T/R INTERFACE BOARD (Rev. A and later) (A2) 19D902975G1



T/R SHELF INTERFACE BOARD (A2) 19D902975G1

(19D902977 Sh.1 Rev.7)

