

## MAINTENANCE MANUAL

## MULTISITE COORDINATOR II CARD CAGE 19D903310P1 AND BACKPLANE 19D903312P1

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#### **SPECIFICATIONS**

DIMENSIONS (H x W)	10.5" x 19" (26.67 cm x 48.26 cm) (6 Rack Units x 340 mm Card Cage)
INPUT VOLTAGE	+5 Vdc, +15 Vdc, -15 Vdc
NUMBER OF CARD SLOTS	21
CONNECTORS: P101-P119 P201-P219	96 Pins
P1E1, P1E2 P2E1, P2E2	96 Pin (long)
PA101-PA119 PA201-PA219	24 Pin
J1-J8	4 Pin Power Connector
JP101-JP119 JP201-JP219	24 Pin Header

#### **DESCRIPTION**

Card Cage 19D903310P1 along with Backplane 19D903312P1 provides housing and system interconnect for all printed circuit boards used in Ericsson GE's MultiSite Coordinator II (MSC II). A single Card Cage and Backplane assembly provides twenty-one (21) card slots.

A single system module used in the MSC II can have many individual circuit boards. Example: The MASTR II Interface Module (MIM) uses up to nine (9) circuit boards: seven (7) Audio Boards and two (2) Controller Boards. Generally, to accommodate all MSC II system "Nodes," more than one card cage and associated backplane is required in a system of any size.

Backplane 19D903312 is a four (4) layered circuit board. To show all layers in this manual, all outside layers are printed in color and all internal layers are printed as gray (refer to the Table of Contents for the **OUTLINE DIAGRAM** of the **Backplane**).

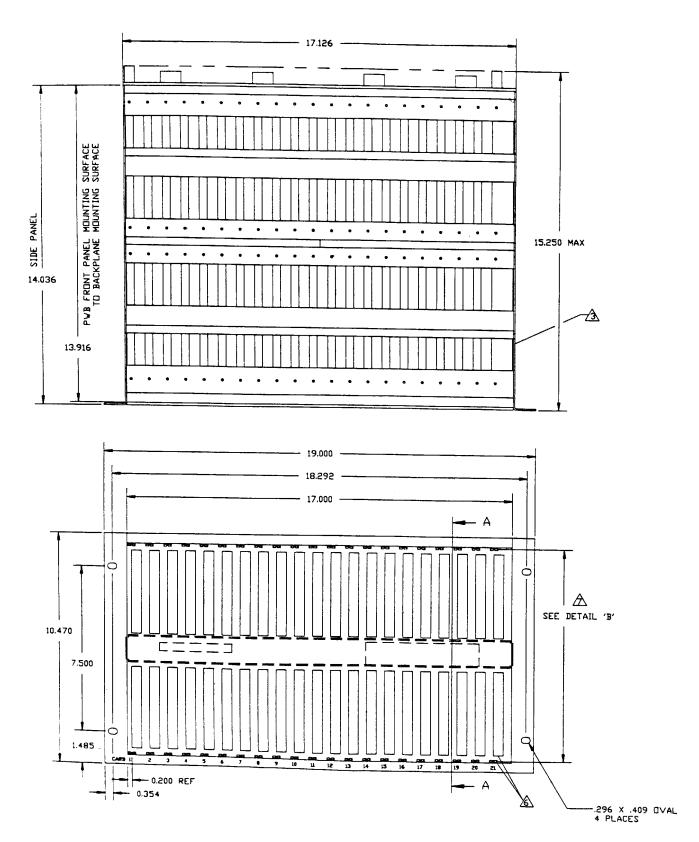
The **OUTLINE DIAGRAM** of the **Backplane** consists of two (2) sheets printed back-to-back. Sheet 1 shows

the front side of the board as seen through the front or open side of the card cage. Assembly Drawing 19D903312, Sheet 1 is printed on the printed circuit layer labeled **CONNECTOR SIDE**. This layer is the colored layer (outside layer). The Assembly Drawing and the **CONNECTOR SIDE** printed circuit layer are printed on the printed circuit layer labeled **INNER SIGNAL 1**. **INNER SIGNAL 1** is the gray layer (internal layer). Sheet 1 of the **OUTLINE DIAGRAM** shows all of the connectors (P101 - P119, P201 - P219, P1E1, P1E2, P2E1, P2E2) where all the various circuit cards plug.

Sheet 2 of the **OUTLINE DIAGRAM** for the **Back-plane** shows the back side of the board as seen from the back side or closed side of the card cage. Assembly Drawing 19D903312, Sheet 2 is printed on the printed circuit layer labeled **WIREWRAP SIDE**. This layer is the colored layer (outside layer). The Assembly Drawing and the **WIRE-WRAP SIDE** printed circuit layer are printed on the printed circuit layer labeled **INNER SIGNAL 2. INNER SIGNAL 2** is the gray layer (internal layer). Sheet 2 of the **OUTLINE DIAGRAM** shows all of the wirewrap connectors on the back of the board (JP101 - JP119, JP201 - JP219, P1E1, P1E2, P2E1, P2E2, PA101 - PA118, PA201 - PA218, PA1-2E2, PA2-2E2 and jacks J1 - J8).



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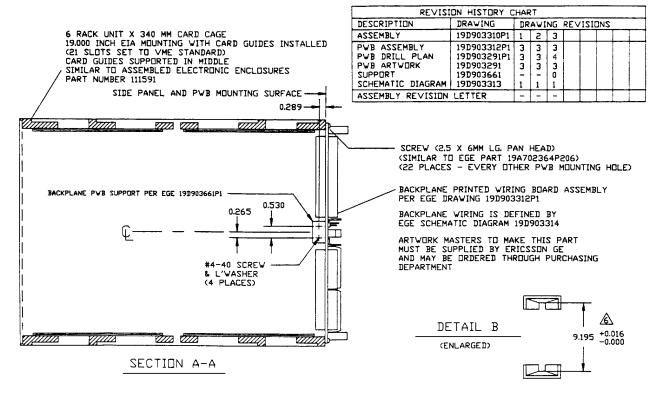


(19D903310, Sh. 1, Rev 3)

## **Card Cage**



UNLESS OTHERWISE SPECIFIED, VENDOR SHALL BUILD TO THE HIGHEST ASSEMBLY REVISION LISTED IN THE REVISION HISTORY CHART.



(19D903310, Sh. 1, Rev. 3)

1. THIS PRODUCT SHALL MEET ALL REQUIREMENTS OF THIS DRAWING PLUS EGE DRAWINGS:

19D903312P1 PWI 19D903291P1 PWI 19D903291 PWI

190903313

PWB ASSEMBLY
PWB DRILL PLAN
PWB ARTWORK
SCHEMATIC DIAGRAM

2. THIS ASSEMBLY IS SUBJECT TO ALL THE VENDOR TEST REQUIREMENTS LISTED IN 344A3886 WITH THE EXCEPTION OF REQUIREMENTS 3, 4, 8, 10, 11 AND 13.

A MARK SIDE PANEL OF CARD CAGE AS SHOWN BELOW. CHARACTERS .125 MIN. HIGH IN CONTRASTONG COLOR.

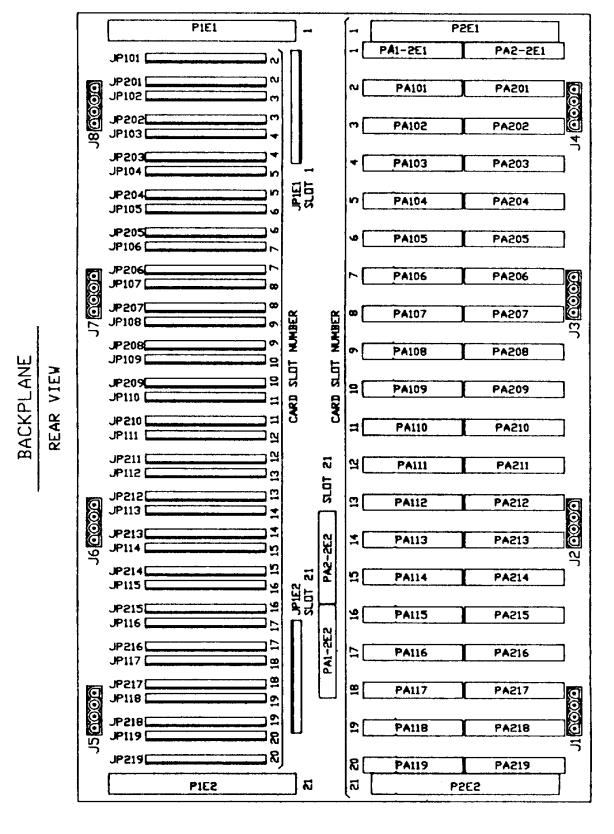
19D903310P1 R REV REV LETTER SHOWN IN VENDOR IDENTIFICATION REVISION HISTORY CHART COUNTRY OF ORIGIN

- 4. SHIP ONE CARD CAGE ASSEMBLY TO A BOX.
- 5. MARK DRAWING NUMBER, DRAWING REVISION NO, REVISION LETTER, VENDOR ID, COUNTRY OF ORIGIN (PER NOTE 3) ON OUTSIDE OF BOX

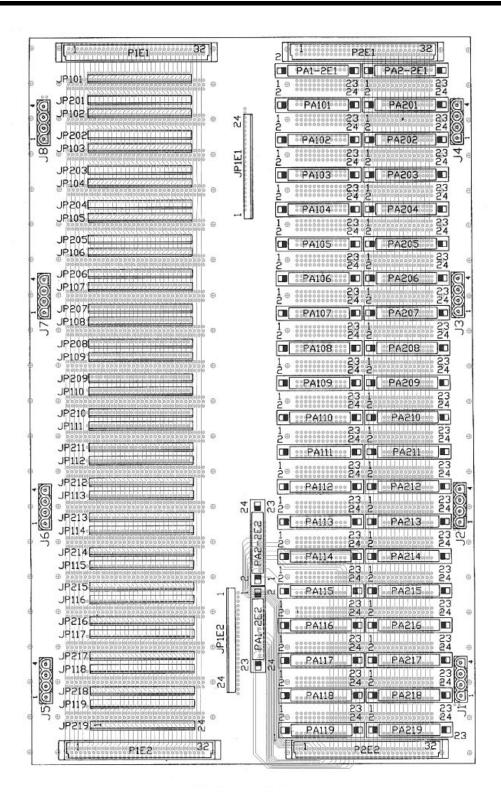
BACKPLANE/CARD GUIDE RELATIONSHIP IS CRITICAL AND WILL REQUIRE AN ASSEMBLY FIXTURE EQUIVALENT TO EGE CLOCK BOARD 190903305 LOCATED IN CARD POSITIONS 1 AND 21

ASSEMBLY MUST ACCEPT ANY COMBINATION OF:
19D903299P1 - CONTROLLER BOARD
19D903302P1 - AUDIO BOARD
19D903305P1 - CLOCK BOARD

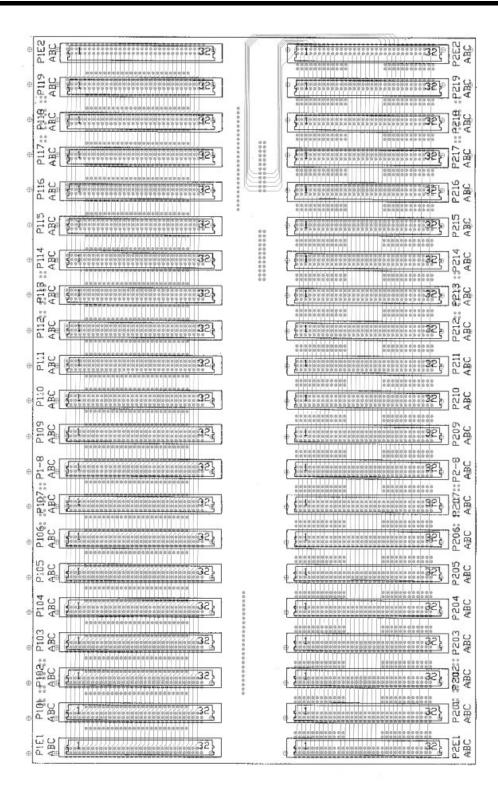
A STANDARD VME CARD CAGE DIMENSION AND TOLERANCE



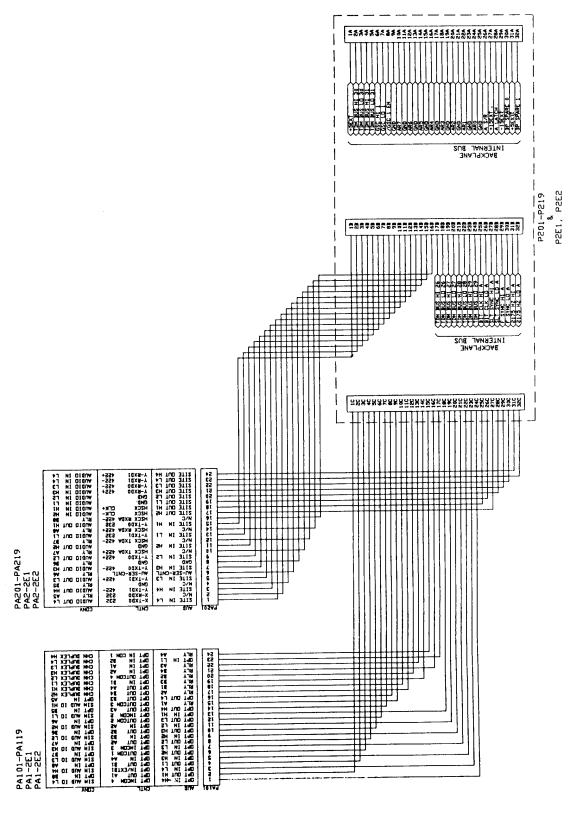
(19D903313, Sh. 1, Rev. 1)



(19D903312, Sh. 2, Rev. 3) (19D903291, Wirewrap Side. Rev. 3)



(19D903312, Sh. 2, Rev. 3) (19D903291, Wirewrap Side. Rev. 3)

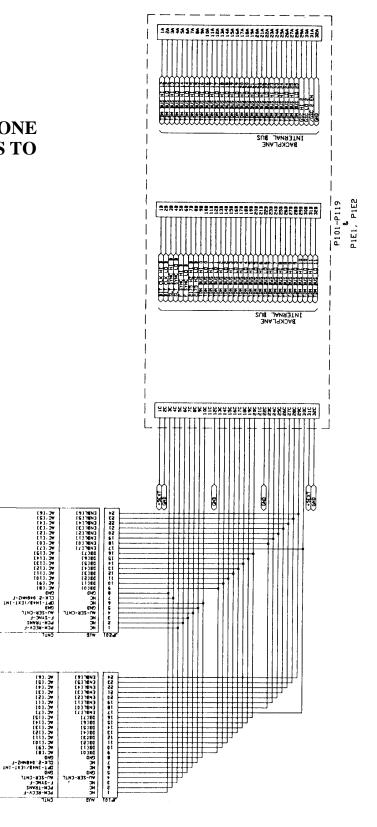


(19D903313, Sh. 1, Rev. 1)

# CIRCUIT SHOWN IS FOR ONE CARD SLOT AND APPLIES TO ALL CARD SLOTS

19201 - JP219, JP181

JP101-JP119, JP1E2



(19D903313, Sh. 1, Rev. 1)

LBI-38663 PARTS LIST

### Card Cage 19D903310P1 Backplane 19D903312P1

Issue

SYMBOL	GE PART NO.	DESCRIPTION
		CARD CAGE 19D903310P1
		BACKPLANE 19D903312P1
P101 thru		AMP 535032-4, 96 Pin Connector (Quantity 38)
P119 and P201		
thru P219		
P1E1, P1E2, P2E1,		AMP 1-215614-4, 96 (long) Pin Connector (Quantity 4)
P2E2 PA101,		AMP 499582-5, 24 Pin Connector (Quantity 42)
thru PAll8		ARE 499302-3, 24 FIR Connector (Quantity 42)
and PA201 thru		
PA218		
JP101 thru JP119		AMP 1-102898-7, 24 Pin Header (Quantity 40)
and JP201		
thru JP219		
J1 thru J8		AMP 173926-1, 4 Pin Power Connector (Quantity 8)
"		
1	19D903291P1	Printed Wire Board (Quantity 1)
2		AMP 535074-2, Shroud (w/mtg. holes) (Quantity 4)
3	19B235546P3	AMP 102320-1, Latch (Quantity 84)