

MAINTENANCE MANUAL
EDACS® INTERFACE
SIMULCAST 150 BAUD INTERFACE MODULE
19D904427G1

TABLE OF CONTENTS

	Page
DESCRIPTION	Front Cover
PARTS LIST	Front Cover
OUTLINE DIAGRAM	Back Cover
SCHEMATIC DIAGRAM	1

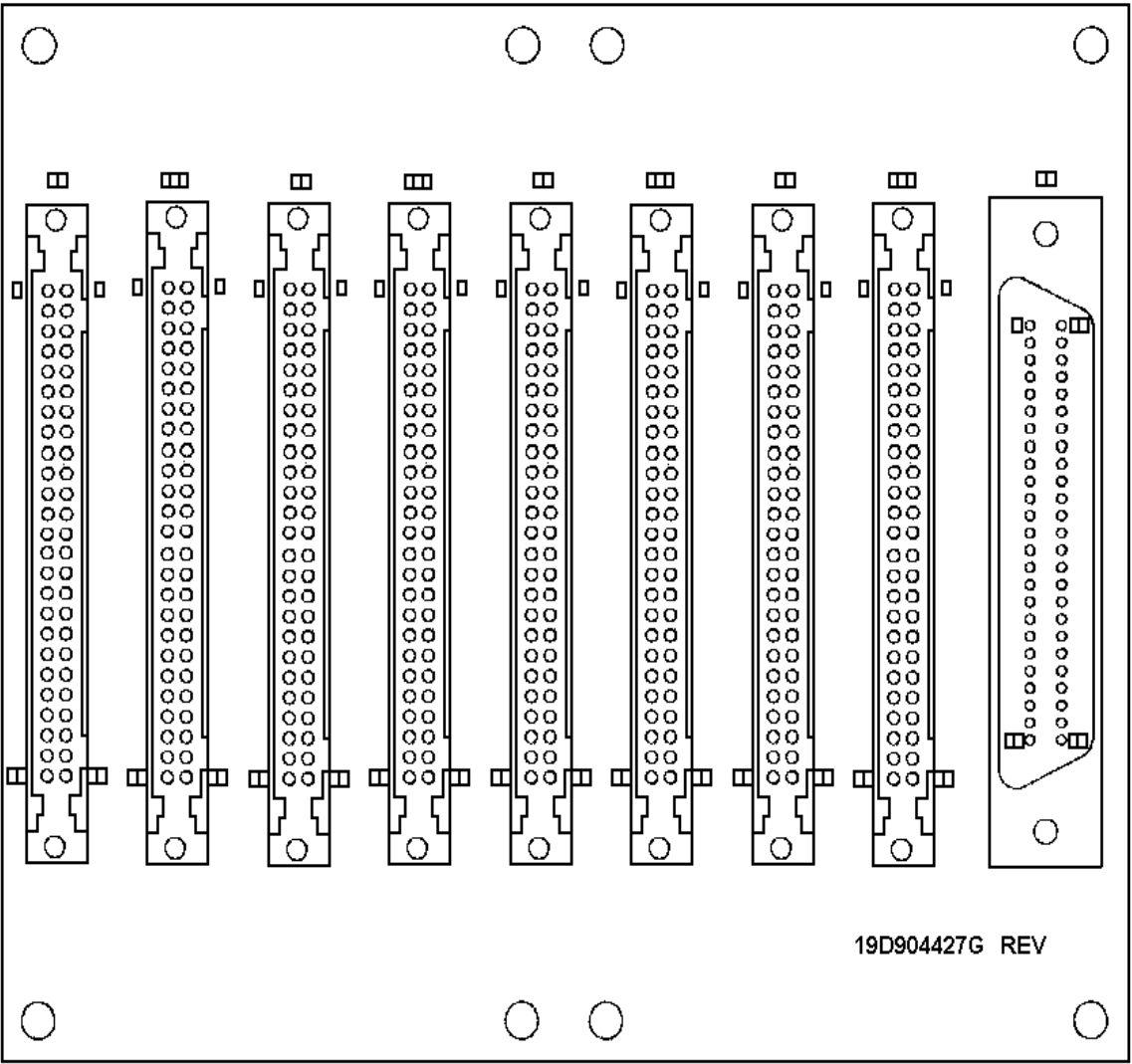
DESCRIPTION

The Simulcast 150 Baud Interface Module 19D904427G1 is used to remap jackfield appearances of 150 baud data. This board reduces the number of jackfields required for systems with 20 or less channels.

PARTS LIST
Simulcast 150 BAUD Data Interface Module
19D904427G1

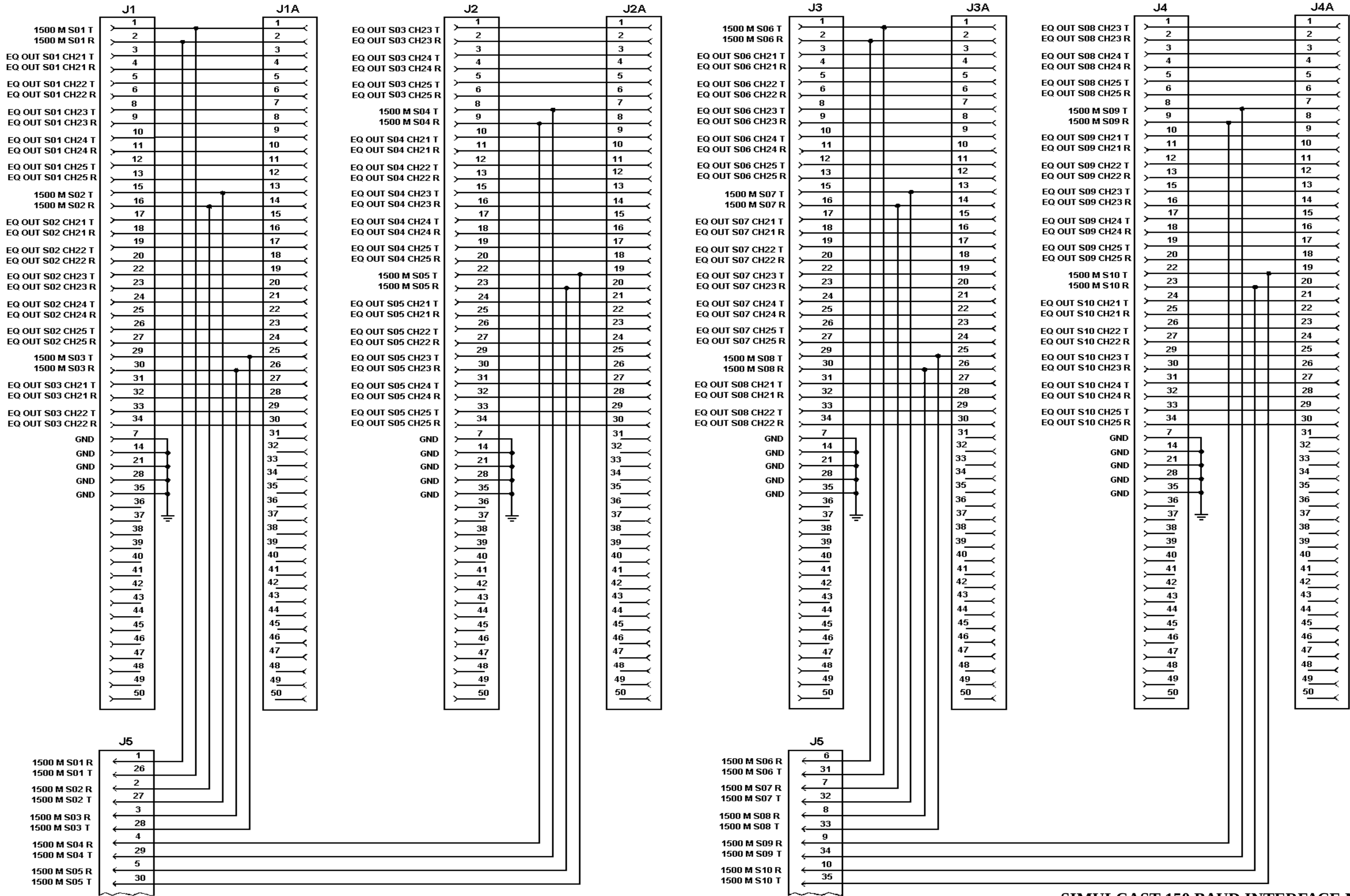
SYMBOL	PART NUMBER	DESCRIPTION
		----- CONNECTORS -----
J1 thru J4, & J1A thru J4A	19B235546P1	Connector, 50-position header, double row, two part, Sim to AMP Cat. #1-102154-0.
J5	19B800935P14	24-Position rectangular receptacle; Sim to Amp. Cat. #52224-1
		---- MISCELLANEOUS ----
4	19B235546P3	Connector Latch.

OUTLINE DIAGRAM



SIMULCAST 150 BAUD INTERFACE MODULE

(19D904427, Sh. 1, Rev. 1)



SIMULCAST 150 BAUD INTERFACE MODULE

(19D904425, Sh. 1, Rev. 0)