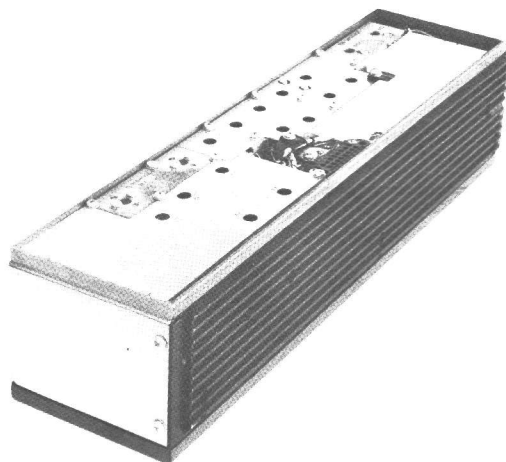


MASTR

PROGRESS LINE
26-50 MHz RECEIVER MODELS 4ER39D20-28



SPECIFICATIONS *

FCC Filing Designation

ER-39-A

Frequency Range

26—50 MHz

Audio Output

5 watts at less than 5% distortion

Sensitivity

12-dB SINAD (EIA Method)	0.35 μ V
20-dB Quieting Method	0.4 μ V

Selectivity

EIA Two-Signal Method	-85 dB (adjacent channel, 40 kHz channels)
20-dB Quieting Method	-100 dB at \pm 30 kHz

Spurious Response

-100 dB

First Oscillator Stability

\pm .0005% (-30°C to +60°C)

Modulation Acceptance

\pm 15 kHz

Squelch Sensitivity

Critical Squelch	0.20 μ V
Maximum Squelch	Greater than 20 dB quieting (less than 2 μ V)

Intermodulation (EIA)

-75 dB

Maximum Frequency Separation

0.4 %

Frequency Response

+1 and -8 dB of a standard 6-dB per octave de-emphasis curve from 300 to 3000 Hz (1000-Hz reference)

*These specifications are intended primarily for the use of the serviceman. Refer to the appropriate Specification Sheet for the complete specifications.

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— WARNING —

No one should be permitted to handle any portion of the equipment that is supplied with high voltage; or to connect any external apparatus to the units while the units are supplied with power. KEEP AWAY FROM LIVE CIRCUITS.

DESCRIPTION

General Electric MASTR Progress Line Receiver Type ER-39-D is a double conversion, superheterodyne FM receiver designed for operation on the 26-50 megahertz band.

The receiver is of single-unit construction and is completely housed in a copper-plated aluminum casting for maximum shielding and rigidity. The top compartment of the casting contains the RF, oscillator, converter, and 1st IF amplifier stages. The bottom portion of the casting contains the IF-audio and squelch board.

CIRCUIT ANALYSIS

The MASTR Progress Line Receiver is completely transistorized, using silicon transistors throughout for added reliability. Input leads to the receiver are individually filtered by the 20-pin feed-through by-pass

connector J443. A regulated +10 volts is used for all receiver stages except the audio PA stage which operates from the 12-volt system supply.

Centralized metering jack J442 is provided for use with General Electric Test Set Model 4EX3A10 or 4EX8K11 for ease of alignment and servicing. The Test Set meters the oscillator, multiplier, and limiter stages as well as the discriminator and regulated 10 volts.

RF AMPLIFIER (A341)

RF Amplifier A341 consists of two high-Q helical resonators and an RF amplifier stage (Q2). The RF signal from the antenna is coupled by RF cable W442/W443 to a tap on L341/L343/L345. The tap is positioned to insure the proper impedance match to the antenna. RF energy is coupled through the two coils by an opening in the shield wall to the RF Amplifier. The coils are tuned to the incoming frequency by air trimmer

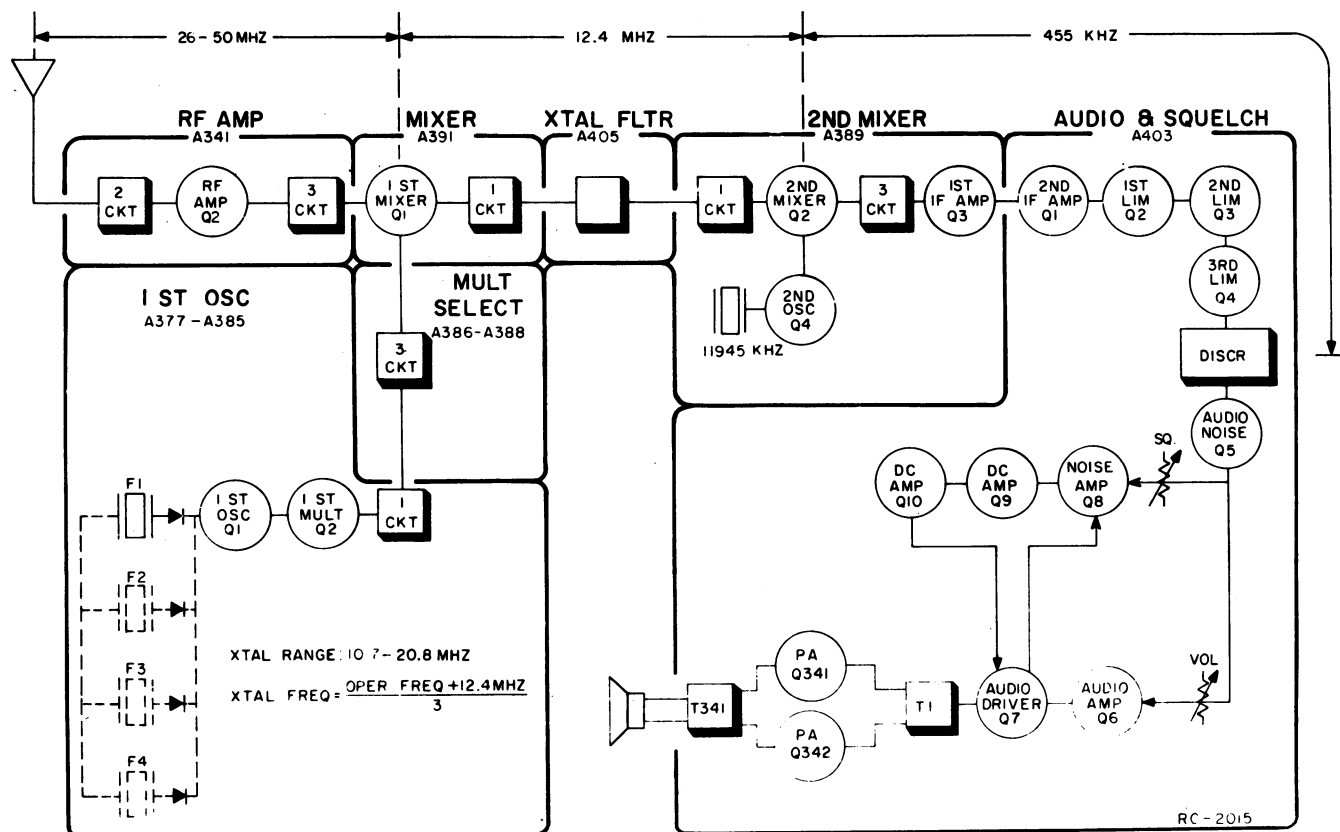


Figure 1 - Receiver Block Diagram

capacitors C341 and C342.

The RF amplifier uses a Field-Effect Transistor (FET) as the active device. The FET may be considered a semiconductor current path (or channel) whose resistance is varied by a voltage applied between the "gate" and "source" terminals. Lead identification for the FET is shown in Figure 2. The FET has voltage-controlled characteristics, any may be compared to a vacuum tube in operation (see Figure 2).

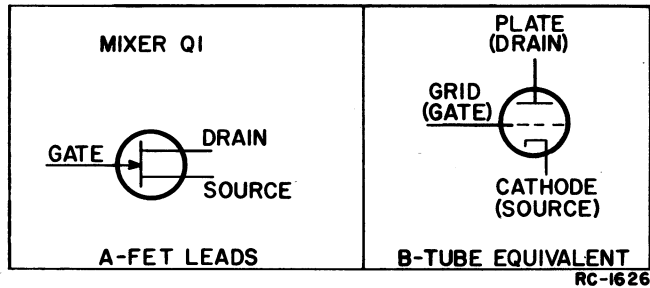


Figure 2 - FET Nomenclature

RF from the antenna is applied to the "source" terminal of FET Q1, which operates as a grounded-gate amplifier. This method of operation provides a low impedance input to the amplifier. The amplified output is taken from the "drain" terminal and coupled through three tuned circuits to the 1st mixer.

1ST OSCILLATOR AND MULTIPLIER (A377-A385)

The receiver 1st oscillator operates in a transistorized Colpitts oscillator circuit. The oscillator crystal operates in a fundamental mode at a frequency of approximately 13 to 20 megahertz. The crystal is cut to provide temperature compensation at the high end of the temperature range and is thermistor compensated at low temperatures. This provides $\pm 0.0005\%$ frequency stability as soon as the receiver is energized—without the warm-up time required by crystal ovens or warmers.

In single frequency receivers, bias for the oscillator transistor is obtained by a jumper from H1 to H2 on the oscillator board.

In multi-frequency receivers, a diode is connected in series with the crystal, and up to three additional crystal circuits can be added. The 10-volt jumper is removed and the proper frequency is selected by switching the desired crystal circuit to +10 volts by means of a frequency selector switch on the control unit.

Switching the +10 volts to the crystal circuit forward biases the diode and reduces its impedance. This applies the crystal

frequency to the base of oscillator transistor Q1. Feedback for the oscillator is developed across C21/C22. The output is coupled to the base of 1st multiplier Q2.

The output of the 1st multiplier is transformer-coupled (T9, T10, T11) to multiplier selectivity assembly A386/A388. The 1st multiplier tank is tuned to three times the crystal frequency. The stage is metered at centralized metering jack J442-4 through metering network CR1, R1, C7 and C8.

MULTIPLIER SELECTIVITY ASSEMBLY (A386/A388)

Following the 1st multiplier tank (T9/T10/T11) are three additional L-C tuned circuits (A386/A388-L1, -L2 and -L3). Capacitor C16/C26/C33 couples the multiplier selectivity output to the base of the first mixer.

1ST MIXER (A391) AND CRYSTAL FILTER (A405)

The 1st mixer uses a Field-Effect Transistor (FET) as the active device (Figure 2). The FET has several advantages over a conventional transistor, including a high input impedance, high power gain, and an output that is relatively free of harmonics (low in intermodulation products).

In 1st mixer A391, RF from the 1st RF amplifier and injection voltage from the multiplier-selectivity assembly are applied to the gate of Q1. The mixer output is taken from the drain with the output tuned to the 12.4-MHz high IF frequency.

A highly selective crystal filter (A405) following the 1st mixer provides the major selectivity for the receiver. The output of the filter is fed through impedance matching transformer A389-T2 to the base of the 2nd mixer.

2ND OSCILLATOR, 2ND MIXER AND 1ST LO IF AMPLIFIER (A389)

A389-Q4 operates in a Colpitts oscillator circuit, with feedback supplied through C18. The oscillator low-side injection voltage (11945 kHz) is applied to the base of the 2nd mixer.

The High IF signal from the filter and the injection voltage from the 2nd oscillator is applied to the base of 2nd mixer Q2. The 455-kHz mixer output is applied to three tuned low IF circuits, L1, L2 and L3. These tuned circuits are required for shaping the nose of the IF waveform, and for rejecting any undesired output frequencies from the 2nd mixer.

The low IF signal is applied to the base of 1st low IF amplifier A389-Q3. The output of Q3 is R-C coupled to the base of the 2nd low IF amplifier.

2ND LO IF AMPLIFIER AND LIMITERS (A403)

Additional amplification of the low IF signal going to the limiter stages is provided by 2nd low IF amplifier Q1. This stage is metered at J442-2 through a metering network consisting of C19, CR3 and R25.

Following the 2nd low IF amplifier are three R-C coupled limiter stages (Q2, Q3 and Q4). The 1st limiter is metered at J442-3 through metering network C20, CR4, and R26.

DISCRIMINATOR (A403)

The limiter output is applied to a Foster-Seely type discriminator, where diodes CR1 and CR2 rectify the 455-kHz signal to recover the audio. The discriminator is metered at J442-10 through metering network C44 and R23.

AUDIO - NOISE AMPLIFIER (A403)

The discriminator output is coupled through a low-pass filter (C16, C18, R21 and R22) to the base of audio-noise amplifier Q5. The filter removes any 455-kHz signal remaining in the discriminator output. Q5 operates as an emitter-follower to match the discriminator impedance to the VOLUME and SQUELCH control. The stage also provides power gain.

AUDIO AMPLIFIERS (A403)

Any audio present in the incoming signal is coupled from the emitter of Q5 through the VOLUME control and a de-emphasis network to the base of audio amplifier Q6. The de-emphasis network consists of C22, C23, C24, R30 and R31.

Audio driver Q7 follows the audio amplifier. Base bias and AC feedback for Q6 are connected from the base of Q7 through C26, R74, RT3 and C50. Negative feedback and base bias for Q7 is supplied through C51. The audio driver output is coupled through transformer T1 to provide phase inversion for the push-pull audio PA stage.

Q341 and Q342 operate as a push-pull, class AB audio PA stage. The PA output is coupled through audio transformer T341 to the loudspeaker. The yellow and white tertiary windings of T341 supply balanced feedback to the emitter of Q7. The feedback winding minimizes distortion and prevents the pick-up of external electrical noise.

The PA stage provides a 5-watt output at less than 5% distortion into a 3.5-ohm load at the receiver output terminals (3.2-ohms at the Control Unit). Base bias for the PA stage and the elimination of cross-over distortion is controlled by bias adjust potentiometer R43. The potentiometer is set

at the factory as shown in STEP 1 of the receiver Test Procedure.

NOTE

Do not adjust bias adjust potentiometer R43 unless PA transistors Q341 and Q342 have been replaced.

Audio high and low are also present at centralized metering jack J442, and can be used as shown in STEP 1 of the Test Procedure.

SQUELCH (A403)

Noise from audio-noise amplifier Q5 operates the squelch circuit. With no carrier present in the receiver, this noise is coupled to the base of noise amplifier Q8 through a high-pass filter which attenuates frequencies below 3 kHz. The filter consists of C47, C48 and R71, as well as C46 and L3 in the collector circuit of Q8. The gain of Q8 is determined by the Squelch control, which varies the bias on the base of Q8. Thermistor RT2 keeps the critical squelch constant over wide variations in temperature.

The output of noise amplifier Q8 is rectified by diodes CR5 and CR6, and filtered by C36 and C37 to produce a negative DC voltage. This DC voltage is applied to the base of DC amplifier Q9, turning it off. When turned off, the collector voltage of Q9 rises to approximately 8 volts, turning on DC amplifier Q10. When conducting, the collector voltage of Q10 drops to almost ground potential, which removes the base bias to audio amplifier Q6 and audio driver Q7, turning them off.

When the receiver is quieted by a signal (unsquelched), the noise in the receiver is reduced, turning DC amplifier Q9 on and DC amplifier Q10 off. This allows the audio stages to conduct so that sound is heard in the speaker. A network composed of C38, CR7 and R62 slows down the switching action of Q10, preventing an obnoxious "thump" from being heard in the speaker.

Resistor R73 connects from the emitter of audio driver Q7 to the emitter of noise amplifier Q8, providing a hysteresis loop in the squelch circuit. When a weak signal opens the squelch, the signal level may be reduced by 4 to 6 dB without the squelch closing. This limits squelch "flutter" or "picket-fence" operation.

With audio driver Q7 conducting, a positive voltage through R53 helps to reduce the gain of noise amplifier Q8. This positive feedback provides a quick, positive switching action in the squelch circuit.

cuit. When the receiver squelches, audio driver Q7 turns off and its emitter potential drops to zero. This reduces the DC feedback through R73 to the emitter of noise amplifier Q8. Reducing the feedback causes Q8 to conduct harder, turning the

audio stages off quickly.

Keying the transmitter removes the +10 volts from J19. This turns off DC amplifier Q9 which turns on Q10, muting the receiver.

DISASSEMBLY

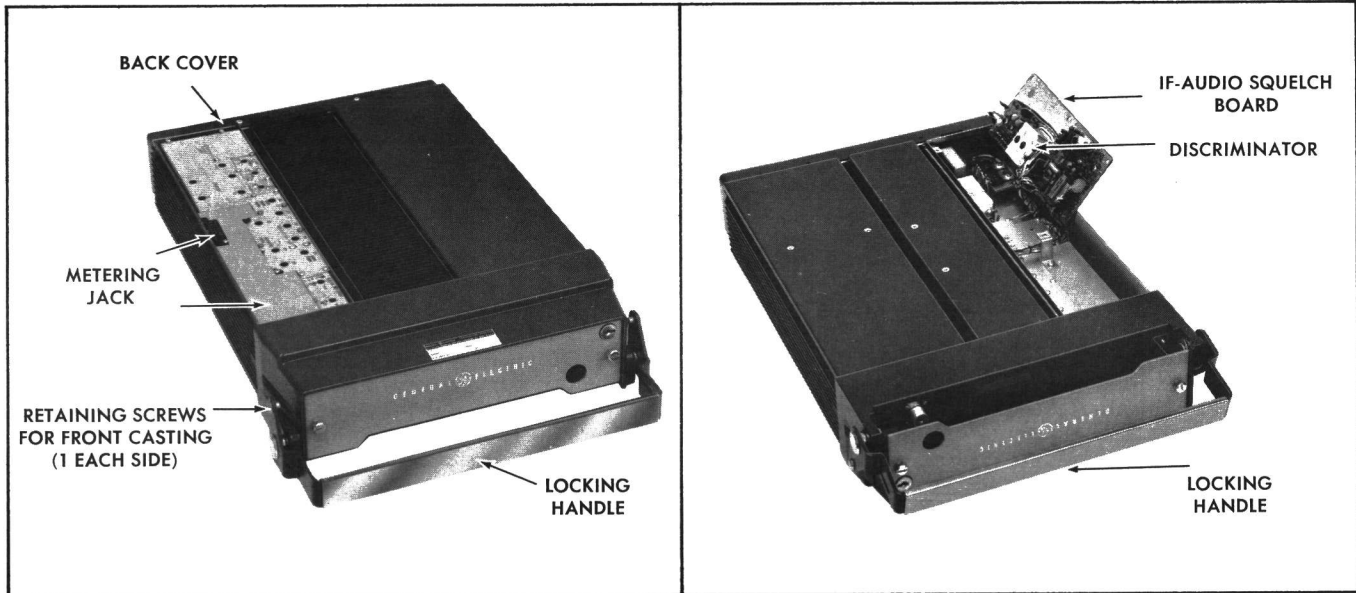


Figure 3 - Removing Top Cover

To service the receiver from the top--

1. Pull locking handle down and pull radio about one inch out of mounting frame.
2. Pry up cover at rear of receiver.
3. Slide cover back and lift off.

To service the receiver from the bottom--

1. Pull locking handle down. Pull radio out of mounting frame.
2. Remove screws in bottom cover. Pry up cover at back of receiver.
3. Slide cover back and lift off.

Figure 4 - Removing Bottom Cover

To remove the receiver from the system frame--

1. Loosen the two Phillips-head retaining screws in from casting (see Figure 2), and pull casting away from system frame.
2. Remove the four screws in the back cover.
3. Remove the two screws holding the receiver at each end of the system frame.
4. Disconnect the antenna jack and the 20-pin connector from the front of the receiver, and slide the unit out of the system frame.

FRONT END ALIGNMENT

EQUIPMENT REQUIRED

1. GE Test Set Models 4EX3A10, 4EX8K11 (or 20,000 ohms-per-volt Multimeter with a 1-volt scale).
2. A 455 kHz and 25-50 MHz signal source. Connect a one-inch piece of insulated wire no larger than .065-inch diameter to generator output probe.

PRELIMINARY CHECKS AND ADJUSTMENTS

1. Connect Test Set to Receiver Centralized Metering Jack J442 and set meter sensitivity switch to the TEST 1 position (or 1-volt position on 4EX8K10, 11).
2. With Test Set in Position J, check for regulated +10 volts. If using Multimeter, measure from C360 to C361.
3. If using Multimeter, connect the positive lead to J442-16 (Ground).

ALIGNMENT PROCEDURE

STEP	METERING POSITION GE Test Set - at J442	TUNING CONTROL	METER READING	PROCEDURE
OSCILLATOR/MULTIPLIER				
1.	D (MULT-1) Pin 4	L4 (on 1st OSC/MULT) and L1, L2 and L3 (on MULT SELECTIVITY)	See Procedure	Tune L4 on 1st OSC/MULT and L1 on MULTI SELECTIVITY for maximum meter reading. Tune L2 for minimum meter reading. Then tune L3 for maximum meter reading. Change voltage scale if necessary.
RF AMPLIFIER & SELECTIVITY				
2.	A (DISC) Pin 10		Zero	Apply an on-frequency signal adjacent to L7. Adjust the signal generator for discriminator zero.
3.	B (2nd IF Amp) Pin 2	L1 (1st RF Amp), L6, L7, C341 and C342 (RF SELECTIVITY)	Maximum	Apply an on-frequency signal to the antenna jack, keeping below saturation. Tune L1, L6, L7, C341, and C342 for maximum meter reading.
4.	"	L4 (1st OSC/MULT) and L1, L2 and L3 (MULT SELECTIVITY)	Maximum	Apply an on-frequency signal as above, keeping below saturation. Tune L4 on 1st OSC/MULT and L1, L2 and L3 on MULT SELECTIVITY for maximum meter reading.
FREQUENCY ADJUSTMENT				
5.	A (DISC) Pin 10	C9 on 1st OSC/MULT (C10, C11 or C12 for multi-frequency)	Zero	Apply an on-frequency signal to the antenna jack. Tune C9 for zero discriminator reading. In multi-frequency units, tune C10, C11 or C12 as required. NOTE For proper frequency control of the receiver, it is recommended that all frequency adjustments be made when the equipment is at a temperature of approximately 75°F. In no case should frequency adjustments be made when the temperature is outside the temperature range of 50° to 90° F.

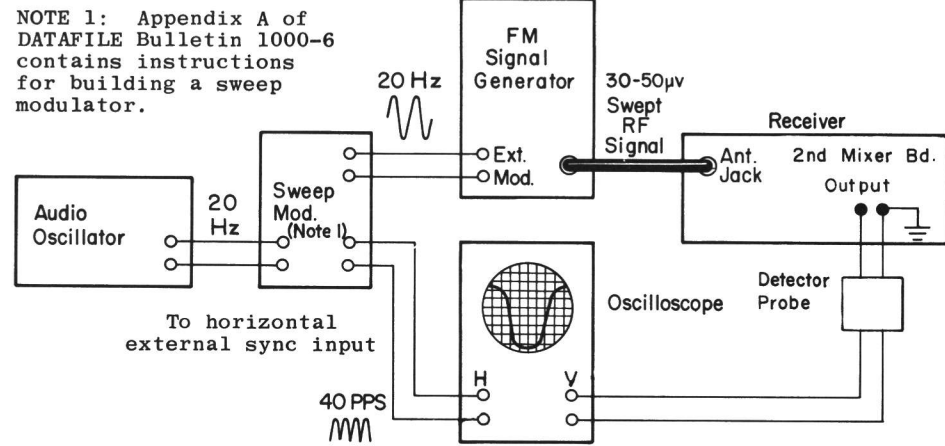
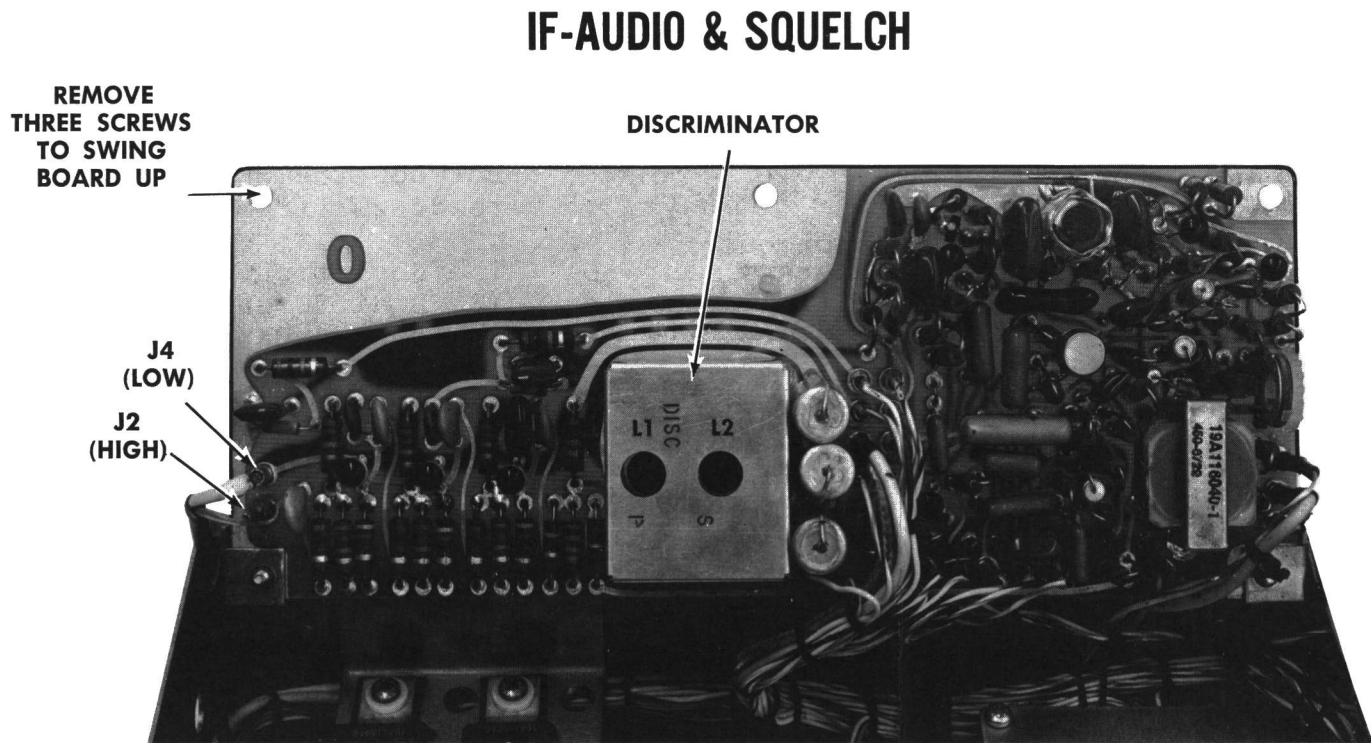
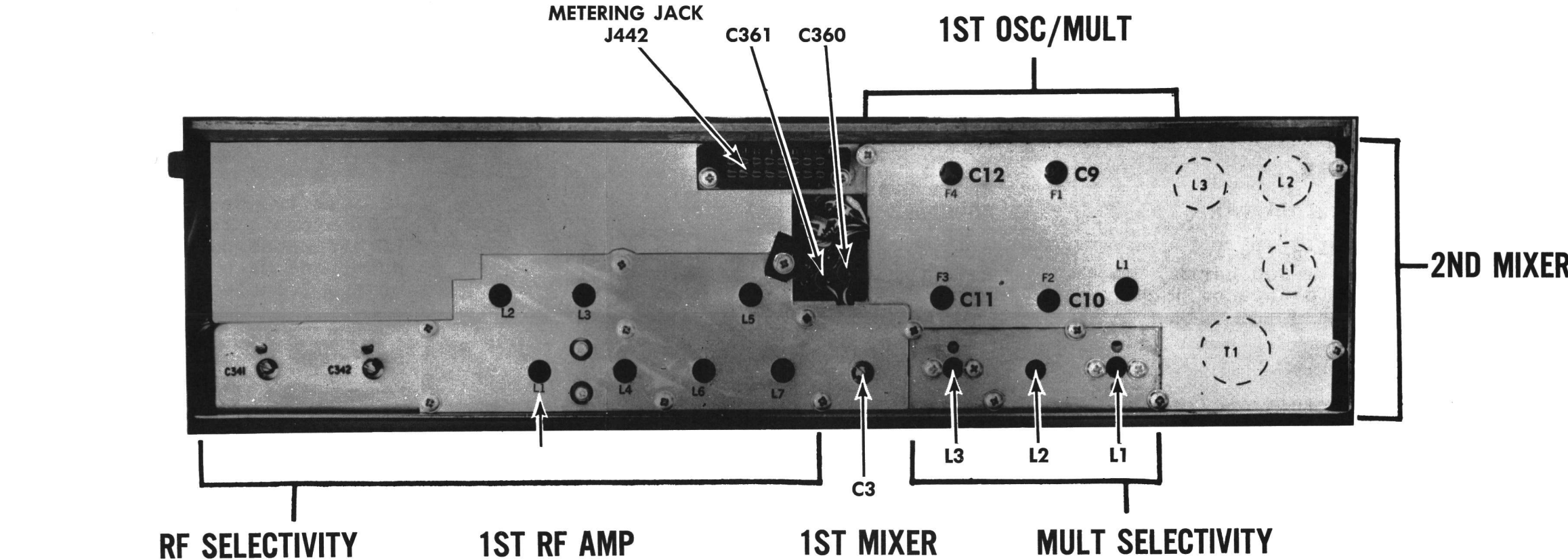


Figure 4 - Test Setup for 20-Hz Double-Trace Sweep Alignment

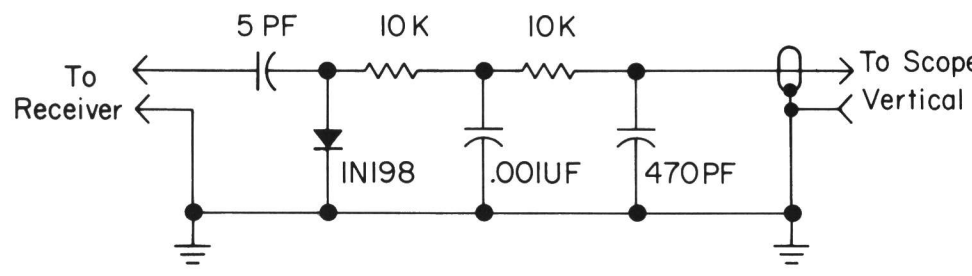


Figure 5 - Detector Probe for Sweep Alignment

COMPLETE RECEIVER ALIGNMENT

EQUIPMENT REQUIRED

1. GE Test Set Models 4EX3A10, 4EX8K11 (or 20,000 ohms-per-volt Multimeter with a 1-volt scale).
2. A 455-kHz signal source (GE Test Set Model 4EX7A10) and 25-50 MHz signal source. Connect a one-inch piece of insulated wire no larger than .065-inch diameter to generator output probe.

PRELIMINARY CHECKS AND ADJUSTMENTS

1. Connect Test Set to Receiver Centralized Metering Jack J442, and set meter sensitivity switch to the TEST 1 position (or 1-volt position on 4EX8K10, 11).
2. For a large change in frequency or a badly mis-aligned receiver, set crystal trimmer C9 to mid-capacity. In multi-frequency receivers, set C10, C11 or C12 to mid-capacity as required. Where the maximum frequency spacing is less than 100 kHz, align the unit on channel F1. If the frequency spacing is greater than 100 kHz, align the receiver on the center frequency.
3. With Test Set in Position J, check for regulated +10 volts. If using Multimeter, measure from C360 to C361.
4. If using Multimeter, connect the positive lead to J442-16 (Ground).

ALIGNMENT PROCEDURE

STEP	METERING POSITION GE Test Set - at J442	TUNING CONTROL	METER READING	PROCEDURE
DISCRIMINATOR & OSCILLATOR				
1.	A (DISC) Pin 10	L1 and L2 (on IF-AUDIO SQUELCH board)	Zero	Remove three screws and swing open the IF-AUDIO & SQUELCH board. Adjust L1 (disc primary) 1/2 turn counterclockwise from the bottom of coil. Next, apply a 455-kHz signal to J2 and J4 and adjust L2 (disc secondary) for zero meter reading.
2.	A (DISC) Pin 10		See Procedure	Alternately apply a 455-kHz and 460-kHz signal and check for readings of at least 0.2 volt, but not more than 0.3 volt. Both readings must be within 0.05 volt. Do not attempt to balance readings any closer than 0.55 volt.
3.	D (MULT-1) Pin 4	L4 (on 1st OSC/MULT) and L1, L2 & L3 (on MULT SELECTIVITY)	See Procedure	Tune L4 on 1st OSC/MULT and L1 on MULT SELECTIVITY for maximum meter reading. Tune L2 for minimum meter reading. Change voltage scale if necessary. Then tune L3 for maximum meter reading. Repeat step 3.
RF AMPLIFIER & SELECTIVITY				
4.	A (DISC) Pin 10		Zero	Apply an on-frequency signal adjacent to L7. Adjust the signal generator for discriminator zero.
5.	B (2nd IF Amp) Pin 2	L7 and L6 (RF SELECTIVITY)	Maximum	Apply an on-frequency signal and tune for maximum meter reading as shown below, keeping signal below saturation. Apply Signal Generator Probe to: L7 L6 Tune: L7 L6
6.	"	C341, C342 and L1 (1st RF Amp)	Maximum	Apply an on-frequency signal to the antenna jack. Tune C341, C342 and L1 for maximum meter reading, keeping signal below saturation.
7.	"	L1 (1st RF Amp), L6, L7, C341, and C342 (RF SELECTIVITY)	Maximum	Apply an on-frequency signal as above, keeping below saturation. Tune L1, L6, L7, C341 and C342 for maximum meter reading.
8.	"	L3 (MULT SELECTIVITY)	Maximum	Apply on-frequency signal as above, keeping below saturation. Tune L2 & L3 (on MULT SELECTIVITY) or maximum meter reading.
2ND MIXER & LO IF				
NOTE The 1st and 2nd mixer, and low IF circuits have been aligned at the factory and will normally require no further adjustment. If adjustment is necessary, use the procedure outlined in STEPS 9, 10, and 11. L2 (on 1st mixer) does not peak, but provides impedance matching for the crystal filter input and should only be tuned when observing IF trace on oscilloscope.				
NOTE Refer to DATAFILE BULLETIN 1000-6 IF Alignment of Two-Way Radio FM Receivers for helpful suggestions on how to determine when IF Alignment is required.				
9.	B (2nd IF Amp) Pin 2	L3, L2, L1, T1 (2nd Mixer)	Maximum	Apply on-frequency, unmodulated signal and tune L3, L2, L1, and T1 for maximum meter reading, keeping signal below saturation.
10.		L3, L2, L1, T1 (2nd Mixer)		Connect scope, signal generator, and detector as shown in Figure 4. Set signal generator level for 3-50 µv and modulate with 16-20 kHz at 20 Hz. With detector at the collector of Q3 (2nd mixer board output), tune L3, L2, L1, T1 (2nd Mixer) and L2 (1st Mixer) or double trace as shown on scope pattern.
11.	A (DISC) Pin 10		See Procedure	Check to see that discriminator idling voltage is within ±.05 volt of zero with no signal applied. Check to see that modulation acceptance bandwidth is between ±15 and 19 kHz.
FREQUENCY ADJUSTMENT				
12.	A (DISC) Pin 10	C9 on 1st OSC (C10, C11 or C12 for multi-frequency)	Zero	Apply an on-frequency signal to the antenna jack. Tune C9 for zero discriminator reading. In multi-frequency units, tune C10, C11 or C12 as required. NOTE For proper frequency control of the receiver, it is recommended that all frequency adjustments be made when the equipment is at a temperature of approximately 75°F. In no case should frequency adjustments be made when the equipment is outside the temperature range of 50° to 90°F.

ALIGNMENT PROCEDURE

26—50 MHz MASTR RECEIVER
MODELS 4ER39D10—28

TEST PROCEDURES

These Test Procedures are designed to help you to service a receiver that is operating---but not properly. The problems encountered could be low power, poor sensitivity, distortion, limiter not operating properly, and low gain. By following the sequence of test steps starting with Step 1, the defect can be quickly localized. Once

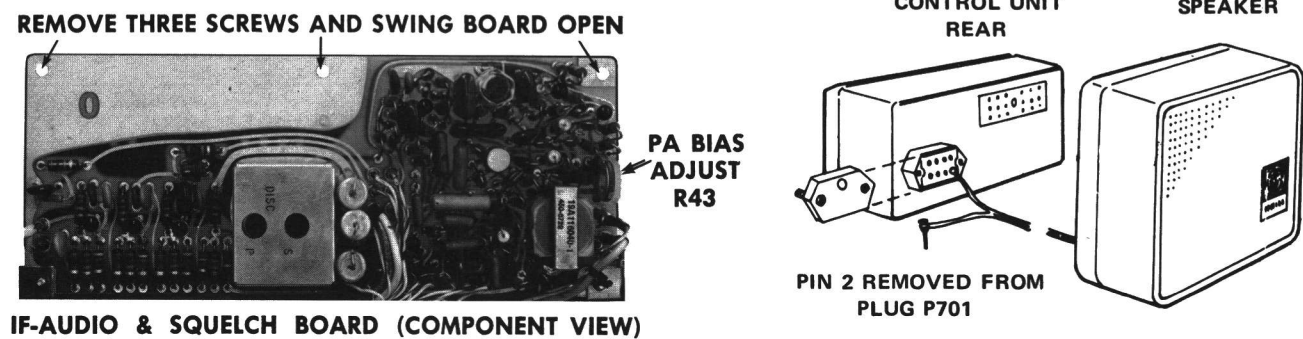
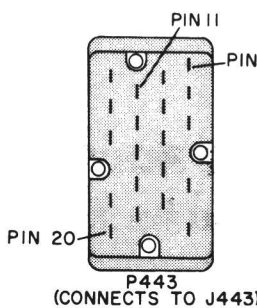
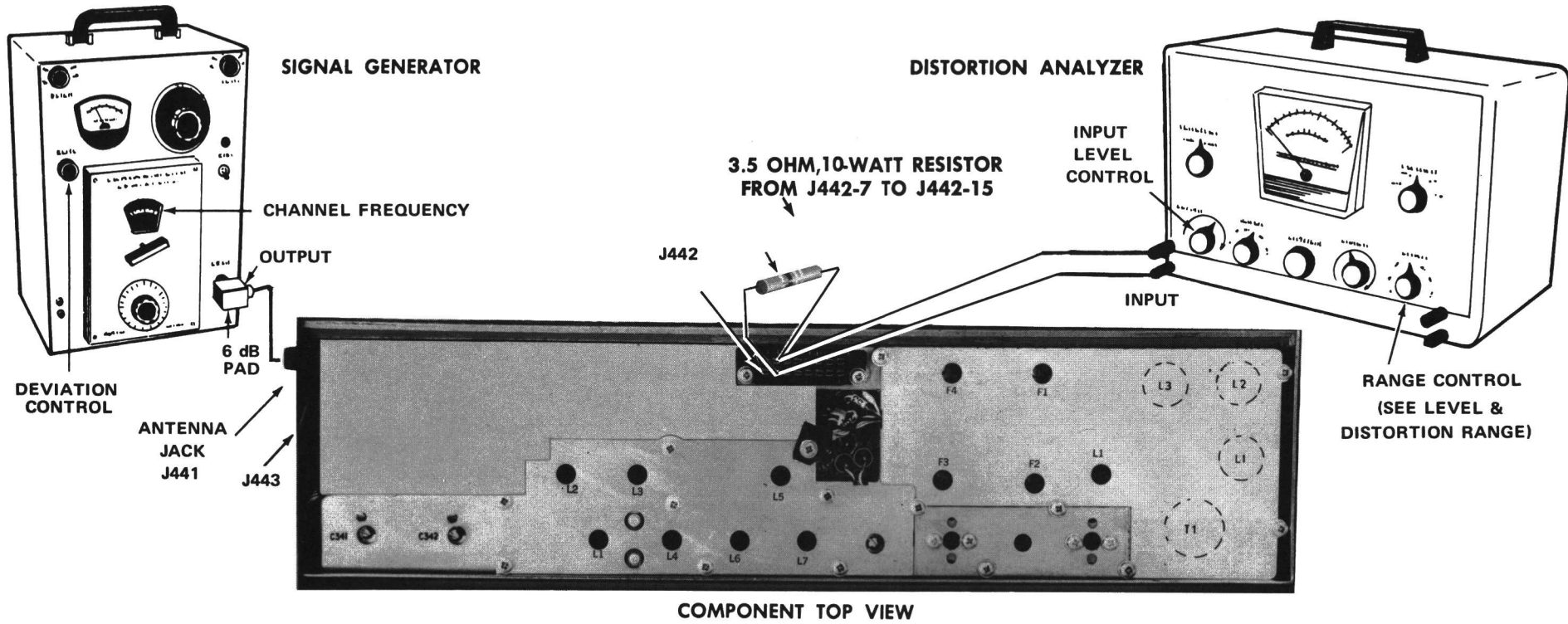
the defective stage is pin-pointed, refer to the "Service Check" listed to correct the problem. Additional corrective measures are included in the Troubleshooting Procedure. Before starting with the Receiver Test Procedures, be sure the receiver is tuned and aligned to the proper operating frequency.

TEST EQUIPMENT REQUIRED

- Distortion Analyzer similar to: Heath IM-12
- Signal Generator similar to: Measurements M-560
- 6-dB attenuation pad, and 3.5-ohm, 10-watt resistor

PRELIMINARY ADJUSTMENTS

1. Connect the test equipment to the receiver as shown for all steps of the receiver Test Procedure.
2. Turn the SQUELCH control fully clockwise for all steps of the Test Procedure.
3. Turn on all of the equipment and let it warm up for 20 minutes.



STEP 1
AUDIO POWER OUTPUT
AND DISTORTION

TEST PROCEDURE

Measure Audio Power Output as follows:

- A. Apply a 1,000-microvolt, on-frequency test signal modulated by 1,000 hertz with ± 3.3 kHz deviation to antenna jack J441.
- B. With Five-Watt Speaker:

Disconnect speaker lead pin from J701-2 (on rear of Control Unit).

Connect a 3.5-ohm load resistor from J442-15 to J442-7. Connect the Distortion Analyzer input across the resistor as shown.

OR

With Handset:

Lift the handset off of the hookswitch. Connect the Distortion Analyzer input from J442-15 to J442-7.

- C. Adjust the VOLUME control for five-watt output (4.18 VRMS using the Distortion Analyzer as a VTVM).
- D. Make distortion measurements according to manufacturer's instructions. Reading should be less than 5%. If the receiver sensitivity is to be measured, leave all controls and equipment as they are.

SERVICE CHECK

If the distortion is more than 5%, or maximum audio output is less than five watts, make the following checks:

- E. Battery and regulator voltage---low voltage will cause distortion. (Refer to Receiver Schematic Diagram for voltages.)
- F. P.A. Bias Adjust (R43) -- Turn the SQUELCH control fully counterclockwise.

Then connect a milliammeter in series with the +12 volt lead at P443-11. With no signal in, adjust R43 for a reading of approximately 20 milliamps. This adjustment should not be necessary unless an output transistor has been replaced.

- G. Audio Gain (Refer to Receiver Troubleshooting Procedure).
- H. Discriminator Alignment (Refer to Receiver Alignment on reverse side of page).

STEP 2
USABLE SENSITIVITY
(12-dB SINAD)

If STEP 1 checks out properly, measure the receiver sensitivity as follows:

- A. Apply a 1000-microvolt, on-frequency signal modulated by 1000 Hz with 3.3-kHz deviation to J441.
- B. Place the RANGE switch on the Distortion Analyzer in the 200 to 2000-Hz distortion range position (1000-Hz filter in the circuit). Tune the filter for minimum reading or null on the lowest possible scale (100%, 30%, etc.)
- C. Place the RANGE switch to the SET LEVEL position (filter out of the circuit) and adjust the input LEVEL control for a +2 dB reading on a mid range (30%).
- D. While reducing the signal generator output, switch the RANGE control from SET LEVEL to the distortion range until a 12-dB difference (+2 dB to -10 dB) is obtained between the SET LEVEL and distortion range positions (filter out and filter in).
- E. The 12-dB difference (Signal plus Noise and Distortion to noise plus distortion ratio) is the "usable" sensitivity level. The sensitivity should be less than 0.35 microvolts with an audio output of at least 2.5 watts (2.9 volts RMS across the 3.5-ohm receiver load using the Distortion Analyzer as a VTVM).

- F. Leave all controls as they are and all equipment connected if the Modulation Acceptance Bandwidth test is to be performed.

SERVICE CHECK

If the sensitivity level is more than 0.25 microvolts, check the alignment of the RF stages as directed in the Alignment Procedure, and make the gain measurements as shown on the Troubleshooting Procedure.

STEP 3
MODULATION ACCEPTANCE
BANDWIDTH (IF BANDWIDTH)

If STEPS 1 and 2 check out properly, measure the bandwidth as follows:

- A. Set the Signal Generator output for twice the microvolt reading obtained in the 12-dB SINAD measurement.
- B. Set the RANGE control on the Distortion Analyzer in the SET LEVEL position (1000-Hz filter out of the circuit), and adjust the input LEVEL control for a +2 dB reading on the 30% range.
- C. While increasing the deviation of the Signal Generator, switch the RANGE control from SET LEVEL to distortion range until a 12-dB difference is obtained between the SET LEVEL and distortion range readings (from +2 dB to -10 dB).
- D. The deviation control reading for the 12-dB difference is the Modulation Acceptance Bandwidth of the receiver. It should be more than ± 15 kHz (but less than ± 19 kHz).

SERVICE CHECK

If the Modulation Acceptance Bandwidth test does not indicate the proper width, make gain measurements as shown on the Receiver Troubleshooting Procedure.

STEP 1 - QUICK CHECKS

TEST SET CHECKS

These checks are typical voltage readings measured with GE Test Set Model 4EX3A10 in the Test 1 position, or Model 4EX8K10 or 11 in the 1-volt position.

Metering Position	Reading With No Signal In	Reading with 1 Micro- volt Unmodulated
A (Disc Idling)	Less than $\pm .05$ VDC	
B (2nd IF)	.15 VDC	.7 VDC
C (1st Lim)	.7 VDC	0.8 VDC
D (Mult-1)	0.6 VDC	
J (Reg. +10 volts)	+10 VDC	

SYMPTOM CHECKS

SYMPTOM	PROCEDURE
NO SUPPLY VOLTAGE	<ul style="list-style-type: none"> ● Check power connections and continuity of supply leads, and check fuse in power supply. If fuse is blown, check receiver for short circuits.
NO REGULATED 10-VOLTS	<ul style="list-style-type: none"> ● Check the 12-volt supply. Then check regulator circuit. (See Troubleshooting Procedure for Power Supply).
LOW 1ST LIM READING	<ul style="list-style-type: none"> ● Check supply voltages and then check oscillator reading at J442-4 & 5 as shown in STEP 2A. ● Make SIMPLIFIED VTVM GAIN CHECKS from 2nd Mixer through 1st Limiter stages as shown in STEP 2A.
LOW OSCILLATOR/MULTIPLIER READINGS	<ul style="list-style-type: none"> ● Check alignment of Oscillator. (Refer to Front End Alignment Procedure). ● Check voltage and resistance readings of 1st Oscillator/Multiplier Q1/Q2. ● Check crystal Y1.
LOW RECEIVER SENSITIVITY	<ul style="list-style-type: none"> ● Check Front End Alignment. (Refer to Receiver Alignment Procedure). ● Check antenna connections, cable and relay. ● Check 1st and 2nd Oscillator injection voltage. ● Check voltage and resistance readings of 1st Mixer, HI IF Amp and 2nd Mixer. ● Make SIMPLIFIED GAIN CHECKS (STEP 2A).
LOW AUDIO	<ul style="list-style-type: none"> ● Check Audio PA (Q341 & Q342) voltage readings on schematic diagram. ● Make simplified gain and waveform checks of audio and squelch stages. (Steps 2A and 2B). ● Make unsquelched voltage readings in Audio section. (Refer to Receiver Schematic Diagram). ● Check voltage and resistance readings on Channel Guard board.
HIGH DISTORTION AT LOW AUDIO LEVELS (50 MW)	<ul style="list-style-type: none"> ● Set PA bias adjust R43 as specified under Service. ● Checks in STEP 1 of TEST PROCEDURES.
IMPROPER SQUELCH OPERATION	<ul style="list-style-type: none"> ● Check voltage and resistance readings of Squelch circuit. (Refer to Receiver Schematic Diagram). ● Make gain and waveform checks of audio and squelch stages. (Steps 2A and 2B).
DISCRIMINATOR IDLING TOO FAR OFF ZERO	<ul style="list-style-type: none"> ● See if discriminator zero is in center of IF bandpass.

STEP 3-VOLTAGE RATIO READINGS

EQUIPMENT REQUIRED:

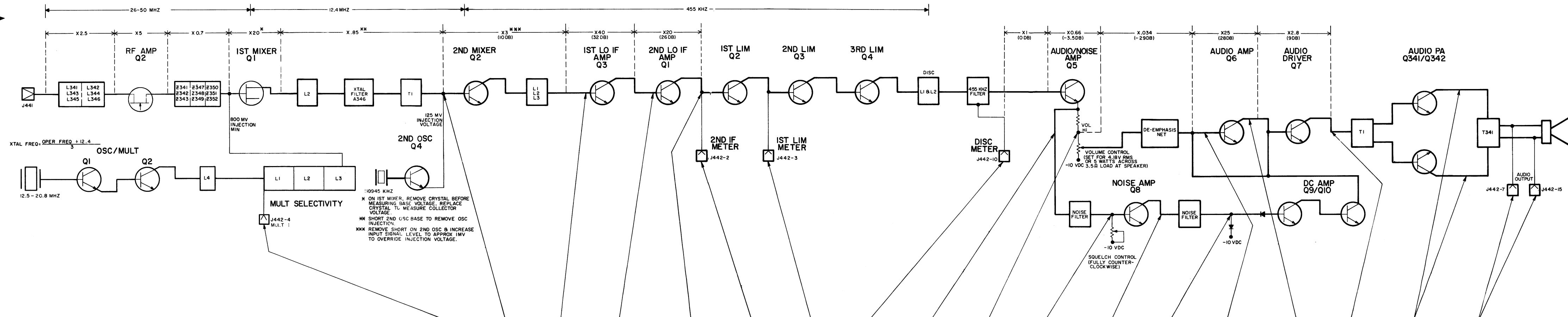
1. RF VOLTMETER (SIMILAR TO BOONTON MODEL 91-CA OR MILLIVAC TYPE MV-18 C.
2. SIGNAL ON RECEIVER FREQUENCY (BELOW SATURATION) CORRECT FREQUENCY CAN BE DETERMINED BY ZEROING THE DISCRIMINATOR. USE 1,000 HERTZ SIGNAL WITH 3.3 KHZ DEVIATION.

PROCEDURE :

1. APPLY PROBE TO INPUT OF STAGE (FOR EXAMPLE, BASE OF RF AMP). PEAK RESONANT CIRCUIT OF STAGE BEING MEASURED AND TAKE VOLTAGE READING (E_1).
2. MOVE PROBE TO INPUT OF FOLLOWING STAGE. REPEAT FIRST RESONANT CIRCUIT THEN PEAK CIRCUIT BEING MEASURED AND TAKE READING (E_2).
3. CONVERT READINGS BY MEANS OF THE FOLLOWING FORMULA

$$\text{VOLTAGE RATIO} = \frac{E_2}{E_1}$$

4. CHECK RESULTS WITH TYPICAL VOLTAGE RATIOS SHOWN ON DIAGRAM.



STEP 2A-SIMPLIFIED VTVM GAIN CHECKS

EQUIPMENT REQUIRED:

1. Oscilloscope.
2. Signal generator (Measurements M560 to equivalent).

PRELIMINARY STEPS:

1. Set VOLUME control for 4.18 volts across 3.5-ohm load. If this cannot be obtained, set to approx. 70% of max. rotation.
2. Set SQUELCH control fully counterclockwise.
3. Receiver should be properly aligned.
4. Connect oscilloscope between system negative and points indicated by arrow.

SIGNAL GENERATOR INPUT AT J441 MAINTAIN SETTING AT DISCRIMINATOR ZERO	UNMODULATED	UNMODULATED	UNMODULATED	UNMODULATED	1 MICROVOLT UNMODULATED	NO SIGNAL INPUT	STANDARD SIGNAL—(1 MILLIVOLT AT 1000 HZ MODULATED BY 10% WITH 3.5KHZ DEVIATION)	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL	STANDARD SIGNAL
PROCEDURE	INCREASE GENERATOR OUTPUT UNTIL VTVM READING ON I-15 SCALE DECREASES BY 50 MIV	INCREASE SIGNAL GENERATOR OUTPUT FROM ZERO UNTIL VTVM READING DECREASES BY 5%	INCREASE SIGNAL GENERATOR OUTPUT FROM ZERO UNTIL VTVM READING DECREASES BY 5%	INCREASE SIGNAL GENERATOR OUTPUT FROM ZERO UNTIL VTVM READING DECREASES BY 5%														CONNECT VTVM OR SCPEE TO J442-7 AND J442-15 WITH SPEAKER DISCONNECTED.
READING	2.4 VDC GENERATOR OUTPUT SHOULD BE APPROX 20 MILLIVOLTS	2.4 VDC GENERATOR OUTPUT SHOULD BE APPROX 600 MICROVOLTS	2.4 VDC GENERATOR OUTPUT SHOULD BE APPROX 600 MICROVOLTS	2.4 VDC GENERATOR OUTPUT SHOULD BE APPROX 600 MICROVOLTS	-0.6 VDC	-2 VDC	0.8 VAC	0.75 VAC	0.55 VAC	0.15 VAC	2.3 VAC	0.05 VAC	0.5 VAC	1.4 VAC	10 VAC	4.18 VAC		

STEP 2B-AUDIO & SQUELCH WAVEFORMS

EQUIPMENT REQUIRED:

1. VTVM-AC & DC
2. Signal generator (Measurements M560 to equivalent).

PRELIMINARY STEPS:

1. Set VOLUME control for 4.18 volts across 3.5-ohm load. If this cannot be obtained, set to approx. 70% of max. rotation.
2. Set SQUELCH control fully counterclockwise.
3. Receiver should be properly aligned.
4. Connect VTVM between system negative and points indicated by arrow.

SCOPE SETTING	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV	0.5 MS/DIV
	1 VOLT/DIV	1 VOLT/DIV	1 VOLT/DIV	2 VOLTS/DIV	0.5 VOLTS/DIV	50 MILLIVOLTS/DIV	1 VOLT/DIV	2 VOLTS/DIV	10 VOLTS/DIV	5 VOLTS/DIV	
	2 V P-P	1.4 V P-P	1 V P-P (NOISE)	5.7 V P-P (NOISE)	3 V P-P (NOISE)	0.1 V P-P	1.9 V P-P	4 V P-P	30 V P-P	12 V P-P	
STANDARD SIGNAL (1 MILLIVOLT AT RECEIVER FIRED MODULATED BY 1KHZ WITH 3.3 KHZ DEVIATION)											
NOISE WAVE FORM (NO SIGNAL INPUT)											

TROUBLESHOOTING PROCEDURE

26-50 MHz MASTR RECEIVER MODELS 4ER39D20-28

Issue 1

(19B204412, Sh. 1, Rev. 5)
(19B204412, Sh. 2, Rev. 5)

(19B216045, Sh. 1, Rev. 4)
(19B216045, Sh. 2, Rev. 2)

19C311803, Sh. 1, Rev. 4
19C311803, Sh. 2, Rev. 6

ALTERNATE L1 & L2

PIN NUMBER MARKING ON SIDE OF COIL CAN OR COLOR DOT IDENTIFIES PIN NUMBER 1 ON DISCRIMINATOR AND 2ND MIXER COILS

A collection of 10 numbered circles, some containing a small drawing of a person. The numbers are: C364, C363, C362, C359, C365, C358, C367, C361, C360, and a circle with a drawing of a person.

A diagram of a 3x3 grid. The top row contains a circle with a vertical line through its center, labeled 'C4' to its left. The middle row contains a circle with a vertical line through its center, labeled 'C3' to its left. The bottom row contains a circle with a vertical line through its center, labeled 'S' to its left. To the right of the bottom row, there is a circle with a vertical line through its center, labeled 'Q2' to its left. To the right of the middle row, there is a circle with a vertical line through its center, labeled 'D' to its left. To the right of the top row, there is a circle with a vertical line through its center, labeled 'G' to its left.

← RUNS ON SOLDER SIDE

RUNS ON BOTH SIDES

← RUNS ON COMPONENT SIDE

NOTE LEAD ARRANGEMENT, AND NOT
CASE SHAPE, IS DETERMINING
FACTOR FOR LEAD IDENTIFICATION.

TRANSISTOR LEAD
IDENTIFICATION

FET LEAD IDENTIFICATION

D S G OR G D S

IN-LINE TRIANGULAR

(19R621264, Rev. 13)

PARTS LIST

LBI-4166A

25-50 MHz RECEIVER
MODELS 4ER39D20 - 4ER39D28

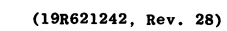
SYMBOL	GE PART NO.	DESCRIPTION
A341		RF AMPLIFIER ASSEMBLY 19B204772G3
		----- CAPACITORS -----
C3 and C4	5493392P7	Ceramic, feed-thru: 1000 pf ±100% -0%, 500 VDCW; sim to Allen-Bradley Type P45C.
C9 and C10	5494481P11	Ceramic disc: 1000 pf ±20%, 1000 VDCW; sim to RMC Type JF Discap.
		----- INDUCTORS -----
L1	7491382P101	Coil, RF: 100 µh ±10%, 4 ohms DC res max; sim to Delevan 3500 Series.
		----- TRANSISTORS -----
Q2*	19A116960P1	N Type, field effect.
		In 19E500873G9 of REV G and earlier: In 19E500873G10, G11 of REV H and earlier:
	19A115953P1	N Channel; sim to T1S34.
		----- RESISTORS -----
R10	3R152P470J	Composition: 47 ohms ±5%, 1/4 w.
R11 and R12	3R152P101J	Composition: 100 ohms ±5%, 1/4 w.
		----- SOCKETS -----
XQ2	5490277P5	Transistor: 3 contacts rated at 1 amp at 400 VRMS; sim to Alcon 1213LL2.
A377 thru A385		FIRST OSCILLATOR ASSEMBLY A377 19B204419G25 (4ER39D20) A378 19B204419G28 (4ER39D21) A379 19B204419G31 (4ER39D22) A380 19B204419G26 (4ER39D23) A381 19B204419G29 (4ER39D24) A382 19B204419G32 (4ER39D25) A383 19B204419G27 (4ER39D26) A384 19B204419G30 (4ER39D27) A385 19B204419G33 (4ER39D28)
		----- CAPACITORS -----
C1 thru C4	5494481P112	Ceramic disc: 1000 pf ±10%, 1000 VDCW; sim to RMC Type JF Discap.
C5 thru C8	5496219P751	Ceramic disc: 33 pf ±5%, 500 VDCW, temp coef -750 PPM.
C9 thru C12	5491271P106	Variable, subminiature: approx 2.1-12.7 pf, 750 v peak; sim to EF Johnson 189.
C13 thru C16.	5496219P40	Ceramic disc: 9 pf ±0.25 pf, 500 VDCW, temp coef 0 PPM.
C17 thru C20	19C300685P93	Ceramic disc: 5 pf ±0.1 pf, 500 VDCW, temp coef 0 PPM.
C21	5496219P771	Ceramic disc: 220 pf ±5%, 500 VDCW, temp coef -750 PPM.
C22	5496219P773	Ceramic disc: 270 pf ±5%, 500 VDCW, temp coef -750 PPM.
C23	5494481P114	Ceramic disc: 2000 pf ±10%, 1000 VDCW; sim to RMC Type JF Discap.
C26 thru C28	5494481P112	Ceramic disc: 1000 pf ±10%, 1000 VDCW; sim to RMC Type JF Discap.

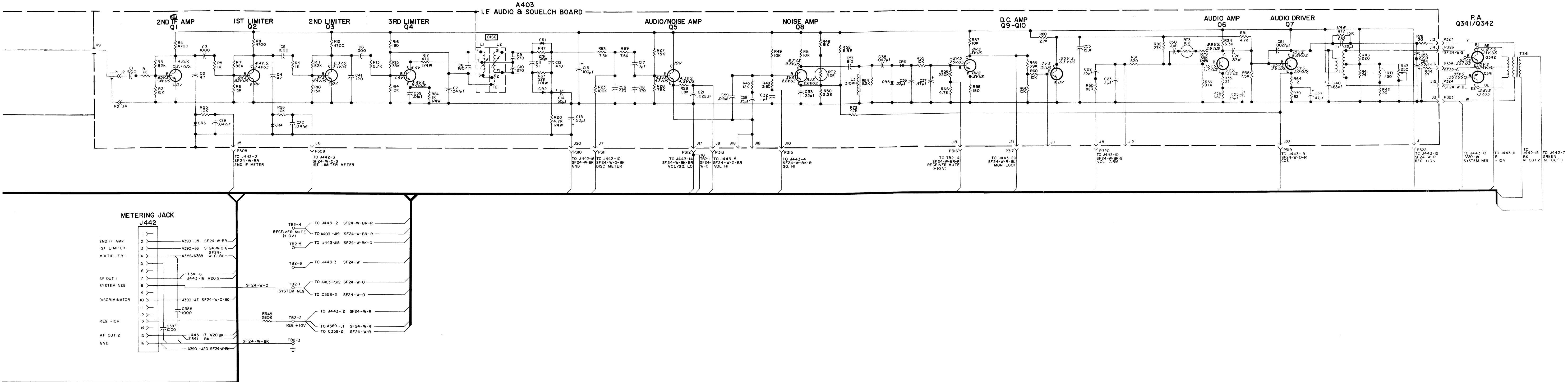
SYMBOL	GE PART NO.	DESCRIPTION
C31	5494481P112	Ceramic disc: 1000 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C45	5490008P35	Silver mica: 220 pf $\pm 5\%$, 500 VDCW.
C46	5496218P563	Ceramic disc: 100 pf $\pm 5\%$, 500 VDCW, temp coef -330 PPM.
		- - - - - DIODES AND RECTIFIERS - - - - -
CR1 thru CR4	19A115603P1	Silicon.
		- - - - - JACKS AND RECEPTACLES - - - - -
J1 thru J6	4033513P4	Contact, electrical: sim to Bead Chain L93-3.
		- - - - - INDUCTORS - - - - -
L2 and L3	7488079P16	Choke, RF: 10 μ h $\pm 10\%$ ind at 640 ma, 0.6 ohm DC res; sim to Jeffers 4421-7K.
		- - - - - TRANSISTORS - - - - -
Q1 and Q2	19A115330P1	Silicon, NPN.
		- - - - - RESISTORS - - - - -
R1 thru R4	3R152P562J	Composition: 5600 ohms $\pm 5\%$, 1/4 w.
R5 thru R8	3R152P104K	Composition: 0.1 megohm $\pm 10\%$, 1/4 w.
R9	3R152P153J	Composition: 15,000 ohms $\pm 5\%$, 1/4 w.
R10	3R152P101K	Composition: 100 ohms $\pm 10\%$, 1/4 w.
R11 and R12	3R152P102J	Composition: 1000 ohms $\pm 5\%$, 1/4 w.
R14	3R152P103J	Composition: 10,000 ohms $\pm 5\%$, 1/4 w.
R15	3R152P101K	Composition: 100 ohms $\pm 10\%$, 1/4 w.
R19	3R152P360J	Composition: 36 ohms $\pm 5\%$, 1/4 w.
R21	3R152P750J	Composition: 75 ohms $\pm 5\%$, 1/4 w.
		- - - - - THERMISTORS - - - - -
RT1 thru RT4	19B209284P5	Disc: 43 ohms res nominal at 25°C, color code green.
RT10 thru RT13	19B209284P7	Disc: 175 ohms DC res; sim to GE 3D2115.
		- - - - - TRANSFORMERS - - - - -
T9 thru T11		COIL ASSEMBLY T9 19B205416G3 T10 19B205416G4 T11 19B205416G5
		- - - - - CAPACITORS - - - - -
C52	5496218P249	Ceramic disc: 27 pf $\pm 5\%$, 500 VDCW, temp coef -80 PPM.
C53	5496218P245	Ceramic disc: 18 pf $\pm 5\%$, 500 VDCW, temp coef -80 PPM.
C54	5496218P243	Ceramic disc: 13 pf $\pm 5\%$, 500 VDCW, temp coef -80 PPM.
		- - - - - INDUCTORS - - - - -
L4	19A121464P2 5491798P5	Coil. Includes: Tuning slug.
		- - - - - SOCKETS - - - - -
XY1 thru XY4		Refer to Mechanical Parts (RC-1692).

SYMBOL	GE PART NO.	DESCRIPTION
		<p>----- CRYSTALS -----</p> <p>NOTE: When reordering give GE Part No. and specify exact freq needed.</p> <p>25-42 MHz crystal freq = (OF <u>+5.30</u> MHz) 3</p> <p>42-50 MHz crystal freq = (OF <u>-5.30</u> MHz) 3</p>
Y1 thru Y4	19B206576P10	Quartz: freq range 10086.666 to 12753.333 KHz, temp range -30°C to +85°C. (25-33 MHz).
Y1 thru Y4	19B206576P11	Quartz: freq range 12753.333 to 15753.333 KHz, temp range -30°C to +85°C. (33-42 MHz).
Y1 thru Y4	19B206576P3	Quartz: freq range 12246.666 to 16246.666 KHz, temp range -30°C to +85°C. (42-50 MHz).
A386 thru A388		<p>MULTIPLIER SELECTIVITY ASSEMBLY</p> <p>A386 19B205326G2 (4ER39D20, 23 and 26)</p> <p>A387 19B205326G11 (4ER39D21, 24 and 27)</p> <p>A388 19B205326G10 (4ER39D22, 25 and 28)</p>
		<p>----- CAPACITORS -----</p>
C7 and C8	5493392P107	Ceramic feed-thru: 470 pf ±100% -0%, 500 VDCW.
C9	5491601P123	Phenolic: 1.5 pf ±5%, 500 VDCW.
C14	5491601P114	Phenolic: 0.51 pf ±5%, 500 VDCW.
C15	5491601P115	Phenolic: 0.56 pf ±5%, 500 VDCW.
C17 and C18	5496218P248	Ceramic disc: 24 pf ±5%, 500 VDCW, temp coef -80 PPM.
C19	5496218P247	Ceramic disc: 22 pf ±5%, 500 VDCW, temp coef -80 PPM.
C20 and C21	5496218P242	Ceramic disc: 12 pf ±5%, 500 VDCW, temp coef -80 PPM.
C22	5496218P241	Ceramic disc: 10 pf ±5%, 500 VDCW, temp coef -80 PPM.
C23	5491601P115	Phenolic: 0.56 pf ±5%, 500 VDCW.
C24	5491601P112	Phenolic: 0.43 pf ±5%, 500 VDCW.
C25	5491601P111	Phenolic: 0.39 pf ±5%, 500 VDCW.
C27 thru C29	5496218P244	Ceramic disc: 15 pf ±5%, 500 VDCW, temp coef -80 PPM.
C30	5491601P137	Phenolic: 0.91 pf ±5%, 500 VDCW.
C31	5491601P114	Phenolic: 0.51 pf ±5%, 500 VDCW.
C32	5491601P115	Phenolic: 0.56 pf ±5%, 500 VDCW.
C37 and C38	5491601P125	Phenolic: 2.0 pf ±5%, 500 VDCW.
C39	5491601P123	Phenolic: 1.5 pf ±5%, 500 VDCW.
		<p>----- DIODES AND RECTIFIERS -----</p>
CR1	4038056P1	Germanium.
		<p>----- RESISTORS -----</p>
R1	3R152P473K	Composition: 47,000 ohms ±10%, 1/4 w.
		<p>----- TRANSFORMERS -----</p>
T1	19B205325G2	Coil, includes L1 and tuning slug 5491798P4.
T2 and T3	19B205325G1	Coil, includes L2, L3, and tuning slug 5491798P4
T4	19B205325G2	Coil, includes L1 and tuning slug 5491798P4.
T5 and T6	19B205325G1	Coil, includes L2, L3, and tuning slug 5491798P4

SYMBOL	GE PART NO.	DESCRIPTION
A389		SECOND MIXER 19B216119G2
		- - - - - CAPACITORS - - - - -
C2	19A116080P7	Polyester: 0.1 μ f \pm 20%, 50 VDCW.
C3	5494481P1I2	Ceramic disc: 1000 pf \pm 10%, 1000 VDCW; sim to RMC Type JF Discap.
C7 and C8	19A116080P5	Polyester: 0.047 μ f \pm 20%, 50 VDCW.
C9	5496219P369	Ceramic disc: 180 pf \pm 5%, 500 VDCW, temp coef -150 PPM.
C10	19A116080P7	Polyester: 0.1 μ f \pm 20%, 50 VDCW.
C14	19A116656P220J2	Ceramic disc: 220 pf \pm 5%, 500 VDCW, temp coef -220 PPM.
C15	7491395P109	Ceramic disc: 1000 pf \pm 10%, 500 VDCW; sim to RMC Type JL.
C16	19A116080P5	Polyester: 0.047 μ f \pm 20%, 50 VDCW.
C17	19A116080P1	Polyester: 0.01 μ f \pm 20%, 50 VDCW.
C18	5490008P25	Silver mica: 82 pf \pm 5%, 500 VDCW; sim to Electro Motive Type IM-15.
C19	5490008P19	Silver mica: 47 pf \pm 5%, 500 VDCW; sim to Electro Motive Type IM-15.
C20	5490008P1	Silver mica: 5 pf \pm 5 pf, 500 VDCW; sim to Electro Motive Type IM-15.
C21 and C22	5496219P49	Ceramic disc: 27 pf \pm 5%, 500 VDCW, temp coef 0 PPM.
C26	5496219P368	Ceramic disc: 160 pf \pm 5%, 500 VDCW, temp coef -150 PPM.
		- - - - - JACKS AND RECEPTACLES - - - - -
J1	4033513P4	Contact, electrical: sim to Bead Chain L93-3.
		- - - - - INDUCTORS - - - - -
L1	19C311181G3	Coil. Includes tuning slug 4038368P1.
L2 and L3	19A115711P1	Transformer, freq: 455 KHz; sim to Automatic Wfg EX12870.
		- - - - - PLUGS - - - - -
P1	4029840P2	Contact, electrical: sim to Amp 42827-2.
P2	4029840P1	Contact, electrical: sim to AMP 41854.
		- - - - - TRANSISTORS - - - - -
Q2	19A115245P1	Silicon, NPM.
Q3	19A115123P1	Silicon, NPM.
Q4	19A115245P1	Silicon, NPM.
		- - - - - RESISTORS - - - - -
R1 and R2	3R77P103K	Composition: 10,000 ohms \pm 10%, 1/2 w.
R3	3R77P512J	Composition: 5100 ohms \pm 5%, 1/2 w.
R4	3R152P333K	Composition: 33,000 ohms \pm 10%, 1/4 w.
R5	3R152P103J	Composition: 10,000 ohms \pm 5%, 1/4 w.
R6	3R77P332K	Composition: 3300 ohms \pm 10%, 1/2 w.
R7	3R77P123K	Composition: 12,000 ohms \pm 10%, 1/2 w.
R8	3R77P622J	Composition: 6200 ohms \pm 5%, 1/2 w.
R9	3R77P302J	Composition: 3000 ohms \pm 5%, 1/2 w.
R10	3R77P202J	Composition: 2000 ohms \pm 5%, 1/2 w.
R11	3R77P201J	Composition: 200 ohms \pm 5%, 1/2 w.
R12	3R77P513J	Composition: 51,000 ohms \pm 5%, 1/2 w.
		- - - - - TRANSFORMERS - - - - -
T2		COIL ASSEMBLY 19B216120G2
		- - - - - CAPACITORS - - - - -
C23	5496218P258	Ceramic disc: 62 pf \pm 5%, 500 VDCW, temp coef -80 PPM.
	5491798P3	Tuning slug.

SYMBOL	GE PART NO.	DESCRIPTION
Y2	19A110398G1	----- CRYSTALS ----- Quartz: freq 11945 KHz $\pm 0.002\%$, temp range -30°C to +75°C.
A391		FIRST MIXER ASSEMBLY 19B216867G3
C2 and C3	5494481P127	----- CAPACITORS ----- Ceramic disc: 2700 μ f $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C4 and C5	5494481P111	Ceramic disc: 1000 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C7	19A116114P2044	Ceramic: 27 pf $\pm 5\%$, 100 VDCW; temp coef -80 PPM.
E1	4038104P1	----- TERMINALS ----- Lug: solder dipped brass.
L2	19B216881G1	----- INDUCTORS ----- Coil.
Q1*	19A116960P1	----- TRANSISTORS ----- N Type, field effect. In 19E500873G9 of REV G and earlier: In 19E500873G10, G11 of REV H and earlier:
	19A115953P1	N Channel; sim to T1S34.
R1	3R152P513J	----- RESISTORS ----- Composition: 51,000 ohms $\pm 5\%$, 1/4 w.
R2	3R152P333K	Composition: 33,000 ohms $\pm 10\%$, 1/4 w.
R3 and R4	3R152P101K	Composition: 100 ohms $\pm 10\%$, 1/4 w.
A403		IF AUDIO AND SQUELCH 19D413129G4
C1	5494481P111	----- CAPACITORS ----- Ceramic disc: 1000 pf $\pm 10\%$, 000 VDCW; sim to RMC Type JF Discap.
C2	5496219P717	Ceramic disc: 47 pf $\pm 10\%$, 500 VDCW, temp coef -750 PPM.
C3	5494481P111	Ceramic disc: 1000 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C4	5496219P717	Ceramic disc: 47 pf $\pm 10\%$, 500 VDCW, temp coef -750 PPM.
C5 and C6	5494481P111	Ceramic disc: 1000 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C7	19A116080P5	Polyester: 0.047 μ f $\pm 20\%$, 50 VDCW.
C8	19A116656P180J1	Ceramic disc: 180 pf $\pm 5\%$, 500 VDCW, temp coef -150 PPM.
C9 and C10	5490008P37	Silver mica: 270 pf $\pm 5\%$, 500 VDCW; sim to Electro Motive Type DM-15.
C11	5496219P656	Ceramic disc: 51 pf $\pm 5\%$, 500 VDCW, temp coef -470 PPM.
C12	5494481P108	Ceramic disc: 470 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C13	19A115680P107	Electrolytic: 100 μ f $\pm 150\%$ -10%, 15 VDCW; sim to Mallory Type TTX.
C14 and C15	19A115680P104	Electrolytic: 50 μ f $\pm 150\%$ -10%, 25 VDCW; sim to Mallory Type TTX.
C17	19A116080P7	Polyester: 0.1 μ f $\pm 20\%$, 50 VDCW.
C18	5494481P108	Ceramic disc: 470 pf $\pm 10\%$, 1000 VDCW; sim to RMC Type JF Discap.
C19 and C20	19A116080P5	Polyester: 0.047 μ f $\pm 20\%$, 50 VDCW.





MODEL NO	FREQ. RANGE	NO. FREQ.
4ER39D20	26-33 MHz	1
4ER39D21	33-42 MHz	1
4ER39D22	42-50 MHz	1
4ER39D23	26-33 MHz	2
4ER39D24	33-42 MHz	2
4ER39D25	42-50 MHz	2
4ER39D26	26-33 MHz	4
4ER39D27	33-42 MHz	4
4ER39D28	42-50 MHz	4

REV	LTR	BLOCK	REV
		PL1950087369	H
		PL19500873610	J
		PL19500873611	K
A389	PL1951611952		A
A403	PL19D41312964		N

IN ORDER TO RETAIN RATED EQUIPMENT PERFORMANCE, REPLACEMENT OF ANY SERVICE PART SHOULD BE MADE ONLY WITH A COMPONENT HAVING THE SPECIFICATIONS SHOWN ON THE PARTS LIST FOR THAT PART.

ALL RESISTORS ARE 1/2 WATT UNLESS OTHERWISE SPECIFIED AND RESISTOR VALUES IN OHMS UNLESS FOLLOWED BY K=1000 OHMS OR MEG=1,000,000 OHMS. CAPACITOR VALUES IN MICROFARADS (EQUAL TO MICROFARADS) UNLESS FOLLOWED BY UF= MICROFARADS. INDUCTANCE VALUES IN MILLIHENRYS UNLESS FOLLOWED BY MH= MILLIHENRYS OR H=HENRYS.

VOLTAGE READINGS
VOLTAGE READINGS ARE TYPICAL READINGS MEASURED TO SYSTEM NEGATIVE (J442-B) WITH TEST SET MODEL 4EX3A10 OR A 20,000 OHM-PER-VOLT METER.
S= NO SIGNAL IN WITH SQUELCH CONTROL FULLY COUNTERCLOCKWISE (MAXIMUM SQUELCH).
US= SQUELCH CONTROL FULLY COUNTERCLOCKWISE WITH A ONE MILLIVOLT MODULATED SIGNAL (UNSQUELCHED) AND 5WATT AUDIO OUTPUT.

- LOW SPLIT 26-33 MHz
- ▲ MID SPLIT 33-42 MHz
- HIGH SPLIT 42-50 MHz
- * C342 1.80-8.3 IN HIGH SPLIT ONLY

SCHEMATIC DIAGRAM

26—50 MHz MASTR RECEIVER
MODELS 4ER39D20—28

The image contains two technical drawings of the RC-1692 vehicle. The top drawing is a top-down view, and the bottom drawing is a side profile view. Both drawings include numbered callouts (1 through 30) identifying various components and features of the vehicle's structure and interior.

Changes in the equipment to improve performance or to simplify circuits are identified by a "Revision Letter", which is stamped after the model number of the unit. The revision stamped on the unit includes all previous revisions. Refer to the Parts List for descriptions of parts affected by these revisions.

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- REV. A - E - Chassis & RF Assembly (19E500873G0, 10 & 11)
To incorporate in original equipment.
- REV. A - 2nd Mixer A389 (19B21611902)
Incorporated in original equipment.
- REV. A-C - IF/Audio & Squelch Board A403 (19D413129G4)
Incorporated in original equipment.
- REV. F - Chassis & RF Assembly (19E500873G0)
To eliminate unnecessary protection. Delete CR1.
- REV. G - To incorporate new transistors. Changed Q341 & Q342.
- REV. H - To incorporate new transistor. Changed Q2.
- REV. F - Chassis & RF Assembly (19E500873G1)
To improve sensitivity. Delete Z351. Add Z353.
- REV. F - Chassis & RF Assembly (19E500875G1)
To improve sensitivity. Delete Z352. Add Z356.
- REV. G - Chassis & RF Assembly (19E500873G10 & 11)
To improve tuning range of RF stage. Changed C342.
- REV. D - IF/Audio & Squelch Board (19D413129G4)
To improve frequency response. Changed R30 and R78.
- REV. E - To prevent squelch lock up. Delete R72. Add R48.
- REV. F - To compensate for vendor change. Changed C26.
- REV. G - To incorporate silicon diodes. Changed CR3 and CR4.
- REV. H - To improve squelch action at -30°C. Changed Q10.
- REV. J - To correct PA bias. Changed R40.
- REV. K - To improve stability of audio output with no load.
Added R85.
- REV. L - To improve frequency response at 300 Hz.
Deleted R85 and Changed C40.
- REV. M - To improve audio quality. Changed R80.
- REV. N - To improve frequency response. Changed C26.
- REV. P - To improve stability. Changed Q5.

ORDERING SERVICE PARTS

LBI-4125

Each component appearing on the schematic diagram is identified by a symbol number, to simplify locating it in the parts list. Each component is listed by symbol number, followed by its description and GE Part Number.

Service parts may be obtained from Authorized GE Communication Equipment Service Stations or through any GE Radio Communication Equipment Sales Office. When ordering a part, be sure to give:

1. GE Part Number for component
2. Description of part
3. Model number of equipment
4. Revision letter stamped on unit

These instructions do not purport to cover all details or variations in equipment nor to provide for every possible contingency to be met in connection with installation, operation or maintenance.

Should further information be desired, or should particular problems arise which are not covered sufficiently for the purchaser's purposes, contact the nearest Radio Communication Equipment Sales Office of the General Electric Company.

MAINTENANCE MANUAL

LBI-4125

MOBILE RADIO DEPARTMENT
GENERAL ELECTRIC COMPANY • LYNCHBURG, VIRGINIA 24502



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