TK-380 SERVICE MANUAL

© 1999-1 PRINTED IN JAPAN B51-8455-00(S) 1568

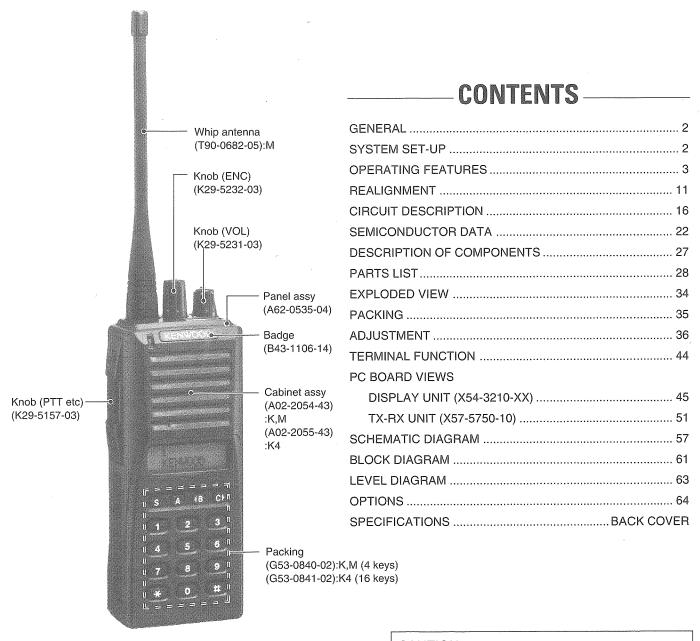


Photo is K4 type with KRA-15

CAUTION

When using an external power connector, please use with maximum final module protection of 9V.

GENERAL / SYSTEM SET-UP

INTRODUCTION

SCOPE OF THIS MANUAL

This manual is intended for use by experienced technicians familiar with similar types of commercial grade communications equipment. It contains all required service information for the equipment and is current as of the publication data. Changes which may occur after publication are covered by either Service Bulletins or Manual Revisions. These are issued as required.

ORDERING REPLACEMENT PARTS

When ordering replacement parts or equipment information, the full part identification number should be included. This applies to all parts: components, kits, or chassis. If the part number is not known, include the chassis or kit number of which it is a part, and a sufficient description of the required component for proper identification.

PERSONNEL SAFETY

The following precautions are recommended for personnel safety:

- DO NOT transmit until all RF connectors are verified secure and any open connectors are properly terminated.
- SHUT OFF and DO NOT operate this equipment near electrical blasting caps or in an explosive atmosphere.
- This equipment should be serviced by a qualified technician only.

SERVICE

This radio is designed for easy servicing. Refer to the schematic diagrams, printed circuit board views, and alignment procedures contained within.

NOTE

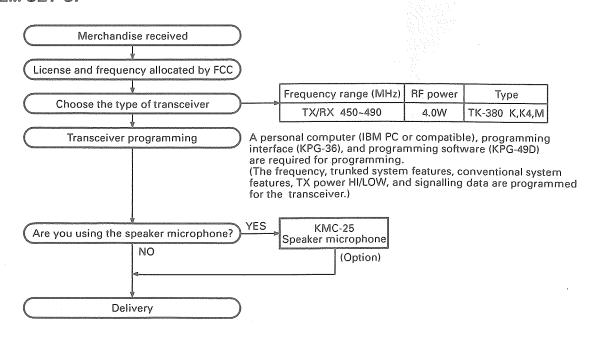
WE CANNOT guarantee oscillator stability when using channel element manufactured by other than KENWOOD or its authorized agents.

FCC COMPLIANCE AND TYPE NUMBERS

Ī	уре	Type acceptance number	Frequency range	Compliance
K	<,K4	ALH24623110	450~490MHz	Parts 22,74,80,90,95

Model &		X57-5750-XX X54-3210-XX		Frequency range Remarks	QT/DQT	DTMF	Charger	Battery	10 Key			
destina	ation	0-10	0-11	0-10	0-11							
TK-380	K, M	0		0		450~490MHz	IF1: 44.85MHz			OP	OP	
117-300	K4	0			0	430~4301411 12	LOC: 44.395MHz				01	0

SYSTEM SET-UP



1. Operation Features

The TK-380 is an UHF FM radio designed to operate in both trunking format and conventional format. The programmable features are summarized.

Model — Trunking Format — Trunking mode Conventional Format — Conventional mode

Trunking Format

This format can handle up to 32 systems with up to 250 groups in each system. The transceiver can be used in both trunked mode and conventional mode. Systems, groups, and their functions are programmed.

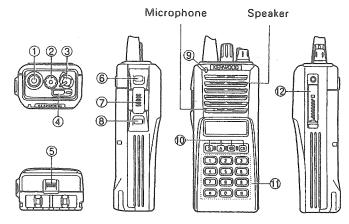
Conventional Format

This format can handle up to 250 groups with 250 channels in each group.

The transceiver can be used only in conventional mode. Groups, channels, and their functions are programmed.

2. Transceiver Controls and Indicators

2-1. Physical Layout



Note: The transceiver is also available without the DTMF keypad (①).

2-2. Panel controls

The key on the top and front panel is momentary-type push buttons. The functions of these keys and knob are explained below.

1 Antenna connector

Connect the supplied antenna here.

2 System or Group selector knob (Programmable)

Trunking Format

Turning the system (or group) selector knob clockwise increases the system (or group) number by one. Turning the knob in the counterclockwise direction decreases the system (or group) number by one.

After the system number (or group number) reaches the highest system number (or group number), it goes back to

lowest system number (or group number).

System numbers (or group numbers) not set are skipped. **Caution**: The FPU (KPG-49D) allows selecting between system selector and group selector.

Conventional Format

Turning the group (or channel) selector knob clockwise increases the group (or channel) number by one. Turning the knob in the counterclockwise direction decreases the group (or channel) number by one.

After the group number (or channel number) reaches the highest group number (or channel number), it goes back to lowest group number (or channel number).

Group numbers (or channel numbers) not set are skipped. **Caution**: The FPU (KPG-49D) allows selecting between group selector and channel selector.

③ Volume/Power switch

Trunking Format

Transceiver Power and Volume switch. Turn clockwise to switch On the transceiver. Turn counterclockwise fully to switch OFF the transceiver. Also adjusts the volume level. When the power is switched off, all the parameters, such as the system and group, are stored in memory. When the power is switched on again, the system returns to the previous conditions.

Conventional Format

Transceiver Power and Volume switch. Turn clockwise to switch On the transceiver. Turn counterclockwise fully to switch OFF the transceiver. Also adjusts the volume level. When the power is switched off, all the parameters, such as the group and channel, are stored in memory. When the power is switched on again, the group returns to the previous conditions.

4 Auxiliary (orange) key (Programmable)

5 Battery pack release catch

Push down to release the battery pack. See Installing the Ni-Cd Battery Pack.

6 MONITOR key* (Programmable)

7 PTT (Push-To-Talk) key

Press this key, then speak into the microphone to call a station.

® LAMP key* (Programmable)

9 TX/BATT indicator

This red LED lights during transmission (it does not light during busy or when transmit is prohibited). If the battery voltage falls below the programmed voltage during transmission, the brightness of this indicator decreases at intervals of about one second, so it can be used as the battery voltage alert function.

1) DTMF keypad (keypad model only)

Press the keys on the telephone keypad to send DTMF tones.

12 Universal connector

Connect the external KMC-25 speaker/ microphone (optional) here. Otherwise, keep the supplied cover in place.

*: MONITOR and LAMP are arbitrary names chosen for these buttons. They can be used for any of the auxiliary functions.

2-3. Programmable keys

The FPU (KPG-49D) enables programmable keys to select the following functions.

■ Trunking Format

Auto Tel, AUX(only when Voice Scrambler is not selected), Connect ID, Disconnect ID, Display Character, Emergency (only AUX key), Function, Group Down, Group Up, Home Group, Key Lock, Lamp, Memory (RCL/STO), Memory (RCL), Memory (STO), Monitor A, Monitor B, Monitor C, Monitor D, Redial, RF Power Lo, Scan, Scan Del/Add, Scan Temporary Delete, Scrambler (Only when Voice Scrambler is selected), System Down, System Up, TEL Disconnect and none.

■ Conventional Format

AUX(only when Voice Scrambler is not selected), Channel Down, Channel UP, Connect ID, Disconnect ID, Display Character, Emergency (only AUX key), Function, Group Down, Group Up, Home Channel, Key Lock, Lamp, Memory (RCL/STO), Memory (RCL), Memory (STO), Monitor A, Monitor B, Monitor C, Monitor D, Operator Selectable Tone, Redial, RF Power Lo, Scan, Scan Del/Add, Scrambler (Only when Voice Scrambler is selected), Talk Around and none.

These functions the FPU programs to the function keys are described in the following sections.

1) Auto TEL (Trunking Format)

Automatically connects available repeaters that are connected to telephone circuits when operating as LTR system. The time allocated to search for available repeaters is 60 seconds, after which connection failure occurs, a DTMF tone is output and the function terminates.

If connection to an available circuit is made, only ID 253, EOT or hang-up time-out can terminate the function.

2) AUX

This function can be programmed when the voice scrambler board is not installed.

If this key is pressed, an underscore ("_") appears at the extreme right of the LCD and AUX port which is inside of the transceiver turns to the active level. If pressed again,

the underscore disappears and the AUX ports turns to the deactive level.

3) Channel up/down (Conventional Format)

When the key is pressed each time, the channel number to be selected is incremented/decremented and repeats if held for one second or longer.

This key works as the voice scrambler code selector in the voice scrambler code select mode.

4) Connect ID

Pressing this key in Conventional mode, automatically sends the preset Connect ID.

5) Disconnect ID

Pressing this key in Conventional mode, automatically sends the preset Disconnect ID.

6) Display character

Trunking Format

This key switches the LCD display between the system/ group number and system/group name.

Conventinal Format

This key switches the LCD display between the group/channel number and group/channel name.

7) Emergency

Trunking Format

Pressing this key for longer than the programmed "Emergency Key Delay Time" causes the transceiver to enter the emergency mode. The transceiver jumps to the programmed "Emergency System/Group" and transmits for the programmed "Active Time".

The transceiver disables mic mute while transmitting. After finishing transmission, the transceiver receivers for the programmed "Interval Time". The transceiver mutes the speaker while receiving. Following the above sequence, the transceiver continues to transmit and receive.

Conventinal Format

Pressing this key for longer than the programmed "Emergency Key Delay Time" causes the transceiver to enter the emergency mode. The transceiver jumps to the programmed "Emergency Group/Channel" and transmits for the programmed "Active Time".

The transceiver disables mic mute while transmitting. After finishing transmission, the transceiver receivers for the programmed "Interval Time". The transceiver mutes the speaker while receiving. Following the above sequence, the transceiver continues to transmit and receive.

8) Function

Pressing this key causes the transceiver to display "FCN". Then, pressing a DTMF key causes the corresponding programmed function to start. This key may be convenient when using many functions with the 12-key keypad (K4 type).

9) Group up/down

When the key is pressed each time, the group number to be selected is incremented/decremented and repeats if held for one second or longer. In Conventional format, this key works as the voice scrambler code selector in the voice scrambler code select mode.

10) Home Channel (Conventional Format)

Press this key once, the channel switches to the preprogrammed home channel.

11) Home group (Trunking Format)

Each pressing of the key selects a preset system/group.

12) Key lock

Pressing this key causes the transceiver to accept entry of only the [Function], [Key Lock], [PTT], [Lamp], [Monitor A], [Monitor B], [Monitor C], [Monitor D], and [Emergency] keys. The locked keys also include the tuning control.

13) Lamp

This key illuminates the LCD and keys on the front panel. When the key is pressed, the LED lamp goes on. When it is released, the lamp goes off after about five seconds. If any key is pressed while the LED lamp is on, the lamp is kept on for five seconds.

14) Memory

This key allows DTMF memory data to be recalled; up to 32 memories each with a memory dial of up to 16 digits and an A/N of up to 10 digits per memory.

15) Monitor

Used to release signalling or squelch when operating as a conventional. It is also used to reset option signalling.

16) Operator Selectable Tone (Conventional Format)

This key switches the pre-set decode QT/DQT and encode QT/DQT to OST (Operator Selectable Tone) tone pair. Press this key, the transceiver enters to OST select mode. In this mode, the display shows "OFF" and the operator can select one of the OST tone pair using the tuning control. The display shows "TONE **" and tone pair No. ** is selected.

Press OST key again, the transceiver exits from the OST select mode, and returns to the group/channel mode with the handset indicator () means that the OST tone pair is selected. OST tone pair number or OFF can be memorized for each channel.

16 kinds of tone pair for OST can be programmed by KPG-49D. OST is useful to access the repeater with same radio frequency and different tone (QT/DQT).

17) Redial

Pressing this key when System/Group(Trunking Format), Group/Channel (Conventional Format) is shown, displays the previously transmitted DTMF code. Pressing [PTT] at this time, transmits the code that is currently displayed.

18) RF power low

Used to temporarily switch transmission output to low power. Turning the function on enables:

Hi→Low, Low→Low

Key states are backed up, except in the PC mode when they are reset.

19) Scan

Press this key starts scanning. Pressing this key stops scanning.

20) Scan Del/Add

Trunking Format

Used to select whether system scan routines are used during system scan. Each pressing of the key (to ON) toggles between lockout and lock. The scan routine is started when on lock. The DEL indicator flashes when the system is on lockout.

Conventinal Format

This key switches the currently displayed channel between "Delete" and "Add".

The "Add" channel contained in the scan sequence, and "Delete" channel is not contained. In the scan mode, this key switches the channel delete or add temporarily.

21) Scan temporary delete (Trunking Format)

This key is temporarily deleted a system being scanned. If you press this key when scan is stopped (when a call is being received from another station), the system is temporarily deleted and scanning restarts.

This key operates even when "Scan Type" is set to "List Type System Scan".

22) Scrambler

If a scrambler code (1 to 4) has been set in the FPU, an underscore ("_") appears at the extreme right of the LCD display when scrambler is active. Pressing this key changes ON/OFF of scramble operation.

Holding this key down for 2 seconds sets Scramble Code Select Mode

23) System up/down (Trunking Format)

When the key is pressed each time, the system number to be selected is incremented/decremented and repeats if held for one second or longer.

24) Talk Around (Conventional Format)

Press this key, the transceiver uses the receive frequency and the tone for transmission.

The operator can call the other party directory (without repeater). Press this key again, the talk around function goes off.

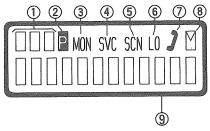
25) Telephone disconnect (Trunking Format)

Pressing this key ends an RIC connection (disconnects the telephone line).

26) None

Sounds error operation beep, and no action will occur.
Use this function when the transceiver is required to be more simple operated.

2-4. Display



1 Sub display

Displays the system, channel and group numbers. Also displays various functions, such as TA.

2 P (Priority) indicator

The P indicator () appears when a selected channel is programmed as priority, in conventional operation.

3 MON (Monitor) indicator

The MON indicator appears when the button programmed as MONITOR is pressed.

4) SVC (Service) indicator

This icon is not used on this transceiver.

5 SCN (Scan) indicator

The SCN indicator appears when using Scan mode.

6 LO indicator

Appears when low power is selected.

7) Handset indicator

The handset indicator () appears when the selected group is programmed as telephone IDs. (Trunking Format) In Conventional Format, the handset indicator () appears when the OST tone pair is selected.

8 MAIL indicator

This icon is not used on this transceiver.

9 Alphanumeric display

Trunking Format

The twelve-character dot matrix alphanumeric display shows the system and group numbers. You can program system and group names with up to ten characters in place of these numbers. The left display is used as a delete indicator (>>) and the right is used for the selective call (:x)

or scrambler (_) function. The delete/add indicator shows the systems locked out of the scanning sequence. Selective call and scrambler are optional functions that can be programmed.

Conventinal Format

The twelve-character dot matrix alphanumeric display shows the group and channel numbers. You can program group and channel names with up to ten characters in place of these numbers. The left display is used as an add indicator (\mathbb{T}) and the right is used for the selective call (\mathbb{T}) or scrambler (\mathbb{T}) function. The add indicator shows the channels unlocked out of the scanning sequence. Selective call and scrambler are optional functions that can be programmed.

3. Scan Operating

3-1. In Case of Trunking Format

1) System scan

System scan can be selected with the "Scan" key by programming the scan feature. When the "Scan" key is pressed and the "SCN" mark appears, scan mode in entered. Scanning starts from the system following the currently displayed system. When a call is received, scanning stops, and the system and group are displayed.

When the system knob or programming key is touched during scanning, the scan stops and the revert system or group can be changed. Scanning resumes one second after the key is released.

System Scan consists of the following 2 types.

Fix system scan

All the set systems except locked-out ones are scanned. If the DEL/ADD feature is assigned to the programmable key, it can be controlled from the front panel.

List type system scan

A scan list can be set for each system.

The list to be scanned can be changed by changing the display system

If many system have been set, the scan speed can be increased by narrowing the systems to be scanned with scan lists.

2) System lockout

The system lockout feature is used to lock systems out of the scan sequence, and can be selected by programming in the following two ways:

Fixed lockout

The system to be locked out is selected by programming. When a locked system is selected, the Delete (\triangleright) indicator appears on the left of the SYSTEM indicator. The revert system is scanned even if it is locked out. If there is a locked system, the Delete (\triangleright) indicator flashes during fixed scanning.

User selectable lockout

If the scan lockout feature is programmed to a key, the user can lock systems out of the scan sequence with the key. To lock a system out of the scan sequence, press the key when the system is displayed. The Delete (\triangleright) indicator is displayed on the left of the SYSTEM indicator.

To unlock a system, select the system and press the key. The Delete (▶) indicator disappears to indicate that the system has returned to the scan sequence. The revert system is scanned even if it is locked out. If there a locked system, the Delete (▶) indicator flashes during fixed scanning. If all systems are locked out, the scan stops and only the revert system is received.

3) Drop-out delay time (Scan resume time)

If a call is received during scan, the scan stops. The scan resume time can be programmed as 0 to 300 seconds in one-second increments. The default value is 3 seconds.

4) Dwell time

The dwell time is the time after transmission ends until the scan resumes in scan mode. It can be set 0 to 300 seconds by programming. The default value is 3 seconds.

5) System/Group revert

System/Group revert can be programmed for one of the following;

Last called revert

The system or group changes to the revert system or group when a call is received with the system or group being scanned.

Last used revert

If a system/group call is received during scanning and the PTT button is pressed for transmission and response within the drop out delay time, the system or group is assigned as the new revert system or group.

Selected revert

If the system/group was changed while scanning, the newly selected system/group.

6) Scan message wait

The time for staying with the home repeater that receives a signal during system scan and monitoring data messages can be programmed. If there is no signal from the home repeater, the system is scanned for about 50ms. If there is a signal, three data messages are monitored. Normally, three data messages are monitored for each system, and it can be increased in multiples of three data messages per line to up to eight lines.

If the repeater data message indicates that there is no call, data monitoring is terminated and the home repeater of the next system is scanned.

7) Group scan operation

Group scan can be programmed for each group. In addition to the ID codes of the selected group, the ID codes of the other groups that are permitted for group scan are decoded. (The two fixed ID and block decode codes are always decoded.)

If, during group scanning, a call is received with one of the selectable group ID codes for which group scan is enabled, the group display indicates the group number that the call came in with. That group then becomes the new selected group. Group scan resumes after the specified dropout delay time or dwell time shared by the system scan elapses.

8) In Conventional system.

If QT or DQT is set for the channel, the channels, including signalling, are scanned.

In case of the priority group is set in conventional system, if a group scan (including group scan during a system scan) temporarily stops (receiving) in a group that does not have priority, a look back is performed to the priority group. Look back is performed according to the look back time A and B settings. If a call is received on the priority group, reception immediately switches to the priority group.

3-2. In Case of Conventional Format

1) Scan types

Single Group Scan

You can scan all valid (ADD) channels in the displayed group that can be selected with the group selector.

Multiple Group Scan

You can scan all valid (ADD) channels in the all valid (ADD) group.

2) Scan Start Condition

One or more non-priority channels must be added to all channels that can be scanned. The transceiver must be in normal receive mode (PTT off).

When you activate the key programmed to the scan function, the scan starts. The scan icon "SCN" lights and "-SCAN-" or revert channel (programmable) is indicated on alphanumeric display.

3) Scan Stop Condition

The scan stops temporarily if the following conditions are satisfied.

- A carrier is detected, then signalling matches on channels for which receive the signalling is set by the programming software
- ② A carrier is detected on the channel for which receiving signalling is not set by the programming software or when the monitor (signalling cancel) function is activated.

4) Scan Channel Types

① Priority channel is the most important channel for the scan, and always detects a signal during scan and when the scan stops temporarily.

② Non-priority channels detects a signal during scan. For the channels that can be selected with the group or channel selector when the scan does not occur, adds an indicator "♥" lights.

5) Priority Channel Setting

A priority channel can be set as follows with the programming software (KPG-49D).

- 1) Specify a priority channel as a fixed priority channel.
- ② Make a selected channel a priority channel.
- ③ Operator selectable Specify the initial channel before the operator changes it.

6) Scan Type According to the Priority Channel

① When no priority channel is set: Only the non-priority channels are scanned.

If a non-priority channel stops temporarily, it stops until there is no signal on the channel.

② When priority channel is set: Either priority channel is scanned.

If a non-priority channel stops temporarily, a priority channel signal is detected at certain intervals.

If a priority channel stops temporarily, it stops until there is no signal on the priority channel.

7) Revert Channel

The revert channel is used to transmit during scanning and set by the programming software (KPG-49D).

① Priority

The transceiver reverts to the priority channel

2 Priority with talkback

The transceiver reverts to the priority channel.

If you press PTT during a resume timer (dropout delay time, TX dwell time) or calling, you can transmit on current channel to answer to the call however revert channel is set to priority channel.

After resume time, scan re-starts and transmission channel is return to priority channel.

3 Selected channel

The transceiver reverts to the channel before scanning or the channel that you changed during scan.

4 Last called channel

The transceiver reverts to the last called channel during the scan.

⑤ Last used channel

The transceiver reverts to the last used (transmitted) channel during scan. "Last used" revert channel includes talkback function.

6 Selected with talkback

The transceiver reverts to the channel before scanning or the channel that you changed during scan.

8) Scan End

When you reactivate the key programmed to the scan function during scan mode, the scan ends.

The scan icon "SCN" and "-SCAN-" or revert channel

(programmable) display goes off.

9) Temporarily Delete/Add

It is possible to delete or add channel temporarily during scan. When scan stops on unnecessary channel for example by interference of the other party, activate the delete/add function (for example press the key), then that channel is deleted temporarily and scan re-start immediately.

When you would like to add the deleted channel temporarily to scan sequence, select the desired (deleted) channel during scan, activate the delete/add function (for example press the key) before scan re-start.

That channel is added temporarily to scan sequence. The temporary deleted or added channels are returns to pre-set delete/add, when the transceiver exits from scan mode.

4. Details of Features

4-1. In Case of Trunking Format/Conventional Format 1) Time-out timer

The time-out timer can be programmed in 15 seconds increments from 15 seconds to ten minutes. If the transmitter is keyed continuously for longer than the programmed time, the transmitter is disabled and a warning tone sounds while the PTT button is held down. The alert tone stops when the PTT button is released.

2) Sub LCD

You can use 3-digit the display to display the system number, channel number or group number. It is useful when the main (12-digit) display indicates system, group or channel name or other functions.

3) Selective Call Alert LED

You can select whether or not the LED on the transceiver flashes in an orange color when selective call was occurred.

4) PTT ID

PTT ID provides a DTMF ANI to be sent with every time PTT (connect ID at beginning of transmission, disconnect ID at end of transmission, or both).

You can program PTT ID "on" or "off" for each channel. The contents of ID are programmed for each transceiver.

The transceiver is capable to have ID. The format is DTMF. The timing that the transceiver sends ID is programmable.

Connect ID: Connect ID is sent on beginning of transmission. Disconnect ID: Disconnect ID is sent on end of transmission. Both: Connect ID is sent on beginning of transmission and disconnect ID is sent on end of transmission.

There is also "PTT ID" setting for each channel.

5) Radio password

When the password is set in the transceiver, user can not use the transceiver unless enter the correct password.

This code can be up to 6 digits from 0 to 9 and input with the keypad or selector, and "S" key.

6) Battery Warning

This transceiver has battery warning feature. If the low voltage is detected during transmission, the transceiver warns it by flashing red "LED".

Then more low voltage is detected during transmission, the transceiver stops transmission and warns it by flashing red "LED" and beep.

Please notice "standard" for the battery exchange, charging time by flashing red LED and beep.

7) Minimum Volume

The minimum volume is programmable (off (0) to 31). The transceiver remains the minimum volume level however the mechanical volume position is set to zero.

4-2. In Case of Trunking Format

1) Call indicator

The call indicator can be programmed for each group. In trunked system, it can be set to respond to a selectable decode ID or one of two fixed IDs, except block IDs. When a call is received with a selectable decode ID, the call indicator flashes. When a call is received with a fixed ID, the call indicator lights continuously.

On a conventional system, the call indicator can be programmed to light for each QT or DQT code. It keeps flashing while a call is being received. It is turned off by pressing any front panel key.

2) Free system ringback

This feature is available only when a telephone interconnected ID code is selected. If a busy tone sounds when the PTT button is pressed, the transceiver enters this mode automatically.

When the PTT button is released, a beep sounds for 400ms to indicate that the mode has been entered. If the scan is on, it is resumed (the "SCN" mark goes on). When any repeater becomes available, a ringing tone sounds and this mode ends.

The mode is terminated when the system, group, scan, PTT, key is changed.

3) System search

This feature can be programmed to automatically access other programmed systems when the selected system cannot be accessed. If an intercept tone sounds when the PTT button is pressed after setting the mode, the transceiver has entered the mode.

If the group ID is a telephone interconnect ID, the transceiver then attempts to access, in succession, other systems that have a telephone interconnect ID in the revert group location. If the group ID is a dispatch ID, the transceiver attempts to access other systems that have a dispatch ID programmed in the revert group location.

If there is no system to be accessed, an intercept tone sounds, the mode is terminated, and the transceiver returns to the first system. If the access is successful, the mode is terminated, and the searched system becomes the new

selected system (If during scanning, the scan stops).

4) Transpond

This feature can be programmed to turn on and off for each group. If the ID of the group for which transpond is enabled is received, two data messages (transmit ID and turn-off code) are automatically transmitted if the PTT button is not pressed as a response within the time set (0 to 300 seconds in 1-second increments). If the PTT button is pressed within the time, the transpond is not preformed.

5) Transmit inhibit

The transceiver can be programmed with a transmit inhibit block of ID codes. If an ID code within this block is decoded the preset time before the PTT button is pressed, transmission is inhibited. The BUSY indicator lights and a busy tone sounds until the PTT button is released to indicate that transmission is not possible (except clear-to talk mode).

Transmission with the group for which the encode ID is not set is inhibited, and the busy tone is output while the PTT button is held down, regardless of the clear-to -talk setting.

6) Auto TEL

A telephone interconnect call can be made by simply pressing the key by assigning this feature to the key. This feature accesses the TEL channel of the available system automatically.

When the key is pressed, a queue tone is output, and the "AUTO TEL" appears on the alphanumeric display along with a flashing handset indicator () to indicate that this mode has been entered. If the TEL ID is set for the revert system, the TEL channel of that system is accessed. If all TEL channels are busy, an attempt is made to access the TEL channels of another system in which the TEL ID code has been programmed. It is repeated for 60 seconds until the access succeeds. If the access succeeds, a dial tone returns from the repeater. If the key is pressed again when the queue tone is sounding, this mode is canceled.

If the access fails after 60 seconds, a deny tone is output and this mode is terminated. When the talk ends, the revert system/group returns. When the scan mode is effective, the scan resumes. The Auto TEL feature can be programmed to turn on or off for each system.

4-3. In Case of Conventional Format

1) "TOT" Pre-Alert

The transceiver has "TOT" pre-alert timer. This parameter selects the time at which the transceiver generates "TOT" pre-alert tone before "TOT" is expired.

"TOT" will be expired when the selected time passes from a TOT pre-alert tone.

2) "TOT" Re-Key Time

The transceiver has "TOT" re-key timer. This timer is the time you can not transmit after "TOT" exceeded. After "TOT" re-key time expired you can transmit again.

3) "TOT" Reset Time

The transceiver has "TOT" reset timer. This timer is the minimum wait time allowed during a transmission that will reset the "TOT" count.

"TOT" reset time causes the "TOT" to continue even after PTT is released unless the "TOT" reset timer has expired.

4) OST (Operator Selectable Tone)

The transceiver is capable to have "OST" function and 16 tone pair (QT/DQT) with max 10-digit name for each tone pair.

"OST" Back Up

The transceiver is programmable the selected "OST" code is memorized or not. If you set to Disable (no memorized), the "OST" function always starts at "off".

Direct "OST"

It is possible to call "OST" number directory using keypad. In this case, keypad is used for "OST", then "Auto PTT" "Store & Send" functions by keypad are not usable.

5) Clear to Transpond

The transceiver waits the transpond of 2-Tone/DTMF if channel is busy until channel open. This feature prevents the interference to other party.

6) Battery Save

This is the automatic battery saver during a standby mode operation. The receiver circuit is repeated on and off to conserve the battery life.

5. Option Signalling (DTMF/2 tone)

Built-in DTMF decoder is available for option signalling. Built-in 2-Tone decoder is available for option signalling.

It is possible to use individual call, group call, DBD (Dead Beat Disable). **Note:** DBD is only DTMF

Preset operation is triggered when matches with Option Signaling

When Option Signaling matches on a Group Channel where set to Yes, the Option Signaling display flashes and Option Signaling is canceled. Settings after this will cause "Transpond" or "Alert" to sound.

Setting the Selective Call Alert LED will make an orange LED start flashing.

Mute or Unmute is triggered by the ID/QT/DQT/Carrier when option signaling is a match (when Option Signal is deactivated by a transmission).

AND/OR

Option Signaling match conditions can be selected with AND/OR logic.

	Alert/Transpond	AF Mute Open
AND	Triggers at match with QT/	Triggers at match with QT/
	DQT/ID+DTMF(2tone);Opt	DQT/ID+DTMF(2tone);Opt
OR	Triggers at match with QT	Triggers only for match with
	/DQT/ID+DTMF(2tone);Opt	QT/DQT/ID;Signaling

Even if set for OR, there is no Alert/Transpond just with DTMF. (LTR format)

Even if set for OR, AF mute cannot be canceled just by a match with DTMF.

In conventional channels not set with QT/DQT, signaling is a match just by receiving the carrier.

Auto Reset

When Option Signaling matches on a Group where set to Yes, Option Signaling is canceled when it matches a group set to Yes.

After Option Signaling is a match, Option Signaling can automatically set to Reset after a specified time.

Dead Beat Disable

When the D.B.D (Dead Beat Disable) code is a match, a preset operation is performed.

When D.B.D matches on all group channels regardless of whether Option Signaling = Yes/No, then TX Inhibit or TX RX Inhibit is activated by settings performed afterwards. D.B.D is canceled when the D.B.D. code + "#" is received.

Transpond is always activated when the D.B.D code is a match. Alert is not output. An Option Signaling match is not displayed.

6. Audible user feedback tones

The transceiver outputs various combinations of tones to notify the user of the transceiver operating state. The main tones are listed below

()),

The high tone is 1477Hz, the mid tone is 941Hz, and the low tone is 770Hz.

Power on tone

This tone is output when the transceiver is turned on. (The high tone is output for 500ms.)

Alert tone

This tone is output when the transceiver is TX inhibition for TOT, battery warning and PLL unlocked. It is output until the PTT button is released. (The 697Hz tone is output.)

Busy Tone

In trunked mode (of trunking format) the busy tone informs the user when the repeater cannot be used (System busy or TX inhibit status).

OPERATING FEATURES/REALIGNMENT

In conventional mode (of conventional format), this informs the user of a Busy Channel Lockout.

· Group Call Tone

The group call tone informs the user of a group call in DTMF/2Tone Option Signaling. This tone repeats 7 times.

Group Call Tone

The group call tone informs the user of a group call in DTMF/2Tone Option Signaling. This tone repeats 7 times.

770Hz		770Hz
30ms	30ms	30ms

Individual Tone

Individual tone is issued on receiving selective call by DTMF/2 Tone Option Signaling.

2000Hz		2000Hz		2000Hz
100ms	100ms	100ms	100ms	100ms

Intercept tone (Trunking Format)

This tone indicates that the transceiver is out of range. It indicates that the PTT button is pressed, and transmission has started, but the repeater cannot be connected and talking is not possible. It is output until the PTT button is released. (The mid tone and low tone are output alternately in 200ms intervals.)

Delay tone (Trunking Format)

This tone is output when the PTT button is pressed and the repeater is accessed three times or more to indicate connection with the repeater is delayed. This tone is the same as the busy tone. (It is not output of CLEAT TO TALK has been set to YES.)

Proceed tone (Trunking Format)

This tone is output when the PTT button is pressed, transmission starts, and the repeater is connected to indicate that the user can talk if the Clear-to-talk function has been set. (The high tone is output for 100ms.)

Queue tone (Trunking Format)

This tone is output until the Auto TEL function is set and the TEL channel is accepted successfully. (The mid tone on for 50ms, off for 50ms, and on for 50ms in 1 second intervals.)

Deny tone (Trunking Format)

This tone is output if the Auto TEL function is set, the queue tone is output, but the TEL channel cannot be accessed within 60 seconds. It is similar to the intercept tone. (The mid tone and low tone are output alternately in 150ms intervals.)

Free system ringback mode tone, system search mode tone (Trunking Format)

This tone indicates that the transceiver is free system ringback mode or system search mode. (The low tone is output for 400ms.)

· Ringing tone (Trunking Format)

This tone indicates that the transceiver can use the repeater in free system ringback mode. (The mid tone and no tone are output eight cycles alternately in 50ms intervals.)

Pre Alert tone (Conventional Format)

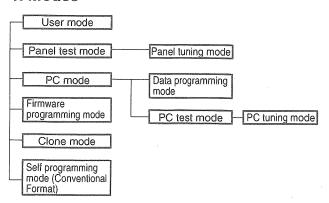
Informs user when nearing transmit inhibit (transmit cutoff) time due to TOT.

The Pre Alert Tone is issued from the time set for TOT Pre Alert until the TOT triggers.

1633Hz		1633Hz		1633Hz
50ms	50ms	50ms	50ms	50ms

REALIGNMENT

1. Modes



Mode	Function
User mode	For normal use.
Panel test mode	Used by the dealer to check the
	fundamental characteristics.
Panel tuning mode	Used by the dealer to tune the radio.
PC mode	Used for communication between the
	radio and PC (IBM compatible).
Data program-	Used to read and write frequency data
ming mode	and other features to and from the radio.
PC test mode	Used to check the radio using the PC.
N-E-E-E-E-E-E-E-E-E-E-E-E-E-E-E-E-E-E-E	This feature is included in the FPU.
	See panel tuning.
Firmware program-	Used when changing the main
ming mode	program of the flash memory.
Clone mode	Used to transfer programming data
	from one radio to another.
Self programming	Frequency, signalling and features
mode (Conventional	write to the radio.
Format)	

2. How to Enter Each Mode

Mode	Operation
User mode	Power ON
Panel test mode	[A]+Power ON (Two seconds)
PC mode	Received commands from PC
Panel tuning mode	[Panel test mode]+[S]
Firmware programming mode	[S]+Power ON (Two seconds)
Clone mode	[C]+Power ON (Two seconds)
Self programming mode	[LAMP]+Power ON
(Conventional Format)	(Two seconds)

3. Panel Test Mode

Setting method refer to ADJUSTMENT.

4. Panel Tuning Mode

Setting method refer to ADJUSTMENT.

5.PC Mode

5-1. Preface

The TK-380 transceiver is programmed by using a personal computer, programming interface (KPG-36) and programming software (KPG-49D).

The programming software can be used with an IBM PC or compatible. Figure 1 shows the setup of an IBM PC for programming.

5-2. Connection procedure

- Connect the TK-380 to the personal computer with the interface cable.
- When the POWER switch on, user mode can be entered immediately. When PC sends command the radio enter PC mode, and "PROGRAM" is displayed on the LCD.

When data transmitting from transceiver, the red LED is

When data receiving to transceiver, the green LED is blinking.

Notes:

- The data stored in the personal computer must match model type, when it is written into the flash memory.
- Change the TK-380 to PC mode, then attach the interface cable.

5-3. KPG-36 description

(PC programming interface cable: Option)

The KPG-36 is required to interface the TK-380 to the computer. It has a circuit in its D-subconnector (25-pin) case that converts the RS-232C logic level to the TTL level.

The KPG-36 connects the universal connector of the TK-380 to the computers RS-232C serial port.

5-4. Programming software description

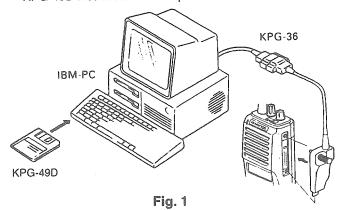
The KPG-49D programming disk is supplied in 3-1/2" disk format. The software on this disk allows a user to program TK-380 radios via programming interface cable (KPG-36).

5-5. Programming with IBM PC

If data is transferred to the transceiver from an IBM PC with the KPG-49D, the destination data (basic radio information) for each set can be modified. Normally, it is not necessary to modify the destination data because their values are determined automatically when the frequency range (frequency type) is set.

The values should be modified only if necessary. Data can be programmed into the flash memory in RS-232C format via the universal connector.

KPG-49D instruction manual parts No.: B62-1096-XX



6. Firmware Programming Mode

6-1. Preface

Flash memory is mounted on the TK-380. This allows the TK-380 to be upgraded when new features are released in the future. (For details on how to obtain the firmware, contact Customer Service.)

6-2. Connection procedure

Connect the TK-380 to the personal computer (IBM PC or compatible) with the interface cable (KPG-36). (Connection is the same as in the PC Mode.)

6-3. Programming

- Start up the programming software (KPG-49D), select "firmware program" in the "Program" item, and press the Return key on the personal computer. This starts up the firmware programmer.
- The top screen is displayed. Press any key to advance to the next screen.
- 3. Set the communications speed (normally, 57600 bps) and communications port in the Setup item.
- 4. Set the firmware to be updated by File select (=F1).
- Turn the TK-380 power ON with the [S] switch held down.
 Hold the switch down for two seconds until the display changes to "PROG 57600". When "PROG 57600" appears, release your finger from the switch.
- 6. Check the connection between the TK-380 and the personal computer, and make sure that the TK-380 Is in the Program mode.
- 7. Press F10 on the personal computer. A window opens on the display to indicate progress of writing. When the TK-

- 380 starts to receive data. the [P] icon is blinking.
- 8. If writing ends successfully, the LED on the TK-380 lights and the checksum is displayed.
- 9. If you want to continue programming other TK-380 s, repeat steps 5 to 8.

Notes:

- To start the Firmware Programmer from KPG-49D, the Fpro path must be set up by KPG-49D Setup.
- This mode cannot be entered if the Firmware Programming mode is set to Disable in the Programming software (KPG-49D).
- When programming the firmware, it is recommend to copy the data from the floppy disk to your hard disk before update the radio firmware.
 - Directry copying from the floppy disk to the radio may not work because the access speed is too slow.

6-4. Function

- If you press the [MON] switch (top of left side) while "PROG 57600" is displayed, the checksum is displayed. If you press the [MON] switch again while the checksum is displayed, "PROG 57600" is redisplayed.
- 2. If you press the [LAMP] switch (bottom of left side) while "PROG 57600" is displayed, the display changes to "PROG 19200" to indicate that the write speed is low speed (19200 bps). If you press the [LAMP] switch again while "PROG 19200" is displayed, the display changes to "PROG 38400", and the write speed becomes the middle-speed mode (38400 bps). If you press the [LAMP] switch again while "PROG 38400" is displayed, the display returns to "PROG 57600".

Note:

Normally, write in the high-speed mode.

7. Clone Mode

Programming data can be transferred from one radio to another by connecting them via their external universal connectors. The operation is as follows (the transmit radio is the master and the receive radio is a slave).

- Turn the master TK-380 power ON with the [C] key held down. If the password is set to the TK-380, the TK-380 displays "CLONE LOCK". If the password is not set, the TK-380 displays "CLONE MODE".
- 2. When "CLONE LOCK" is displayed, only the knob (encoder) and [S], and [0] to [9] keys can be accepted. When you enter the correct password, and "CLONE MODE" is displayed, the TK-380 can be used as the cloning master. The following describes how to enter the password.
- How to enter the password with the keypad;
 If you press a key while "CLONE LOCK" is displayed the number that was pressed is displayed on the TK-380. Each press of the key shifts the display in order to the left. When

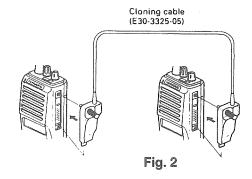
you enter the password and press the [S] key, "CLONE MODE" is displayed if the entered password is correct. If the password is incorrect, "CLONE LOCK" is redisplayed. How to enter the password with the encoder;

If the encoder is rotated while "CLONE LOCK" is displayed, numbers (0 to 9) are displayed flashing. When you press the [S] key, the currently selected number is determined. If you press the [S] key after entering the password in this procedure, "CLONE MODE" is displayed if the entered password is correct. If the password is incorrect, "CLONE LOCK" is redisplayed.

- 4. Power on the slave TK-380.
- 5. Connect the cloning cable (No. E30-3325-05) to the universal connectors on the master and slave.
- 6. Press the [S] key on the master while the master displays "CLONE MODE". The data of the master is sent to the slave. While the slave is receiving the data, "PROGRAM" is displayed. When cloning of data is completed, the master displays "END", and the slave automatically operates in the User mode. The slave can then be operated by the same program as the master.
- The other slave can be continuously cloned. When the [S] key on the master is pressed while the master displays "END", the master displays "CLONE MODE". Carry out the operation in step 4 to 6.

Note:

Only the same models can be cloned together.



8. Self Programming Mode

Write mode for frequency data and signalling etc. Mainly used by the person maintaining the user equipment.

8-1. Enter to the self programming mode

Delete R144 (SELF, Figure 3) in the TX-RX unit and turn the power switch on while pressing the [LAMP] key. When enter the self progrumming mode, "SELF PROG" is displayed.

Note

This mode (self programming mode) cannot be set when it has been disabled with the FPU.

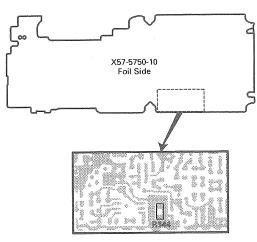


Fig. 3

8-2. Channel Setting Mode

This is a mode for making channel settings with the panel keys without using the FPU.

Pressing [MON] when [SELF PROG] is displayed, sets Channel Setting Mode.

Select an item set with [C] and change the selection with the encoder.

The data displayed with [B] is stored in the memory and then proceeds to the next item. Pressing [C] proceeds to the next item without storing it in the memory.

Press [MON] to set the display to [SELF PROG] and return to reset (default) status.

Flow Chart

	Self programming mode	
	[MON]	
[MON]	Channel set mode	Channel selection [C] Group selection
TRACALI	[B]	base propagation [10] Continue propagation
[MON]	RX frequency	[LAMP]
[MON] i	[B]/[C]	
-	RX signalling [B]/[C]	[LAMP] OFF [LAMP] QT [LAMP] DQT N S
[MON]	TX frequency	OFF
	[B]/[C]	[LAMP]
[MON]	TX signalling	OFF (LAMP) QT (LAMP) DQT N (S) DQT I
[MON]	[B]/[C]	[LAMP] [LAMP] [LAMP] [S] [S]
[INION]	Scan delete/add	
[MON]	[B]/[C]	
	Busy channel lockout yes/no [B]/[C]	1
[MON]	RF Power High/Low	
	[B]/[C]	
[MON]	Beat shift yes/no	
[MON]	[B]/[C]	
LIMON	Wide/Narrow	
	[B]/[C]	_

Items set in Channel Setting Mode are as follows.

Function settings	Display	Remarks
Channel select	CH or GRP	
RX Frequency	RXF	[LAMP] : Freq. On/Off switching
		[A] : 5kHz/6.25kHz/7.5kHz/1MHz
		step switching
RX Signalling	RXS	[LAMP] : OFF/QT/DQT switching
		[A]: 1 step/Standard switching
		[S]: DQT Normal/Invert swtiching
TX Frequency	TXF	Key operation same as RX
		Frequencies
TX Signalling	TXS	Key operation same as RX
		Signalling
Scan Del/Add	SCN	Delete/Add
Busy Channel	BSY	YES/NO
Lockout		
RF Power	PWR	HIGH/LOW
Beat Shift	SFT	YES/NO .
Wide/Narrow	W/N	Wide/Narrow

8-3. Function Setting Mode

This is a mode for using the panel keys to make function settings without using the FPU, that operate on all channels.

Pressing the [LAMP] when [SELF PROG] is displayed, sets the Function Setting Mode.

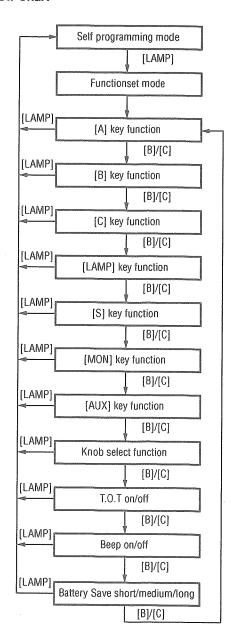
Select an item set with [C] and change the selection with the encoder.

Press [LAMP] to display [SELF PROG] and return to reset (default) status.

Items set in Function Set Mode are as follows.

Function settings	7	Domonto	
<u> </u>	Display	Remarks	
[A]	Α	Key Function	
[B]	В	Key Function	
[C]	С	Key Function	
[LAMP]	LAMP	Key Function	
[S]	S	Key Function	
[MON]	MON	Key Function	
[AUX]	AUX	Key Function	
[KNOB]	KNB	Knob Function	
T.O.T	TOT	ON/OFF at T.O.T all settings	
		ON:TOT[60s]/Pre-Alert[10s]/	
		Rekey Time[5s]/Reset Time[5s]	
		OFF:TOT[600s]/Pre-Alert[Off]	
		Rekey Time[Off]/Reset Time[Off]	
Beep	BEP	ON/OFF at BEEP all settings	
		ON:Power On Tone[On]/	
		Control Tone[On]/Warning Tone[On]	
		OFF:Power ON Tone[Off]/	
°		Control Tone[Off]/	
		Watning Tone[Off]	
Battery Save	BAT	OFF/SHORT/MEDIUM/LOG	

Flow Chart



8-4. Memory Reset Mode

This mode is used to clear data for functions that can be set in Self Programming Mode or to return to reset values (default).

Pressing [S] when [SELF PROG] is shown, sets the display to [CLEAR NO?].

Turning the encoder alternately switches the display between [CLEAR NO?] - [CLEAR YES?].

Pressing [S] when [CLEAR YES?] is shown, clears the data and sets the display to [ALL CLEAR].

Pressing [S] again, returns the display to [SELF PROG].

Pressing [S] when [CLEAR NO?] is shown, returns the display to [SELF PROG] without resetting the data.

1. Overview

This transceiver is UHF/FM portable transceiver designed to operate in the frequency range of 450 to 490MHz and an UHF/FM band EFJ LTR™ trunked system compatible FM portable transceive that can be programmed to operate on both LTR and conventional systems.

2. Circuit Configuration by Frequency

The receiver is a double-conversion superheterodyne with a first intermediate frequency (IF) of 44.85MHz and a second IF of 455kHz. Incoming signals from the antenna are mixed with the local signal from the PLL to produce the first IF of 44.85MHz.

This is then mixed with the 44.395MHz second local oscillator output to produce the 455kHz second IF. This is detected to give the demodulated signal.

The transmit signal frequency is generated by the PLL VCO, and modulated by the signal from the microphone. It is then amplified and sent to the antenna.

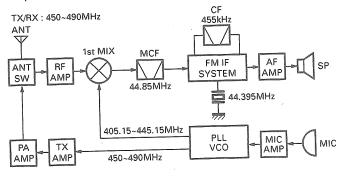


Fig. 1 Frequency configuration

3. Receiver System

3-1. RF unit

An incoming RF signal from the antenna terminal is passed through the antenna switch (D12, D14, and D15 are off) and

then the bandpass filter (L47,48,49,51,52,54,55,56). The bandpass filter is adjusted by a variable capacitor. The input voltage to the variable capacitor is regulated by the voltage output from the D/A converter (IC8). The signal is amplified by RF amplifier Q24, and passed through the bandpass filter (L29,31,33,34,36) and band-eliminate filter (L27) to remove the spurious signal again. The resulting signal is applied to the first mixer (IC18), where it is mixed with the first local oscillator signal output from the frequency synthesizer to produce the first IF (44.85MHz). The 1st mixer uses the GaAs IC.

3-2. IF unit

The first IF signal is passed through a four-pole monolithic crystal filter (XF1) to remove a adjacent channel signal. The filtered first IF signal is amplified by the first IF amplifier (Q12) and then applied to the IF system IC (IC12). The IF system IC provides a second mixer, second local oscillator, limiting amplifier, quadrature detector and RSSI (Received Signal Strength Indicator). The second mixer mixes the first IF signal with the 44.395MHz of second local oscillator output (crystal unit X2) and produces the second IF signal of 455kHz.

The second IF signal is passed through the ceramic filter (CF1; Wide, CF2; Narrow) to more remove the adjacent channel signal. The filtered second IF signal is amplified by the limiting amplifier and demodulated by the quadrature detector with ceramic discriminator (CD1). The demodulated signal is routed to the audio circuit.

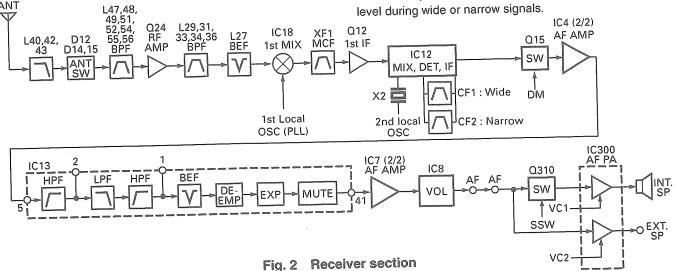
3-3. Wide/Narrow changeover circuit

Narrow and Wide settings can be made for each channel by switching the ceramic filters CF1 (Wide), CF2 (Narrow).

The WIDE (high level) and NARROW (low level) data is output from IC19 (microconputer) pin 99.

When a WIDE (high level) data is received, Q6 turn off and Q7 turn on. When a NARROW (low level) data is received, Q6 turn on and Q7 turn off. D5, D7 are switched to ceramic filters when a high/low level data is received.

Q9 turns on/off with the Wide/Narrow data and the IC12 detector output level is changed to maintain a constant output level during wide or narrow signals.



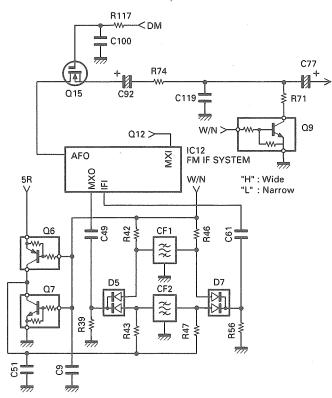


Fig. 3 Wide/Narrow changeover circuit

3-4. Audio amplifier circuit

The demodulated signal from IC12 goes through the mute switch (Q15) and is amplified by IC4 (2/2), high-pass filtered, low-pass filtered, high-pass filtered, band-eliminate filtered, and de-emphasized by IC13.

The signal then goes through an AF amplifier IC7 (2/2), an electronic volume control (IC8), and an AF switch (Q310 is on), and is routed to audio power amplifier (IC300), where it is amplified and output to the internal speaker.

The audio mute signal (AM) from the shift register becomes Low in the standby and Q304, Q305 which are power supply circuit for IC300 turn off. Also, IC13 is set to the power down mode according to data from microprocessor, and the AF signal is muted. When the audio is output, AM becomes High to turn Q304, Q305 ON, and voltage is supplied to power terminal VP of IC300. Also, IC13 is canceled out of the power down mode.

The speaker is switched by the logic of speaker switching terminal SSW on the universal connector. When SP-MIC is not attached, the logic of SSW becomes High and SW (Q310) is turned ON, and the AF signal is input to both amplifiers of IC300.

When SP-MIC is attached, SSW is connected to GND at inside of SP-MIC. For this reason, Q310 is turned OFF, and the AF signal is input only to amplifier for EXT SP of IC300.

Change of INT/EXT SP refer to Fig. 4.

į	AM	SSW	VC1	VC2	SP
į	Н	Н	Н	L	INT
	H	L	L	Н	EXT
	L	Н	L	L	MUTE
	L	L	L	L	MUTE

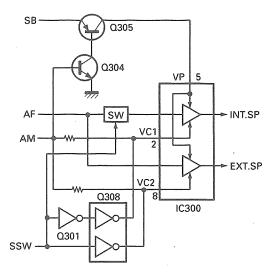


Fig. 4 Audio amplifier circuit

3-5. Squelch circuit

The output from IC12 enters FM IC again, then passed through a band-pass filter. The noise component output from IC12 is amplified by Q4 and rectified by D4 to produce a DC voltage corresponding to the noise level. The DC voltage is sent to the analog port of the CPU (IC19). And IC12 outputs a DC voltage (RSSI) corresponding to the input of the IF amplifier. The CPU reads the RSSI signal via pin 93.

IC19 determines whether to output sounds from the speaker by comparing the input voltage of pin 91 and pin 93 with the preset value.

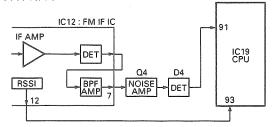


Fig. 5 Squelch circuit

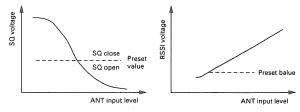


Fig. 6 Squelch and RSSI voltage vs ANT input level

4. Transmitter System

4-1. Microphone amplifier

The signal from the internal microphone goes through the mute switch (Q300).

When the SP-MIC is not attached, the microphone switching terminal (MSW) on the universal connector becomes High, and mute switch (Q300) is turned ON. When the SP-MIC is

attached, MSW is connected to GND at inside of SP-MIC. For this reason, Q300 is turned OFF, the internal microphone is muted, and only the input of the external microphone is supplied to the microphone amplifier of the TX-RX unit.

The signal from microphone passes through the limitter circuit in D8, Mic mute switch (Q17 is off in TX) and through the high-pass filter, the ALC circuit, the low-pass filter, the high-pass filter, and pre-emphasis/IDC circuit in IC13. When encoding DTMF, mute switch (Q13) is turned OFF for muting the microphone input signal.

The signal passes through the D/A converter (IC8) for the maximum deviation adjustment, and enters the summing amplifier consisting of IC7 (1/2), and is mixed with the low speed data from the CPU (IC19) and 9600bps DATA from Optional Board Terminal.

The output signal from the summing amplifier passes through the D/A converter (IC8) again and goes to the VCO modulation input.

The other output signal from the summing amplifier passes through the D/A converter (IC8) again for the BAL adjustment, and the buffer amplifier (IC1 (2/2)), and goes to the VCXO modulation input.

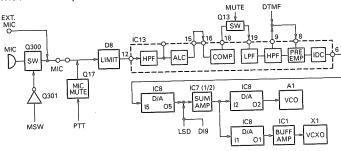


Fig. 7 Microphone amplifier

4-2. Drive and Final amplifier

The signal from the T/R switch (D9 is on) is amplified by the pre-drive (Q18) and drive amplifier (Q20) to 50mW.

The output of the drive amplifier is amplified by the RF power amplifier (IC100) to 4.0W (1W when the power is low). The RF power amplifier consists of two stages MOS FET transistor. The output of the RF power amplifier is then passed through the harmonic filter (LPF) and antenna switch (D12 is on) and applied to the antenna terminal.

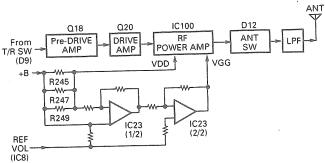


Fig. 8 Drive and final amplifier and APC circuit

4-3. APC circuit

The APC circuit always monitors the current flowing through the RF power amplifier (IC100) and keeps a constant current. The voltage drop at R245, R247 and R249 is caused by the current flowing through the RF power amplifier and this voltage is applied to the differential amplifier (IC23 1/2).

IC23(2/2) compares the output voltage of IC23(1/2) with the reference voltage from IC8, and the output of IC23(2/2) controls the VGG of the RF power amplifier to make the both voltages to same voltage.

The change of power high/low is carried out by the change of the reference voltage. Q22,23 and 25 are turned on in transmit and the APC circuit is active.

5. Frequency Synthesizer Unit

5-1. Frequency synthesizer

The frequency synthesizer consists of the VCXO (X1), VCO (A1), PLL IC(IC14) and buffer amplifiers.

The VCXO generates 16.8MHz. The frequency stability is 1.5ppm within the temperature range of -30 to +60°C. The frequency tuning and modulation of the VCXO are done to apply a voltage to pin 1 of the VCXO. The output of the VCXO is applied to pin 8 of the PLL IC.

The TK-380's VCO consists of 2VCO and covers a dual range of the 405.15~445.15MHz, and the 450~490MHz. The VCO generates 405.15~445.15MHz for providing to the first local signal in receive. In TX, the pin 3 of the VCO goes low and the VCO generates 450~490MHz.

The output of the VCO is amplified by the buffer amplifier (Q16) and routed to the pin 5 of the PLL IC. Also the output of the VCO is amplified by the buffer amplifier (Q18) and routed to the next stage according to T/R switch (D9, D23).

The PLL IC consists of a prescaler, fractional divider, reference divider, phase comparator, charge pump. This PLL IC is fractional-N type synthesizer and performs in the 40.50 or 60kHz reference signal which is eighth of the channel step (5, 6.25 or 7.5kHz). The input signal from the pins 5 and 8 of the PLL IC is divided down to the 40, 50 or 60kHz and compared at phase comparator. The pulsed output signal of the phase comparator is applied to the charge pump and transformed into DC signal in the loop filter (LPF). The DC signal is applied to the pin 1 of the VCO and locked to keep the VCO frequency constant.

PLL data is output from DT (pin 75). CP (pin 19) and EP (pin 47) of the microprocessor (IC19). The data are input to the PLL IC when the channel is changed or when transmission is changed to reception and vice versa. A PLL lock condition is always monitored by the pin 31 (UL) of the microprocessor. When the PLL is unlocked, the UL goes low.

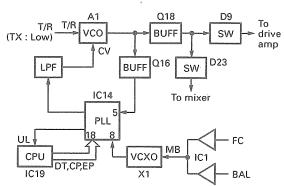


Fig. 9 PLL block diagram

6. Control Circuit

The control circuit consists of microprocessor (IC19) and its peripheral circuits. It controls the TX-RX unit and transfers data to and from the display unit. IC19 mainly performs the following;

- 1) Switching between transmission and reception by PTT signal input.
- 2) Reading system, group, frequency, and program data from the memory circuit.
- 3) Sending frequency program data to the PLL.
- 4) Controlling squelch on/off by the DC voltage from the squelch circuit.
- 5) Controlling the audio mute circuit by decode data input.
- 6) Transmitting tone and encode data.

6-1. Memory circuit

Memory circuit consists of the CPU (IC19) and a flash memory (IC17), a flash memory has a capacity of 2M bits that contains the transceiver control program for the CPU and data such as transceiver channels and operating features.

This program can be easily written from an external devices. Data. such as DTMF memories and operating status, are programmed into the EEPROM (IC20).

Flash Memory

Note: The flash memory holds data such as written with the FPU (KPG-49D), firmware program (User mode, Test mode, Tuning mode, etc.) This data must be rewritten when replacing the flash memory.

• EEPROM

Note : The EEPROM stores tuning data (Deviation, Squelch, etc.).

Realign the transceiver after replacing the EEPROM.

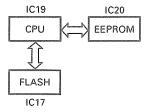


Fig. 10 Memory circuit

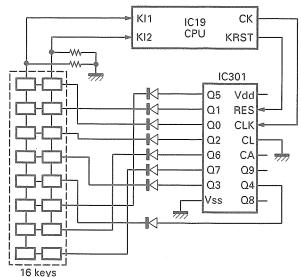
6-2. Low battery warning

The battery voltage is monitored by the microprocessor (IC19). When the battery voltage falls below the voltage set by the Low Battery Warning adjustment, the red LED flashes to notify the operator that it is time to replace the battery. If the battery voltage falls even more (approx. 5.8V), a beep sounds and transmission is stopped.

Low battery warning	Battery condition
The red LED flashes during transmission	The battery voltage is low but the transceiver is still usable.
The red LED flashes and continuous beep sounds while PTT pressed	The battery voltage is low and the transceiver is not usable to make calls.

6-3. Key input

If the clock is supplied to CLK terminal when the RES terminal (CPU pin 78) of the decade counter (IC301) is set to Low, Q0 to Q7 become High sequentially. Normally, KI1 and KI2 are Low (pulled down). When any key is pressed, KI1 or KI2 become High. The CPU detects which key is pressed, according to the voltage of KI1 and KI2 and clock timing.



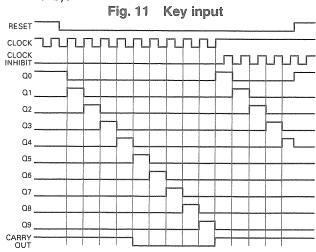


Fig. 12 Decade counter timing chart

7. Signalling Circuit

7-1. Encode

Low-speed data (QT,DQT,LTR)

Low-speed data is output from pin 1 of the CPU. The signal passes through a low-pass CR filter, and goes to the summing amplifier (IC7 1/2). The signal is mixed with the audio signal and goes to the VCO (A1) and VCXO (X1) modulation input after passing through the D/A converter (IC8) for BAL adjustment.

• High-speed data (DTMF)

High-speed data is output from pin 2 of the CPU. The signal passes through a low-pass filter consisting of IC10, and provides a TX DTMF tone and a RX DTMF tone TX DTMF deviation making an adjustment by microprocessor is passed through the D/A convertor (IC8), and then applied to the audio processor (IC13).

The signal is mixed with the audio signal and goes to the VCO and VCXO, the RX DTMF tone is passed a summing amplifier (IC7 2/2), the D/A converter (IC8) for audio control, audio power amplifier and then to the speaker.

MSK (ESN)

ESN utilizes 1200bps MSK signal. MSK signal is output from pin 6 of IC13. The signal passes through the D/A converter (IC8) for the MSK deviation adjustment, and is routed to the VCO. When encoding MSK, the microphone input signal is muted.

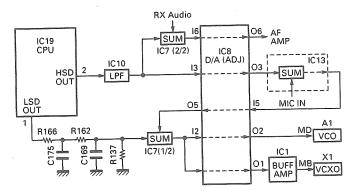


Fig. 13 Encode

7-2. Decode

Low-speed data (QT,DQT,LTR)

The demodulated signal from the IF IC (IC12) is amplified by IC4 (2/2) and passes through a low-pass filter (IC11) to remove audio components. The signal is input to pin 95 of the CPU.

The CPU digitizes this signal, performs processing such as DC restoration, and decodes the signal.

High-speed data (DTMF)

The DTMF input signal from the IF IC (IC12) is amplified by IC4 (2/2) and goes to IC16, the DTMF decoder. The decoded information is then processed by the CPU. During transmission and standby, the DTMF IC is set to the power down mode when the PD terminal is High. When the line is busy, the PD terminal becomes Low, the power down mode is canceled and decoding is carried out.

• High-speed data (2 tone)

The demodulated signal from the IF IC (IC12) is amplified by IC4 (2/2) and passes through an audio processor (IC13) and band-pass filter (IC2) to remove a low-speed data. The CPU digitizes this signal, performs processing such as DC restoration, and decodes the signal.

MSK (ESN)

The MSK input signal from the IF IC is amplified by IC4 (1/2) and goes to pin 5 of IC13. The signal is demodulated by MSK demodulator in IC13. The demodulated data goes to the CPU for processing.

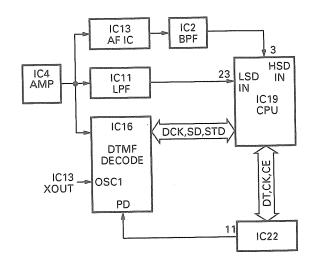


Fig. 14 Decode

8. Power Supply Circuit

Battery +B is supplied via a 3A fuse from the battery terminal connected to the TX-RX unit. After passing through the power switch, power supply (SB) is applied to the three AVRs. IC5 supplies 5V (5M) to the control circuit, and IC9 supplies 5V (5C) to common circuits. IC6 supplies to the TX circuit, the RX circuit and common circuits of needless save mode. During transmission, 5TC becomes Low and Q3 is turned ON to supply 5V (5T) to the TX circuit. During reception, 5RC becomes Low and Q2 is turned ON to supply 5V (5R) to the RX Circuit.

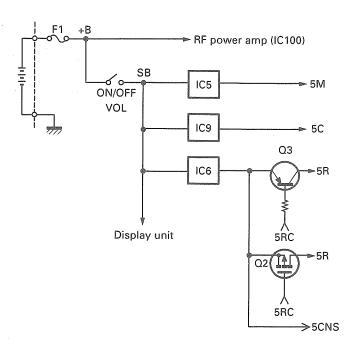


Fig. 15 Power supply circuit

9. Optional Board Terminal

Terminals for mounting the option board are provided at the bottom edge of the TX-RX unit. The table below shows the correspondence between the board and terminals. R37, R69, R250, R259, R260, R276, R280 may have to be removed depending on the type of option board being used.

Name	Function
SB	Battery (7.5V)
GND	Ground
TXD	Serial data
RXD	Serial data
SQ	Busy: high
LOK	Link acquired : low (TX mode)
DI/ANI	Modulation (ANI) input
DEO	Detect output
TXAI/MUTE	Modulation output from board or mic mute: low
TXAO	Modulation input to board
RXAI	Received signal input to board
RXAO	Received signal output from board
D1	Binary 1
D2	Binary 2
OPT	Scramble, Emergency:low
PTTIN	PTT switch signal input to board (TX:low)
5CNS	Battery (5V)
DI9	9600 bps data output
RXEMAO	Received signal output from board (after de-
,	emphasis)
RXEMAI 🐣	Received signal input to board (after de-
	emphasis)
PTTOUT	PTT switch signal output from board (TX:low)
MONI	Busy:low
LAMP	Busy:low
AAC	Audio Amp Control signal output from board
	(Busy:high)
Audio Beep	Beep signal output from board.
AUX TXD	Serial data
AUX RXD	Serial data

Table 1 Terminal name and function

Microprocesser: 30612M4A-407GP (TX-RX UNIT: IC19)

Pin function

	Pin function				
Pin No.	Port Name	1/0	Function		
1	LSDOUT	0	Low speed data output.		
2	HSDOUT	0	High speed data output.		
3	HSDIN	ı	High speed data input.		
4	DTMSTD	ī	DTMF decode IC data detect input.		
5	SELF	ı	Self programming mode input.		
6	BYTE	1	÷5V.		
7	CNVSS	Ī	GND.		
8	SFTOE	0	Shift register output enable.		
9	LCDCS	0	LCD driver chip select output.		
10	RESET	1	Microcomputer reset input.		
11	XOUT	-	9.8304MHz (System clock).		
12	VSS	-	GND.		
13	XIN	-	9.8304MHz (System clock).		
14	VCC	-	+5V		
15	AUX		AUX switch input.		
16	AFTRD	<u> </u>	MSK modulation data output timing pulse		
'0	, 11 17 10	'	input.		
17	AFRTM		MSK demodulation data input timing pulse		
1/	ALD HVI	'	input.		
10	EN2		Encoder pulse input 2.		
18		0	PLL IC clock output.		
19	PLLCLK				
20	BEEP	0	Beep data output.		
21	AFRDT	<u> </u>	MSK demodulation data input.		
22	AFREG1	0	AFIC register switching data output 1.		
23	AFREG2	0_	AF IC register switching data output 2.		
24	EEPDAT	0	EEPROM data output.		
25	DACSTB	0	D/A converter IC data strobe output.		
26	AFCLR	0	MSK flame reset output.		
27	SAVE	0	Battery save output.		
28	LAMP	<u> </u>	LAMP switch input.		
29	AUXTXD	0	External Serial interface output.		
30	AUXRXD	<u> </u>	External Serial interface input.		
31	PLLUL	1	PLL unlock detect input.		
32	AFMSKE	0	MSK modulation enable (Enable active "H").		
33	TXD	0	Serial interface output (ex. PC).		
34	RXD	ı	Serial interface input (ex. PC).		
35	AFDAT	0	MSK data output.		
36	PTT	ı	PTT switch input.		
37	RDY	-	Not used.		
38	ALE	-	Not used.		
39	HOLD	-	Not used.		
40	HLDA	-	Not used.		
41	BLCK	-	Not used.		
42	RD	-	Flash memory RD bus.		
43	BHE	-	Not used.		
44	WR	-	Flash memory WR bus.		
45	DTMCLK	0	DTMF decode IC clock output.		
46	CNTCLK		Common clock output.		
47	PLLSTB	0	PLL IC data strobe output.		
48	CS0	0	Flash memory chip enable.		
49	A19	-	Not used.		
1	A9~A18	-	Flash memory address bus.		
60	VCC	-	+5V		
61	A8	-	Flash memory address bus.		
UI	170	<u></u>	1 radii memory address bus.		

Pin	Port		
No.	Name	1/0	Function
62	VSS	-	GND.
63~70	A0~A7	-	Flash memory address bus.
71	MONI	1	Monitor switch input.
72	EN4	I	Encoder pulse input 4.
73	EN3	Ī	Encoder pulse input 3.
74	EN1		Encoder pulse input 1.
75	MINDAT	0	Common data output.
76	KEY2	I	Key scan input 2.
77	KEY1	ı	Key scan input 1.
78	RESET	0	Key scan IC reset output
79~86	D0~D7	-	Flash memory data bus.
87	DTMDAT	ı	DTMF decode IC data input.
88	PF	ı	PF switch input.
89	VOL	l I	Volume level input.
90	BATT	1	Battery voltage input.
91	ANLSQL	1	Squelch level input.
92	TEMP	Π	Thermistor input.
93	RSSI	Ī	Received signal strength indicator input
			(RSSI).
94	AVSS	-	GND.
95	LSDIN	I	Low speed data input.
96	VREF	-	+5V
97	AVCC	-	+5V
98	SFTSTB1	0	Shift register data strobe output.
99	W/N	0	Wide/Narrow switching output.
100	AFSTB	0	AF IC data strobe output.

Shift register 1 : BU4094BCFV (TX-RX UNIT : IC21)

Pin function

Pin No.	Port	Port Name	Function
4	Q1	LEDR	Red LED. H:ON, L:OFF
5	Q2	LEDG	Green LED. H:ON, L:OFF
6	Q3	KEYBLT	Key back light. H:ON, L:OFF
7	Q4	MMUTE	Mic mute. H:Unmute, L:Mute
14	Q5	5RC	RX power control. H:TX, L:RX
13	Q6	5TC	TX power control. H:RX, L:TX
12	Q7	BSHIFT	Beat shift. H:ON, L:OFF
11	Q8	DTMPD	DTMF decode IC power down.
			H:Power Down, L:Busy

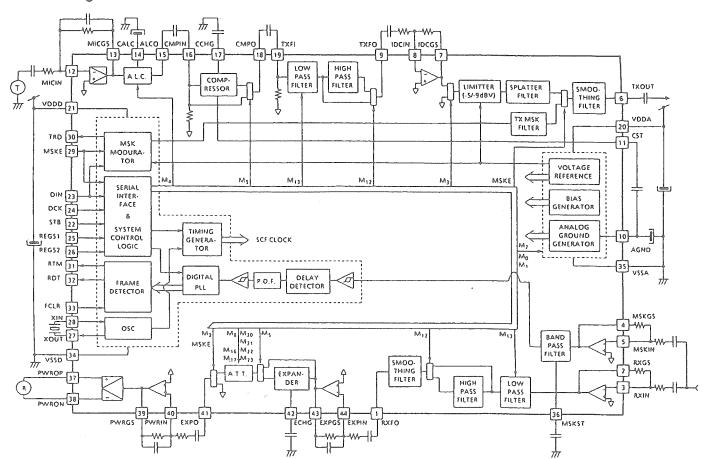
Shift register 2 : BU4094BCFV (TX-RX UNIT : IC22)

Pin function

Pin No.	Port	Port Name	Function
4	Q1	AM1	Audio mute 1. H:Unmute, L:Mute
5	Q2	LOK	Link complete. (Programmable active H/L)
6	Q3	T/R	TX/RX switching. H:RX, L:TX
7	Q4	DM	Dead mute. H:RX, L:TX
14	Q5	OPT	Option board control. H:ON, L:OFF
			Auxiliary. (Programable active H/L)
13	Q6	CODE1	Option board data 1. H:ON, L:OFF
12	Q7	CODE2	Option board data 2. H:ON, L:OFF
11	Q8	SQ	External squelch. (Programmable active H/L)

Audio Processor : TC35453F (TX-RX Unit IC13)

Block diagram



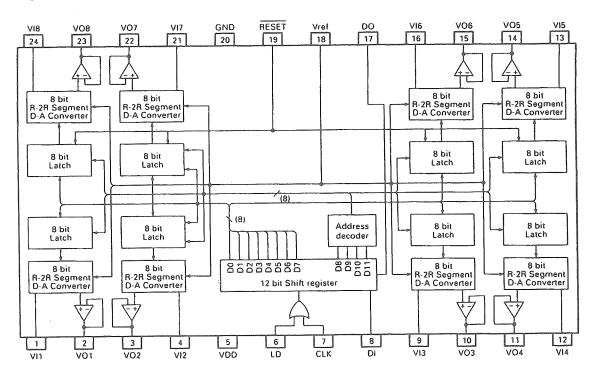
■ Pin function

Pin No.	Port Name	VO	Function
1	RXFO	0	RX audio filter output.
2	RXGS	0	RX audio signal level setting amplifier
			output.
3	RXIN	1	RX audio signal level setting amplifier input.
4	MSKGS	0	MSK RX level setting amplifier output.
5	MSKIN	I	MSK RX level setting amplifier input.
6	TXOUT	0	TX signal output.
7	IDCGS	0	IDC input level setting amplifier output.
8	IDCIN	I	IDC input level setting amplifier input.
9	TXFO	0	TX audio filter circuit output.
10	AGND	-	Analog reference voltage stabilization.
11	CST	1/0	Compressor stabilization.
12	MICIN	ı	Microphone amplifier input.
13	MICGS	0	Microphone amplifier output.
14	CALC	1/0	ALC Circuit response time setting.
15	ALCO	0	ALC circuit output.
16	CMPIN	1	Compressor input.
17	CCHG	1/0	Compressor response time setting.
18	CMPO	0	Compressor output.
19	TXFI	ı	TX audio filter input.
20	VDDA	-	Analog power supply.
21	VDDD	-	Digital power supply.
22	STB	I	Data strobe pulse input / system reset input 1.
23	DIN	1	Data input.

(
Pin No.	Port Name	I/O	Function
24	DCK	1	Data synchronized clock input.
25	REGS1	I	Internal register select input 1
26	REGS2	I	Internal register select input 2.
27	XOUT	0	Oscillation circuit output.
28	XIN	ı	Oscillation circuit input.
29	MSKE	- 1	MSK modulation enable input.
30	TRD	0	MSK modulation data latch timing output.
31	RTM	0	MSK RX synchronized clock output.
32	RDT	0	MSK RX data output.
33	FCLR	I	Flame detect circuit reset input 1
			System reset input 2.
34	VSSD	-	Digital ground.
35	VSSA	-	Analog ground.
36	MS KST	I/O	MSK modem demodulation circuit
			stabilization.
37	PWROP	0	Speaker operation positive output.
38	PWRON	0	Speaker operation negative output.
39	PWRGS	0	RX output level setting amplifier output.
40	PWRIN		RX output level setting amplifier input.
41	EXPO	0	Expander output.
42	ECHG	I/O	Expander response time setting.
43	EXPGS	0	Expander input level setting amplifier output.
44	EXPIN	l	Expander input level setting amplifier input.

D/A Converter : M62364FP (TX-RX Unit IC8)

■ Block diagram



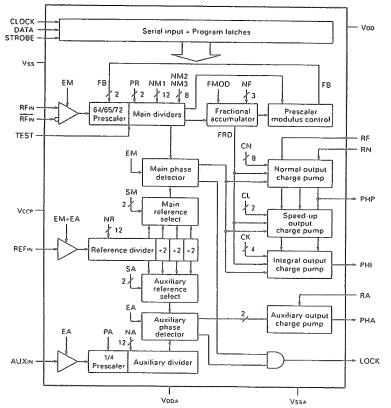
Pin function

Pin No.	Pin code	VO	Function
1	VI1	1	D/A converter input.
2,3	VO1,VO2	0	8-bit resolution D/A.
4	VI2	I	D/A Converter input.
5	Voo	-	Power supply.
6	LD	I	When the LD is at the low level, the clock
			input reception mode is entered. and data
			can be uptaken by the 12-bit shift register.
			Then at the threshold rising from low to
			high, the 12-bit shift register value is loaded
			to the D/A output register.
7	CLK	T	Shift clock input. With the rise of the shift
			clock, the input signal from the DI is input
			to the 12-bit shift register.
8	DI	1	Serial data input. Input serial data 12 bits long.
9	VI3	I	D/A converter input.

Pin No.	Pin code	1/0	Function
10,11	VO3,VO4	0	8-bit resolution D/A.
12,13	VI4,VI5	١	D/A converter input.
14,15	VO5,VO6	0	8-bit resolution D/A.
16	VI6	1	D/A converter input.
17	DO	0	12-bit shift register MSB bit data is output.
18	VREF	-	Terminal for determining the D/A
			Conversion reference point level.
			Vo = (VIN - VDAref) x n/256 + VDAref
19	RESET	-	When a low level signal is input to the
			RESET terminal, all the D/A output register
			value become low.
20	GND	-	GND.
21	VI7	П	D/A converter input.
22,23	VO7,VO8	0	8-bit resolution D/A.
24	VI8		D/A converter input.

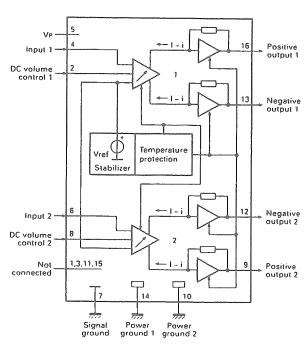
PLL System: SA7025DK (TX-RX Unit IC14)

■ Block diagram



Audio Power Amplifier: TDA7053AT (Display Unit IC300)

■ Block diagram



Pin description

Pin No.	Symbol	Description
1	CLOCK	Serial clock input.
2	DATA	Serial data input.
3	STROBE	Serial strobe input.
4	Vss	Digital ground.
5	RFIN	Prescaler positive input.
6	RFIN	Prescaler negative input.
7	VCCP	Prescaler positive Supply voltage. This pin supplies
		power to the prescaler and RF input buffer.
8	REFIN	Reference divider input.
9	RA	Auxiliary current setting; resistor to Vssa.
10	AUXin	AuxIliary divider input.
11	PHA	Auxiliary phase detector output.
12	Vssa	analog ground.
13	PHI	Integral phase detector output.
14	PHP	Proportional phase detector output.
15	VDDA	Analog supply voltage. This pin supplies power
		to the charge pumps, Auxiliary prescaler.
		Auxiliary and Reference buffers.
16	RN	Main current setting; resistor to Vssa.
17	RF	Fractional compensation current setting;
		resistor to Vssa.
18	LOCK	Lock detector output.
19	TEST	Test pin; connect to VDD.
20	Vdd	Digital supply voltage. This pin supplies power
***************************************		to the CMOS digital part of the device.

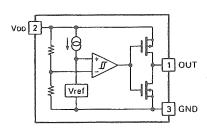
Pin description

Pin No.	Symbol	Description
1	NC	Not connected.
2	VC1	DC volume control 1.
3	NC	Not connected.
4	VI(1)	Voltage input 1.
5	VP	Positive Supply voltage.
6	V1(2)	Voltage input 2.
7	SGND	Signal ground.
8	VC2	DC volume control 2.
9	OUT2+	Positive output 2.
10	PGND2	Power ground 2.
11	NC	Not conncted.
12	OUT2-	Negative output 2.
13	OUT1-	Negative output 1.
14	PGND1	Power ground 1.
15	NC	Not connected.
16	OUT1÷	Positive output 1.

Voltage Detector: RN5VL42C

(TX-RX Unit IC3)

■ Block diagram (CMOS output)

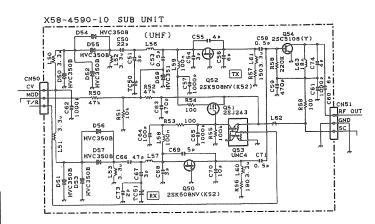


Pin function

Pin No.	Pin code	Function
1	OUT	Output.
2	Vod	Power supply.
3	GND	Ground.

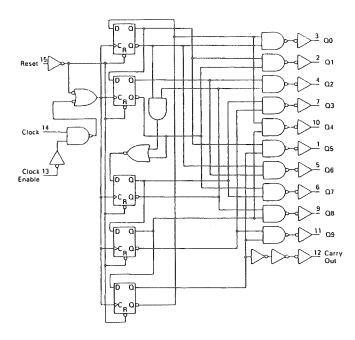
VCO System: X58-4590-10 (SUB Unit: A1)

Circuit diagram



Counter: MC74HC4017F (Display Unit IC301)

Logic circuit



Input

CLOCK (pin No.14) - Clock Input
The rising edge of this clock advances the count.

Controller input

RESET (pin No.15) - Asynchronous Reset Input
When this pin is High, the counter is initialized. and Q0 and
CARRY OUT output become High. At this time, Q1 to Q9
become Low.

CLOCK ENABLE (Pin No.13) - Clock Enable Input (Low active) The count operation is forbidden when this pin is High. When it is Low. the normal count is carried out. When the clock input (pin No.14) is used as enable (High active). this input can be used for the count as the rising clock.

Output

Q0 to Q9 (pins 3,2,4,7,10,1,5,6,9,11) - Decoded Decade Counter Output

These outputs become High only during a single clock cycle.

CARRY OUT (pin No.12) - Cascade Output Pin

This output is used as the cascade output, or as the ÷10 output during the 50% duty cycle. When the count reaches "5", this output becomes Low. When the count reaches "0" or is reset, this output becomes High. When counters are cascade-connected, this output sends the rise signal to clock input of the next counter.

DESCRIPTION OF COMPONENTS

DISPLAY UNIT (X54-3210-XX)

Ref. No.	Use/Function	Onerat	ion/Condition
IC300	IC	Audio power am	
L			
IC301	IC	Counter	/Key scan
Q300	FET	DC switch	/ INT MIC on/off
Q301	FET	DC switch	
Q302	Transistor	DC switch	/ LED (Red) driver
Q303	Transistor	DC switch	/ LED (Green) driver
Q304	Transistor	DC switch	
Q305	Transistor	Current driver	/ Audio amp AVR
Q306	Transistor	DC switch	
Q307	Transistor	Current driver	/ LCD back light LED AVR
Q308	FET	DC switch	/ SP INT/EXT
Q309	Transistor	Temperature cor	mpensation
Q310	FET	Mute switch	
D300	Zener diode	Surge absorption	1
D301	LED	LED	/ Red, Green
D302	Diode	Quick discharge	/AF mute
D303	Zener diode	Voltage referenc	е
D304	Diode	Voltage referenc	е
D305~310	LED	LCD back light	
D315~318	Diode	Reverse current	prevention
D319~321	Zener diode	Surge absorption	1

TX-RX UNIT (X57-5750-10)

Ref. No.	Use/Function	Operation/Condition
IC1,2	IC	Buffer amplifier
IC3	IC	Voltage detector / Reset
IC4	IC ·	Buffer amplifier
IC5	IC	Voltage regulator / 5M
IC6	IC	Voltage regulator / 5V
IC7	IC	Buffer amplifier
IC8	IC	D/A converter (Adjustment)
IC9	IC	Voltage regulator / 5C
IC10	IC	Active filter / For HSDout
IC11	IC	Active filter / For LSDin
IC12	IC	FM IF system
IC13	IC	Audio processor
IC14	IC	PLL system
IC16	IC	DTMF decoder
IC17	IC	Flash memory
IC18	IC	Active DBM
IC19	IC	Microprocessor
IC20	IC	EEPROM
IC21,22	IC	Shift register / Output expander
IC23	IC	Comparator (APC)
IC24	IC	Analog switch
Q1	Transistor	Switch
Q2	FET	DC switch / 5R
Q3	Transistor	DC switch / 5T
Q4	Transistor	Noise amplifier / Squelch
Q5	FET	DC switch / Save
Q6	Transistor	2 nd IF W/N switch sets to on when Narrow
Q7	Transistor	2 nd IF W/N switch sets to on when Wide

Ref. No.	Use/Function	Operation/Condition
Q8	Transistor	Ripple filter
Q9	Transistor	DC switch / W/N audio amplitude adjust
Q10	Transistor	AF mute switch
Q11	FET	Mute switch
Q12	Transistor	IF amplifier
Q13	FET	Mute switch / MIC line mute
Q14	FET	DC switch
Q15	FET	DET mute
Q16	Transistor	PLL IC fin amplifier
Q17,18	Transistor	Buffer amplifier
Q19	Transistor	Clock frequency shift
Q20	Transistor	RF amplifier / TX driver
Q21	FET	DC switch
Q22	Transistor	DC switch
Q23	FET	DC switch
Q24	FET	RF amplifier
Q25	Transistor	DC switch
Q26	FET	Mute switch / MIC line mute
D1	Diode	Reverse protection
D2	Diode	Overload protection
D3	Diode	Reverse current protection
D4	Diode	Noise detection
D5	Diode	RF switch (2 nd IF wide/narrow)
D6	Diode	Current steering
D7	Diode	RF switch (2 nd IF wide/narrow)
D8	Diode	Voltage clamp
D9	Diode	TX/RX switch
D10	Diode	Overload protection
D12,14,15	Diode	ANT switch
D16	Diode	Overload protection
D17,18	Diode	Surge absorption
D20,22	Diode	Varactor tuning
D23	Diode	Voltage drop
D24,25	Diode	ANT switch

SUB UNIT (X58-4590-10)

Ref. No.	Use/Function	Operation/Condition
Q50	FET	VCO oscillation
Q51	FET	DC switch
Q52	FET	VCO oscillation
Q53	Transistor	DC switch
Q54	Transistor	RF Buffer amplifier
D50-D57	Diode	Frequency control
D58	Diode	TX modulation

* New Parts. △ indicates safety critical components.

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

L: Scandinavia K: USA Y: PX (Far East, Hawaii) T: England Y: AAFES (Europe) X: Australia X: Australia

P: Canada E: Europe M: Other Areas

Ref. No.	Address	New parts	Parts No.	Description	Destination	Ref. No. A
		P	TK-380	(Y50-488)		50
1	1A		A02-2054-43	CABINET ASSY(4 KEYS)	K,M	SP
1	1A		A02-2055-43	CABINET ASSY(16 KEYS)	K4	ANT
2	3B		A62-0535-04	PANEL ASSY		MIC300
3	2C		B09-0363-03	CAP (SP/MIC) ACSY		IC100
4	2A	*	B38-0810-05	LCD ASSY		-
5	1B		B43-1106-14	BADGE (KENWOOD)	K,K4	51
6	1C		B46-0470-00	WARRANTY CARD ACSY INSTRUCTION MANUAL ACSY	K,K4	D
7	1C	*	B62-0967-00	THUTTION TO THE TENT		C301
7	1C	*	B62-0988-00	INSTRUCTION MANUAL ACSY	M	C302
, 8	3B	*	B72-1447-04	MODEL NAME PLATE		C304
						C305 C307
9	3B	*	E04-0416-05	RF COAXIAL RECEPTACLE(SMA)		U307
10	3A		E23-1048-05	TERMINAL (BATT-)		C308
11	3B	*	E23-1101-05	TERMINAL (BATT+) TERMINAL (ANT)		C309
12	2B	*	E23-1104-04	FLAT CABLE		C310
13	2B		E37-0672-05	I LAT CAUCE		C311
1.4	зА		F37-0673-05	LEAD WIRE WITH CONNECTOR(PTT)		C312
14 15	1A		E37-0674-05	LEAD WIRE WITH CONNECTOR(SP)		
15 16	3B		E58-0440-05	SQUARE SOCKET (SP/MIC)		C313
,,,	"					C314
17	2A	*	F10-2310-03	SHIELDING PLATE(LCD)		C315 C316,317
18	3B		F10-2255-04	SHIELDING PLATE(P-MODULE)		C318
19	2B	*	F10-2271-03	SHIELDING CASE (FRONT END)		6310
20	2B	*	F10-2272-03	SHIELDING CASE (DBM)		C321-333
21	2A	*	F10-2274-03	SHIELDING CASE (VCO-OUT)		C335-339
	0.4	*	F20-3303-04	INSULATING SHEET(MIC/GND)		C340
22	2A	"	F2U-33U3-U4	1400E411140 ONCE 1111147 E1127	Ì	C341-344
23	1A		G01-0881-04	COIL SPRING		CN300
23 24	1B		G09-0418-05	KNOB SPRING (VOL,ENC)		
25	1B	Ì	G10-0799-04	FIBROUS SHEET (SP)		CN301
27	3A		G11-2544-04	SHEET (SHASSIS)		CN302
29	3A	*	G11-2582-04	SHEET (PTT)		CN303
l	- 1			DACKING (TOP)		CN304 L300,301
31	3B		G53-0811-03	PACKING (TOP) PACKING (BATT+)		[[500,501
32	3A		G53-0814-04	PACKING (BATT+) PACKING (4 KEYS)	K.M	L302,303
33	18		G53-0840-02	PACKING (4 KEYS)	K4	L304,305
34	1B		G53-0841-02	1 WOKING (10 KETO)		L306,307
25	2D		H12-3014-02	PACKING FIXTURE		L308,309
35	1D			CARTON BOARD		CP300,301
36 37	3D		1110 1012 01	ITEM CARTON CASE		
1	"					CP302
38	1A		J19-1572-04	HOLDER		CP303
39	2A		* J21-8380-03	HARDWARE FIXTURE(P-MODULE)	v	R300 R301
40	2C		J29-0618-15	HOOK ACS	'	R301
41	3B		J82-0045-05	FPC (VOL,ENC)		11304
42	3B		* J82-0066-05	FPC (SQUARE SOCKET)		R303
10			K29-5157-03	KNOB (PTT etc)		R304
43	1A 1A		K29-5158-03	KEY TOP (PTT etc)		R305
44 45	1A		K29-5165-03	LEVER KNOB	1	R306
45	1B		K29-5231-03	KNOB (VOL)	į	R307
47	1B		K29-5232-03	KNOB (ENC)		Doon
1						R308
Α	3B		N14-0569-04	CIRCULAR NUT (VOL,ENC)		R309 R310
В	3B		N30-2604-46	PAN HEAD MACHINE SCREW(ANT)	=,	R310
C	3A		N30-2610-46	PAN HEAD MACHINE SCREWICASI		R312
D	2A		N67-2606-46	PAN HEAD SEMS SCREW(P-MODU PAN HEAD TAPTITE SCREW(UNIT)	LL)	11012
E	2A		N83-2005-46	SCREW SET AC	SY	R313,314
48	2C		N99-2004-05	SUNEW SLI	-	R315
	1	- 1		1	1	1 1 0040
49	3B	- 1	R31-0617-05	VARIABLE R (POWER SW/VOL)	1	R316

Ref. No.	Address	New parts	Parts No.	Description	Destination
50	2B		S70-0414-05	TACT SWITCH (AUX SW)	
SP ANT	1B 2D	*	T07-0347-05 T90-0682-05 T91-0579-05	SPEAKER WHIP ANTENNA ACSY MIC ELEMENT	M
MIC300 IC100	2A 2A	*	M68732HA	IC(P-MODULE (440-490MHz)	
51	3B	į	W02-1814-05	ENCODER	
[]		AY		3210-XX) -10:K,M -11:H	(4
C301 C302 C304 C305 C307			CC73GCH1H470J C92-0560-05 CK73FB1C474K CC73GCH1H101J CK73GB1C104K	CHIP C 47PF J CHIP-TAN 10UF 6.3WV CHIP C 0.47UF K CHIP C 100PF J CHIP C 0.10UF K	
C308 C309 C310 C311 C312			CC73GCH1H101J CK73FB1C474K CK73GB1C104K CC73GCH1H470J CK73GB1C104K	CHIP C 100PF J CHIP C 0.47UF K CHIP C 0.10UF K CHIP C 47PF J CHIP C 0.10UF K	
C313 C314 C315 C316,317 C318			C92-0628-05 C92-0647-05 CC73GCH1H101J CC73GCH1H470J CC73GCH1H101J	CHIP-TAN 10UF 10WV TAN C 3.3UF 4WV CHIP C 100PF J CHIP C 47PF J CHIP C 100PF J	
C321-333 C335-339 C340 C341-344 CN300			CC73GCH1H470J CC73GCH1H470J CK73GB1E153K CC73GCH1H470J E40-5891-05	CHIP C 47PF J CHIP C 47PF J CHIP C 0.015UF K CHIP C 47PF J FLAT CABLE CONNECTOR(24P:TX-RX	
CN301 CN302 CN303 CN304 L300,301			E40-5892-05 E40-5662-05 E40-5887-05 E40-5823-05 L92-0141-05	FLAT CABLE CONNECTOR(14P) PIN ASSY SOCKET (SP) PIN ASSY (PTT) FLAT CABLE CONNECTOR(LCD) FERRITE CHIP	
L302,303 L304,305 L306,307 L308,309 CP300,30			L92-0138-05- L92-0141-05 L92-0138-05 L92-0141-05 R90-0723-05	FERRITE CHIP FERRITE CHIP FERRITE CHIP FERRITE CHIP MULTI-COMP 47K X2	
CP302 CP303 R300 R301 R302			R90-0724-05 R90-0724-05 RK73GB1J103J RK73FB2A101J RK73GB1J470J	MULTI-COMP 1K X4 MULTI-COMP 1K X4 CHIP R 10K J 1/16W CHIP R 100 J 1/10W CHIP R 47 J 1/16W	K4
R303 R304 R305 R306 R307			RK73GB1J471J RK73GB1J182J RK73GB1J104J R92-1252-05 RK73GB1J821J	CHIPR 1.8K J 1/16W CHIPR 100K J 1/16W CHIPR 0 0HM CHIPR 820 J 1/16W	
R308 R309 R310 R311 R312		-	RK73GB1J153J R92-1252-05 RK73GB1J331J RK73GB1J102J RK73GB1J104J	CHIPR 15K J 1/16W CHIPR 0 0 HM CHIPR 330 J 1/16W CHIPR 1.0K J 1/16W CHIPR 100K J 1/16W	
R313,3 R315 R316 R317	14		RK73GB1J102J RK73GB1J104J RK73GB1J473J RK73GB1J472J	CHIPR 1.0K J 1/16W CHIPR 100K J 1/16W CHIPR 47K J 1/16W CHIPR 4.7K J 1/16W	į.

DISPLAY UNIT (X54-3210-XX) TX-RX UNIT (X57-5750-10)

Ref. No.	Address	New parts	Parts No.		Descri	ptior)	Desti	ination	Ref. No.	Address	New parts	Parts No.		Description		Destination
R318		<u> </u>	RK73GB1J104J	CHIP R	100K	J	1/16W	1		C26		İ	CK73GB1E223K	CHIP C	0.022UF K		
R319			RK73GB1J820J	CHIP R	82	J	1/16W			C27 -29			CK73GB1H471K	CHIP C	470PF K		
R320,321			RK73GB1J820J	CHIP R	82	J	1/16W	ς K	(4	C30			CK73GB1H102K	CHIPC	1000PF K		
R324		1	RK73GB1J102J	CHIP R	1.0K	J	1/16W			C31			CK73GB1C104K	CHIPC	0.10UF K		
R325			RK73GB1J102J	CHIP R	1.0K	J	1/16W	K	(4	C32			CK73FB1A105K	CHIP C	1.0UF K		
R326			RK73GB1J124J	CHIP R	120K	J	1/16W			C33 ,34			CK73GB1H471K	CHIP C	470PF K		
R327		ļ.	RK73GB1J563J	CHIP R	56K	J	1/16W		- 1	C35			CK73GB1E103K	CHIPC	0.010UF K		
R328			RK73GB1J124J	CHIP R	120K	J	1/16W			C36			CK73GB1C104K	CHIP C	0.10UF K		
R331			RK73GB1J103J	CHIP R	10K	J	1/16W			C37			CK73GB1H471K	CHIPC	470PF K		
R332			RK73GB1J272J	CHIP R	2.7K	J	1/16W		l	C38			CK73GB1H102K	CHIP C	1000PF K		
R333			RK73GB1J103J	CHIP R	10K	J	1/16W			C39 ,40			CK73GB1C104K	CHIP C	0.10UF K		
R336			R92-1252-05	CHIP R	0 OHM	•	.,			C41		1	C92-0713-05	TAN C	10UF 6.3W\	1	
R337		1	RK73GB1J472J	CHIP R	4.7K	J	1/16W			C42		1	CK73GB1H102K	CHIP C	1000PF K		
R338-341			RK73GB1J101J	CHIP R	100	J	1/16W			C43			CK73GB1C333K	CHIP C	0.033UF K		
S301-303			S70-0457-05	TACT SWI			,			C44			CK73GB1H471K	CHIP C	470PF K		
D300			NNCD6.8G	ZENER DIC	nne				December	C45			CC73GCH1H100D	CHIP C	10PF D		
D301			B30-2019-05	LED(RE/GF					I	C46			CC73GCH1H121J	CHIP C	120PF J		
D302			1SS373	DIODE	"				I	C47			CK73GB1C104K	CHIPC	0.10UF K		
D302			015AZ2.4-X	ZENER DIC	IDE				ı	C48			CK73GB1H471K	CHIPC	470PF K		
D303 D304			MA2S111	DIODE	,,,,					C49			CK73GB1C104K	CHIP C	0.10UF K		
					D (0)					CCO			007000114114000	CHIEC	1005 0		
D305,306			B30-2171-05	LED(1608/					,	C50		-	CC73GCH1H100D	CHIPC	10PF D		
D307-310			B30-2171-05	LED(1608/	U/8)			K.	1	C51 C52			CK73GB1E103K	CHIP C	0.010UF K		
D315			IMN10	DIODE				K		C52 C53			CC73GCH1H271J CK73GB1H102K	CHIP C	270PF J 1000PF K		
D316 D317			MA2S111 MA2S111	DIODE				K	.4	C53			CK73GB1E103K	CHIP C CHIP C	0.010UF K		
D318			IMN10	DIODE						C55			CK73GB1C104K	CHIP C	0.10UF K	,	
D319-321	1		015AZ6.8	ZENER DIC						C56			C92-0662-05	TAN C	15UF 6.3WV		
IC300			TDA7053AT	IC(AUDIO						C57			CK73GB1H472K	CHIP C	4700PF K		
IC301 Q300			MC74HC4017F 2SJ243	IC(GATE CI FET	NIO2)					C58 C59			CK73GB1H471K CK73GB1H222K	CHIP C CHIP C	470PF K 2200PF K		
4000			200240	11-1			2								220017 11		
Q301			UPA672T	FET					- 1	C60			CK73GB1C273K	CHIP C	0.027UF K		
Q302-304			2SC4617(S)	TRANSIST	OR				- 1	C61			CK73GB1C104K	CHIP C	0.10UF K		
Q305			2SB798(DL,DK)	TRANSIST						C62			CK73GB1E123K	CHIP C	0.012UF K		
Q306			2SC4617(S)	TRANSIST				1		C63			CK73GB1H122K	CHIP C	1200PF K		
Q307			2SB1132(Q,R)	TRANSIST	OR					C64			CK73GB1H102K	CHIP C	1000PF K		
Q308			UPA672T	FET						C65 ,66			CC73GCH1H680J	CHIP C	68PF J		
Q309			2SC4617(S)	TRANSIST	OR					C67			CK73GB1C104K	CHIP C	0.10UF K		
Q310			2SK1824	FET						C68 ,69			CK73GB1E103K	CHIP C	0.010UF K		
TH300	<u> </u>		TN10-3S154JT	THERMIST	OR		······			C70 C71			CK73GB1C104K CC73GCH1H220J	CHIP C CHIP C	0.10UF K 22PF J		
			TX-RX UNIT		750-1	0)							CC/30CH1H2203	CHILL	22PF J		
C1			CK73GB1H471K		470PF					C72			CK73GB1C683K	CHIP C	0.068UF K		
C2 ,3			CK73GB1E103K	CHIP C	0.010UF					C73			CC73GCH1H100D	CHIP C	10PF D		
C4 -6			CK73GB1H471K	CHIP C	470PF					C74			CK73GB1C104K	CHIPC	0.10UF K		
C7 C8			CK73GB1C104K CK73GB1H103K	CHIP C CHIP C	0.10UF 0.010UF					C75 C76			CK73GB1E103K CK73GB1C104K	CHIP C CHIP C	0.010UF K 0.10UF K		
υd			ACALLI ODE AND	OTHI C	0.01001	K				, ,			5.770007010410	Jim U	0.1001 K		
C9			CK73GB1C104K	CHIP C	0.10UF	K				C77			C92-0662-05	TAN C	15UF 6.3WV	•	
C10			CK73GB1E103K	CHIP C	0.010UF			-		C78			CK73GB1H562J	CHIPC	5600PF J		
C11	1		CK73GB1H471K	CHIP C	470PF				1	C79			C92-0713-05	TAN C	10UF 6.3WV	•	
C12		700	CK73GB1E103K	CHIP C	0.010UF					C81			CK73GB1H471K	CHIP C	470PF K		
C13			CC73GCH1H101J	CHIP C	100PF	J				C82			CK73GB1C333K	CHIP C	0.033UF K		
C14	1		C92-0628-05	CHIP-TAN	10UF	10W	V			C84			CK73GB1H562J	CHIP C	5600PF J		
C15			C92-0592-05	CHIP-TAN		6.37				C85			CK73GB1H471K	CHIP C	470PF K		
C16			CK73GB1H471K	CHIP C	470PF					C86			CK73GB1H562J	CHIPC	5600PF J		
C17			C92-0628-05	CHIP-TAN		10W	V			C87			CK73GB1C333K	CHIPC	0.033UF K		
C18			CK73FB1C334K	CHIP C	0.33UF					C88			CK73GB1C104K	CHIP C	0.10UF K		
C19			C92-0628-05	CHIP-TAN	101 IE	10W	۸/		-	C89			CC73GCH1H820J	CHIP C	82PF J		
C20			CK73GB1C104K	CHIP-TAIN CHIP C	0.10UF		v		Consessed	C91			CK73GB1H471K	CHIPC	470PF K		
C21			C92-0592-05	CHIP-TAN			i/V			C92			C92-0662-05	TAN C	15UF 6.3WV		
C22			CK73GB1H331K	CHIP C	330PF		v v			C93			CK73GB1H272J	CHIPC	2700PF J		
C23			C92-0592-05	CHIP-TAN		6.3V	٧V			C95			CK73GB1H471K	CHIPC	470PF K		
										C96			CC73GCH1H330J	CHIPC	33PF J		
C24 ,25			CK73GB1H471K	CHIP C	470PF	K				C07			CC79CC11411000D	CHIRC	2.000		
										C97		1	CC73GCH1H030B	CHIPC	3.0PF B		

TX-RX UNIT (X57-5750-10)

Ref. No.	Address	New parts	Parts No.		Description	Destination	Ref. No.	Addres	S New parts	Parts No.		Description	Destination
C98 ,99			CK73GB1H272J	CHIP C	2700PF J		C176-180			CK73GB1H471K	CHIPC	470PF K	,
C100-102			CK73GB1C104K	CHIP C	0.10UF K		C181			CK73GB1E103K	CHIPC	0.010UF K	
C103			CC73GCH1H151J	CHIP C	150PF J	9	C182			CC73GCH1H060B	CHIPC	6.0PF B	
C104			CK73GB1H152J	CHIP C CHIP C	1500PF J 1000PF K	awara a	C183 C184			CC73GCH1H270J CC73GCH1H680J	CHIP C CHIP C	27PF J 68PF J	
C105			CK73GB1H102K	CMIFC	TUUUFF K	The party of the p	G104			00/30011110003	GIN C		
C106			CK73GB1E103K	CHIP C	0.010UF K		C185			CK73GB1H471K	CHIP C	470PF K	
2107			CC73GCH1H030B	CHIP C	3.0PF B		C186			CC73GCH1H270J	CHIP C	27PF J	
C108			C92-0635-05	TAN C	10UF 6.3WV		C188,189			CK73GB1H471K	CHIP C	470PF K	
C109 C110			C92-0714-05 CK73GB1C104K	TAN C CHIP C	4.7UF 6.3WV 0.10UF K		C190 C191,192			CK73HB1C103K CK73GB1H471K	CHIP C CHIP C	0.010UF K 470PF K	1
5110													
0111			CK73GB1H471K	CHIP C	470PF K		C193		*	CC73GCH1H100D	CHIPC	10PF D 4.5PF B	
C112			C92-0713-05	TAN C	10UF 6.3WV		C194 C195			CC73GCH1H4R5B CK73GB1H471K	CHIP C CHIP C	4.5PF B 470PF K	
C113,114			CK73GB1C104K	CHIP C	0.10UF K		C195			CC73GCH1H390J	CHIP C	39PF J	
C115 C117,118			CK73GB1H471K CK73GB1C104K	CHIP C CHIP C	470PF K 0.10UF K		C190			CK73FB1C474K	CHIP C	0.47UF K	
C119			CC73GCH1H181J	CHIP C	180PF J	and the same of th	C198 C199			CC73GCH1H070B CC73GCH1H101J	CHIP C	7.0PF B 100PF J	
C120			CK73GB1C473K	CHIP C	0.047UF K 0.10UF K		C200			C92-0565-05	CHIP-TAN	6.8UF 10WV	
C121,122			CK73GB1C104K CK73GB1E103K	CHIP C CHIP C	0.010UF K		C200			CC73GCH1H2R5B	CHIP C	2.5PF B	
C123 C124,125			CK73GB1C104K	CHIP C	0.10UF K		C201			CK73GB1H471K	CHIP C	470PF K	
U124,123			CK70dB10104K			The state of the s							
C126			C92-0519-05				C203			CC73GCH1H050B	CHIPC	5.0PF B 0.5PF B	
C127			C92-0592-05	CHIP-TAN CHIP C	4.7UF 6.3WV 0.10UF K		C204 C205		1	CC73GCH1H0R5B CC73GCH1H101J	CHIP C CHIP C	0.5PF B 100PF J	
C128 C129			CK73GB1C104K CK73GB1H562J	CHIPC	5600PF J		C205			CC73GCH1H030B	CHIPC	3.0PF B	
C129 C130			CK73GB1H102K	CHIP C	1000PF K		C207			CK73GB1E103K	CHIP C	0.010UF K	
											0.000	470P5 14	
C131			CK73GB1H562J	CHIP C CHIP C	5600PF J 5.0PF B		C209 C210			CK73GB1H471K CC73GCH1H020B	CHIP C CHIP C	470PF K 2.0PF B	
C133 C134			CC73GCH1H050B CK73GB1E153K	CHIP C	0.015UF K		C210		-	CK73FB1C474K	CHIP C	0.47UF K	
C134 C135			CK73GB1E103K	CHIP C	0.010UF K		C213			CC73GCH1H101J	CHIP C	100PF J	
C136			CK73GB1E473J	CHIP C	0.047UF J		C214			CC73GCH1H020B	CHIP C	2.0PF B	
C137			CK73GB1E103K	CHIP C	0.010UF K	e de la constitución de la const	C215			CC73GCH1H060B	CHIP C	6.0PF B	
C137			CC73GCH1H030B	CHIP C	3.0PF B		C216		*	CC73GCH1H4R5B	CHIP C	4.5PF B	
C139			CK73GB1H471K	CHIP C	470PF K		C217			CC73GCH1H040B	CHIP C	4.0PF B	
C140			CC73GCH1H050B	CHIP C	5.0PF B		C218			CC73GCH1H120J	CHIP C	12PF J	
C141			C92-0592-05	CHIP-TAN	4.7UF 6.3WV		C219		*	CC73GCH1H4R5B	CHIP C	4.5PF B	
C142			CK73GB1H471K	CHIP C	470PF K		C220	Salar WA		CC73GCH1H050B	CHIP C	5.0PF B	
C143			CC73GCH1H100D	CHIP C	10PF D		C221			CK73GB1H471K	CHIP C	470PF K	
C144			C92-0714-05	TAN C	4.7UF 6.3WV		C222,223			CC73GCH1H030B	CHIP C	3.0PF B	
C145	ŀ	i	CK73FB1H563K	CHIP C	0.056UF K		C225	İ		CC73GCH1H101J	CHIPC	100PF J	
C146,147			CK73GB1H102K	CHIP C	1000PF K		C226			CK73GB1H471K	CHIP C	470PF K	
C149			CK73GB1H471K	CHIP C	470PF K		C230			CC73GCH1H010B	1	1.0PF B	
C150			CC73GCH1H050B	CHIP C	5.0PF B		C231			CK73GB1H471K	CHIP C	470PF K	
C152			CC73GCH1H330J	CHIP C	33PF J		C232			CC73GCH1H470J	CHIP C	47PF J	
C154 C155			CK73GB1C104K CK73GB1H471K	CHIP C CHIP C	0.10UF K 470PF K		C233 C234			CK73GB1H471K CC73GCH1H100D	CHIP C CHIP C	470PF K 10PF D	
C157			CK73GB1H471K	CHIP C	470PF K		C236			CC73GCH1H470J	CHIP C	47PF J	
C159			CK73HB1C103K	CHIP C	0.010UF K		C237			CC73GCH1H010B CK73GB1E103K	CHIP C	1.0PF B	
C160			CK73GB1C104K	CHIP C	0.10UF K 4.0PF B		C238 C239			CK73GB1E103K CK73GB1H471K	CHIP C CHIP C	0.010UF K 470PF K	
C161 C162			CC73GCH1H040B CK73GB1H471K	CHIP C	4.0FF B 470PF K		C239			CC73GCH1H130J	CHIPC	13PF J	
											CHIEC	470DE V	
C163			CK73GB1C104K	CHIP C CHIP C	0.10UF K 0.010UF K		C242,243 C245			CK73GB1H471K CC73GCH1H3R5B	CHIP C CHIP C	470PF K 3.5PF B	
C165 C166			CK73GB1E103K CK73HB1C103K	CHIP C	0.010UF K		C245 C247			CK73GB1H471K	CHIP C	470PF K	
C166 C167			CC73GCH1H040B	CHIP C	4.0PF B		C247			CC73GCH1H1R5B	CHIPC	1.5PF B	
C168			CC73GCH1H120J	CHIP C	12PF J		C249-253			CK73GB1H471K	CHIP C	470PF K	
C169			CK73GB1E103K	CHIP C	0.010UF K		C254			CC73GCH1H050B	CHIP C	5.0PF B	
C170			CK73HB1C103K	CHIP C	0.010UF K		C255			CC73GCH1H020B	CHIPC	2.0PF B	
C170			CC73GCH1H070B	CHIP C	7.0PF B		C256			CC73GCH1H050B	CHIPC	5.0PF B	
C172			CC73GCH1H050B	CHIP C	5.0PF B		C257,258		1	CK73GB1H471K	CHIPC	470PF K	
C173			CK73GB1H471K	CHIP C	470PF K		C259			CC73GCH1H101J	CHIP C	100PF J	
C174			CK73GB1C104K	CHIP C	0.10UF K		C261,262			CK73HB1C103K	CHIP C	0.010UF K	
	1	1	CK73GB1H472K	CHIP C	4700PF K	1	C263	1		CK73GB1H103K	CHIPC	0.010UF K	

Dos Na	Address	New	Daria Na	Pagaintin	Doc4!*	D-4 **	A .1 -1	New	D			•	1
Ref. No.	Address	parts		Description	Destination	Ref. No.	Address	parts			Descript		Destination
C264			CC73GCH1H150J	CHIP C 15PF J		CP4 ,5			R90-0743-05		RESISTOR 4		
C265 C269			CK73GB1H103K CC73GCH1H030B	CHIP C 0.010UF K CHIP C 3.0PF B		CP6 -21 CP22-24			R90-0741-05	MULTIPLE	RESISTOR 1	K X2	
C276			CC73GCH1H2R5B	CHIP C 2.5PF B		R1			R90-0743-05 RK73GB1J103J	CHIP R	RESISTOR 4		
CN1			E40-5823-05	FLAT CABLE CONNECTOR		R2			RK73GB1J473J	CHIPR	10K J 47K J		
CN2			E40-9517-05	PIN ASSY SOCKET (4P)		R3			RK73GB1J154J	CHIP R	150K J	1/16W	
CN3			E40-5890-05	FLAT CABLE CONNECTOR(24P)		R4			RK73GB1J104J	CHIP R	100K J		
CN4	-		E23-0342-05	TEST TERMINAL (2P)		R5			RK73GB1J103J	CHIP R	10K J		
CN5 -12 F1			E23-1081-05 F53-0130-05	TERMINAL FUSE (3A)		R6 ,7 R8			RK73GB1J104J RK73GB1J472J	CHIP R	100K J 4.7K J		
				,								·	
CD1 CF1		*	L79-1072-05 L72-0962-05	TUNING COIL CERAMIC FILTER		R9 R10	İ		RK73GB1J474J RK73GB1J472J	CHIP R CHIP R	470K J		
CF2		*	L72-0963-05	CERAMIC FILTER		R11			RK73GB1J104J	CHIP R	4.7K J 100K J		
L1			L92-0149-05	FERRITE CHIP		R12			RK73GB1J184J	CHIP R	180K J		
L2			L92-0138-05	FERRITE CHIP	and the state of t	R13			RK73GB1J104J	CHIP R	100K J		
L3			L40-4791-37	SMALL FIXED INDUCTOR(4.700UH)		R14			R92-1252-05	CHIP R	0 OHM		
L4			L40-1091-37	SMALL FIXED INDUCTOR(1.000UH)		R15			RK73GB1J104J	CHIP R	100K J	1/16W	
L5 ,6			L92-0138-05	FERRITE CHIP		R16 ,17			RK73GB1J473J	CHIP R	47K J	1/16W	
L9 L10			L40-1271-36 L40-3971-36	SMALL FIXED INDUCTOR(12NH/8)		R18			RK73GB1J154J	CHIP R	150K J		
-10			140-39/1-36	SMALL FIXED INDUCTOR(39NH/8)	THE REAL PROPERTY AND ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY ADDRESS OF THE PERTY	R19 ,20			RK73GB1J104J	CHIP R	100K J	1/16W	
L11 ,12 L13			L92-0138-05 L40-2271-36	FERRITE CHIP SMALL FIXED INDUCTOR(22NH/8)	ALT COMPANY	R21 R22 ,23			RK73GB1J273J RK73GB1J823J	CHIP R	27K J		
L14			L40-1081-36	SMALL FIXED INDUCTOR(100NH)	1	R24 ,23			RK73GB1J8Z3J	CHIP R CHIP R	82K J		
L15			L40-1571-36	SMALL FIXED INDUCTOR(15NH/8)	1	R25			RK73GB1J473J	CHIP R	47K J 4.7K J		
L17			L40-1571-36	SMALL FIXED INDUCTOR(15NH/8)		R26			RK73GB1J473J	CHIP R	47K J		
_18			L39-1272-05	TOROIDAL COIL		R27			RK73GB1J332J	CHIP R	3.3K J	1/16W	
L19			L92-0138-05	FERRITE CHIP		R28			RK73GB1J474J	CHIP R	470K J	•	
_20			L40-1571-36	SMALL FIXED INDUCTOR(15NH/8)		R29			RK73GB1J184J	CHIP R	180K J		
.21 ,22			L39-1272-05	TOROIDAL COIL		R30			RK73GB1J334J	CHIP R	330K J	1/16W	
L24			L92-0138-05	FERRITE CHIP		R31			RK73GB1J102J	CHIP R	1.0K J	1/16W	
L25			L40-2771-36	SMALL FIXED INDUCTOR(27NH/8)		R32			RK73GB1J104J	CHIP R	100K J	1/16W	
L26 L27		*	L92-0138-05	FERRITE CHIP		R33			RK73GB1J184J	CHIP R	180K J		
L27 L28		-	L40-3378-67 L92-0138-05	SMALL FIXED INDUCTOR(33NH/8) FERRITE CHIP		R34			RK73GB1J683J	CHIP R	68K J		
L29		*	L40-1578-67	SMALL FIXED INDUCTOR(15NH/8)		R35 R36			RK73GB1J220J RK73GB1J154J	CHIP R CHIP R	22 J 150K J	1/16W 1/16W	
L30			L92-0149-05	FERRITE CHIP		R37			R92-1252-05	Cunn	0.00184		
L31		*	L40-1878-67	SMALL FIXED INDUCTOR(18NH/8)		R38			RK73GB1J101J	CHIP R CHIP R	0 OHM 100 J	1/16W	
.32		*	L34-4564-05	AIR-CORE COIL (4T)		R39			RK73GB1J472J	CHIP R	4.7K J	1/16W	
.33		*	L40-2278-67	SMALL FIXED INDUCTOR(22NH/8)		R40 ,41			RK73GB1J334J	CHIP R	330K J	1/16W	
.34		4	L40-3378-67	SMALL FIXED INDUCTOR(33NH/8)		R42 ,43			RK73GB1J223J	CHIP R	22K J		
.35			L40-1085-34	SMALL FIXED INDUCTOR(100NH/8)	The state of the s	R44			RK73GB1J473J	CHIP R	47K J	1/16W	
.36		*	L40-3378-67	SMALL FIXED INDUCTOR(33NH/8)		R45			RK73GB1J472J	CHIP R	4.7K J	1/16W	
.37 .38		*	L34-4564-05 L40-1878-67	AIR-CORE COIL (4T) SMALL FIXED INDUCTOR(18NH/8)		R46 ,47			RK73GB1J223J	CHIP R	22K J	1/16W	
.39			L40-1871-36	SMALL FIXED INDUCTOR(18NH/8)	500	R48 R49			R92-1252-05 RK73GB1J223J	CHIP R CHIP R	0 OHM 22K J	1/16W	
.40		#	L34-4564-05	AIR-CORE COIL (4T)	15700000000	R50		*	RN73GH1J913D	CHIP R	91K D		
41			L40-1071-36	SMALL FIXED INDUCTOR(10NH/8)		R51			RN73GH1J683D	CHIPR	68K D		
.42 ,43		*	L34-4564-05	AIR-CORE COIL (4T)		R52			RN73GH1J913D	CHIP R	91K D		
44		*	L40-2278-67	SMALL FIXED INDUCTOR(22NH/8)		R53			RK73GB1J473J	CHIP R	47K J	1/16W	
45			L40-1092-81	SMALL FIXED INDUCTOR	MCGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGG	R54			RK73GB1J123J	CHIP R	12K J	1/16W	
46		*	L40-6878-67	SMALL FIXED INDUCTOR(68NH/8)	on of the second	R55			RN73GH1J333D	CHIP R	33K D	1/16W	
.47		*	L40-3378-67	SMALL FIXED INDUCTOR(33NH/8)		R56			RK73GB1J472J	CHIP R	4.7K J	1/16W	
48 -52 53			L34-4564-05 L92-0138-05	AIR-CORE COIL (4T) FERRITE CHIP		R57			RK73GB1J183J	CHIP R	18K J	1/16W	-
.53 .54 -56			L92-0138-05 L40-2771-36	SMALL FIXED INDUCTOR(27NH/8)		R58 R59			RK73GB1J184J RK73GB1J564J	CHIP R CHIP R	180K J 560K J	1/16W 1/16W	
(1		*	L77-1789-05	TCXO (16.8MHZ/4P/16)		DEC							
(2		-	L77-1760-05	CRYSTAL RESONATOR(44.395MHZ)		R60 R61			RK73GB1J123J RK73GB1J103J	CHIP R CHIP R	12K J 10K J	1/16W 1/16W	
3		- 1	L77-1708-05	CRYSTAL RESONATOR(3.579545MHZ)	The state of the s	R62			RN73GH1J913D	CHIP R	91K D	1/16W	
(4			L78-0462-05	RESONATOR (9.8304M/8*2.5)		R63		- [RK73GB1J474J	CHIP-R	470K J	1/16W	
F1		*	L71-0530-05	MCF (44.85MHZ/6P/1)		R64		í	RK73GB1J184J	CHIP R	180K J	1/16W	
;P1			R90-0724-05	MULTI-COMP 1K X4		R65			RK73GB1J103J	CHIP R	10K J	1/16W	
P2		- 1	R90-0718-05	MULTI-COMP 4.7K X4		R66			R92-1252-05	CHIP R	0 OHM		İ

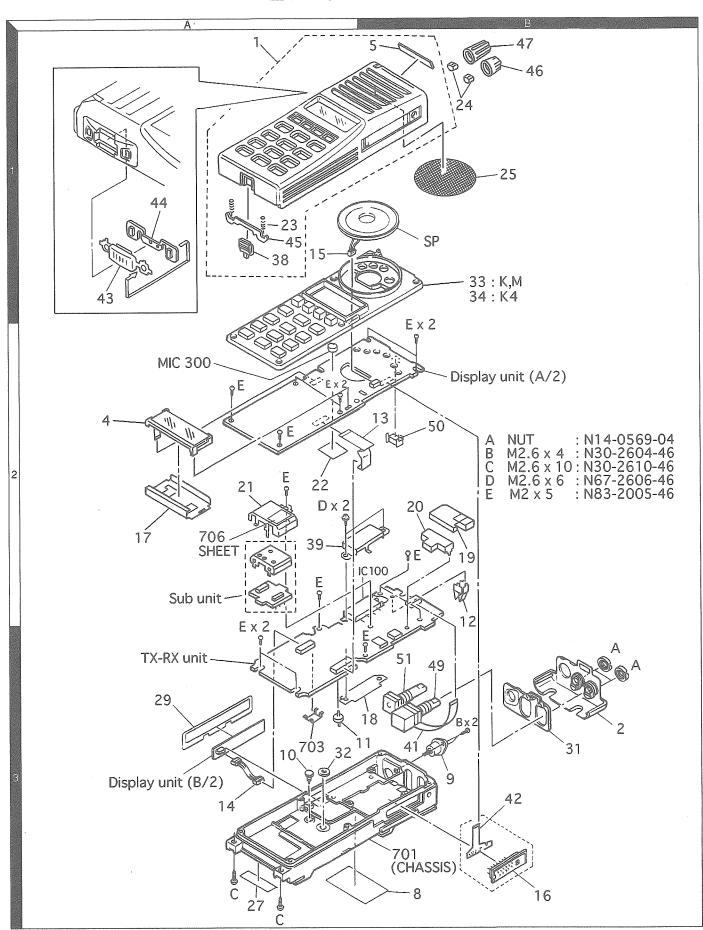
TX-RX UNIT (X57-5750-10)

Ref. No.	Address	New parts	Parts No.		Description	1	Destination	Ref. No.	Address	New parts	Parts No.		Destination		
367			RN73GH1J274D	CHIP R	270K D	1/16W		R153			R92-1252-05	CHIP R	0 OHM		
368			RK73GB1J223J	CHIP R	22K J	1/16W		R154			RK73GB1J183J	CHIP R	18K J	1/16W	
369			R92-1252-05	CHIP R	0 OHM			R156			RK73GB1J102J	CHIP R	1.0K J	1/16W	
170			RN73GH1J682D	CHIP R	6.8K D	1/16W		R160			RK73GB1J682J	CHIP R	6.8K J	1/16W	
170 171			RK73GB1J183J	CHIP R	18K J	1/16W		R162	'		RK73GB1J103J	CHIP R	10K J	1/16W	
17 1			111(700)101000	01111	1011	.,									
172			RK73GB1J155J	CHIP R	1.5M J	1/16W		R163			RK73HB1J103J	CHIPR	10K J 22K J	1/16W	
374	ĺ		RK73GB1J183J	CHIP R	18K J	1/16W		R166		1	RK73GB1J223J	CHIP R		1/16W	
75			RN73GH1J683D	CHIP R	68K D	1/16W		R167			R92-1252-05	CHIP R	0 OHM	4 (4 (0) \$4	
176	1		RK73GB1J474J	CHIPR	470K J	1/16W		R168			RK73GB1J680J	CHIP R	68 J	1/16W	
₹78			RN73GH1J682D	CHIP R	6.8K D	1/16W	200	R177			RK73GB1J101J	CHIPR	100 J	1/16W	
R79			 RK73GB1J101J	CHIP R	100 J	1/16W		R180			RK73GB1J473J	CHIP R	47K J	1/16W	
173			RK73GB1J152J	CHIP R	1.5K J	1/16W		R184			RK73GB1J102J	CHIP R	1.0K J	1/16W	
180 181			RK73GB1J1220J	CHIP R	22 J	1/16W		R185			RK73GB1J220J	CHIP R	22 J	1/16W	
R83			RK73GB1J184J	CHIP R	180K J	1/16W		R189			RK73HB1J473J	CHIP R	47K J	1/16W	
185 185			RK73GB1J103J	CHIP R	10K J	1/16W		R190			RK73GB1J472J	CHIP R	4.7K J	1/16W	
												0.455	0.01/	4 (4 (3) 1)	
R86			RK73GB1J223J	CHIP R	22K J	1/16W		R195			RK73GB1J222J RK73HB1J102J	CHIP R CHIP R	2.2K J 1.0K J	1/16W 1/16W	
189			RK73GB1J102J	CHIP R	1.0K J	1/16W	1	R199				1			
R90	1	1	RK73GB1J153J	CHIP R	15K J	1/16W		R202			RK73GB1J271J	CHIP R	270 J	1/16W	
R91			RK73GB1J473J	CHIP R	47K J	1/16W		R210			RK73GB1J561J	CHIP R	560 J	1/16W	1
R93			RK73GB1J183J	CHIP R	18K J	1/16W		R216			RK73GB1J152J	CHIP R	1.5K J	1/16W	
R94			RK73GB1J153J	CHIP R	15K J	1/16W		R218			RK73HB1J473J	CHIP R	47K J	1/16W	
n94 R95		1	RK73GB1J1394J	CHIP R	390K J	1/16W		R219			RK73GB1J180J	CHIP R	18 J	1/16W	1
			RK73GB1J394J	CHIP R	2.2K J	1/16W		R221			RK73HB1J102J	CHIPR	1.0K J	1/16W	
R96			i .		2.2K J 150 J	1/16W		R241			RK73GB1J331J	CHIPR	330 J	1/16W	1
R97 R100			RK73GB1J151J R92-1252-05	CHIP R	0 OHM	1/1044		R241 R242			R92-1252-05	CHIP R	0 OHM	17 10 44	
1100															
R101			RK73GB1J560J	CHIP R	56 J	1/16W		R244			R92-1252-05	CHIP R	0 OHM	1 /4\A/	
3102			RK73GB1J333J	CHIP R	33K J	1/16W		R245		İ	RK73EB2ER39K	CHIP R	0.39 K	1/4W	
R104			RK73GB1J102J	CHIP R	1.0K J	1/16W		R247			RK73EB2ER39K	CHIP R	0.39 K	1/4W	ĺ
R106			RK73GB1J470J	CHIP R	47 J	1/16W	1	R248			R92-1252-05	CHIP R	0 OHM		
R107			RK73GB1J473J	CHIP R	47K J	1/16W		R249			RK73EB2ER39K	CHIPR	0.39 K	1/4W	
R109			R92-1252-05	CHIP R	0 OHM			R250			R92-1252-05	CHIP R	0 OHM		
			RK73GB1J220J	CHIP R	22 J	1/16W		R251-253			RK73GB1J154J	CHIP R	150K J	1/16W	
R110			1	CHIPR	0 OHM	1/1000		R254			RK73GB1J271J	CHIP R	270 J	1/16W	
R112		Ì	R92-1252-05	CHIP R				R255-257			RK73GB1J154J	CHIP R	150K J	1/16W	
R114 R115			R92-1252-05 RK73GB1J184J	CHIP R	0 OHM 180K J	1/16W		R258			RK73GB1J1343	CHIP R	270 J	1/16W	
11113			11117308101010	01111 11	70011	1, 1011									
R116			RK73GB1J103J	CHIP R	10K J	1/16W		R259,260			R92-1252-05	CHIP R	0 OHM		
R117	İ	1	RK73GB1J184J	CHIP R	180K J	1/16W		R261			RK73GB1J103J	CHIP R	10K J	1/16W	
R118			RK73GB1J221J	CHIP R	220 J	1/16W		R262			RK73GB1J470J	CHIP R	47 J	1/16W	
R119			RK73GB1J102J	CHIP R	1.0K J	1/16W		R263,264			RK73GB1J104J	CHIP R	100K J	1/16W	
R120			RK73GB1J104J	CHIP R	100K J	1/16W		R265			RK73GB1J473J	CHIP R	47K J	1/16W	
Dana			DV300D4 1000 I	CUID	ו ער ו	1 /1 6\//		R266			R92-1252-05	CHIP R	0 OHM		
R121			RK73GB1J222J	CHIP R	2.2K J	1/16W 1/16W		R267			RK73GB1J181J	CHIP R	180 J	1/16W	
R122			RK73GB1J221J	CHIP R	220 J	1/1044		R268			RK73GB1J105J	CHIP R	1.0M J	1/16W	
R124			R92-1252-05	CHIP R	0 OHM	1/16\4/		R269			RK73GB1J105J	CHIP R	22K J	1/16W	
R125 R126			RK73GB1J124J RK73GB1J470J	CHIP R	120K J 47 J	1/16W 1/16W		R270			R92-1252-05	CHIP R	0 OHM	17,044	
,,,,,,,															
R127			RK73GB1J103J	CHIP R	10K J	1/16W		R271			RK73GB1J222J	CHIP R	2.2K J	1/16W	
R128			R92-1252-05	CHIP R	0 OHM			R272			R92-1252-05	CHIPR	0 OHM		
R129			RK73HB1J104J	CHIP R	100K J	1/16W		R273			RK73GB1J223J	CHIP R	22K J	1/16W	
R130			R92-1252-05	CHIP R	0 OHM		'	R276			R92-1252-05	CHIPR	0 OHM		
R131			RK73GB1J470J	CHIP R	47 J	1/16W		R277			RK73GB1J223J	CHIPR	22K J	1/16W	
R132			RK73GB1J684J	CHIP R	680K J	1/16W		R279,280			R92-1252-05	CHIP R	0 OHM		
R133,134			R92-1368-05	CHIP R	0 OHM	.,		R282	1		RK73GB1J223J	CHIP R	22K J	1/16W	
R135,134		1	RK73GB1J272J	CHIP R	2.7K J	1/16W		R283			RK73GB1J472J	CHIP R	4.7K J	1/16W	
			RK73GB1J272J	CHIP R	1.2K J	1/16W		R284			RK73GB1J221J	CHIP R	220 J		
R136 R137			RK73GB1J122J	CHIP R	1.2K J	1/16W		R285			R92-1252-05	CHIP R	0 OHM	.,	
											DIVERGE COST	OLUB B	1.01/	4 (4.0) \$1	
R138			RK73HB1J103J	CHIP R	10K J	1/16W		R296			RK73GB1J102J	CHIPR	1.0K J		
R139		1	R92-1252-05	CHIP R	0 OHM			R297,298	1		RK73HB1J473J	CHIP R	47K J		
R140,141			RK73HB1J473J	CHIP R	47K J	1/16W		R299			RK73GB1J101J	CHIP R	100 J	1/16W	
R142			RK73HB1J102J	CHIP R	1.0K J	1/16W		D1			1SR154-400	DIODE			
R143			RK73HB1J474J	CHIP R	470K J	1/16W		D2 ,3			MA2S111	DIODE			
R144			R92-1252-05	CHIP R	0 OHM		1	D4	-		RB706F-40	DIODE			
	j	1	RK73HB1J102J	CHIP R	1.0K J	1/16W	1	D5	1	- 1	DAN222	DIODE			1

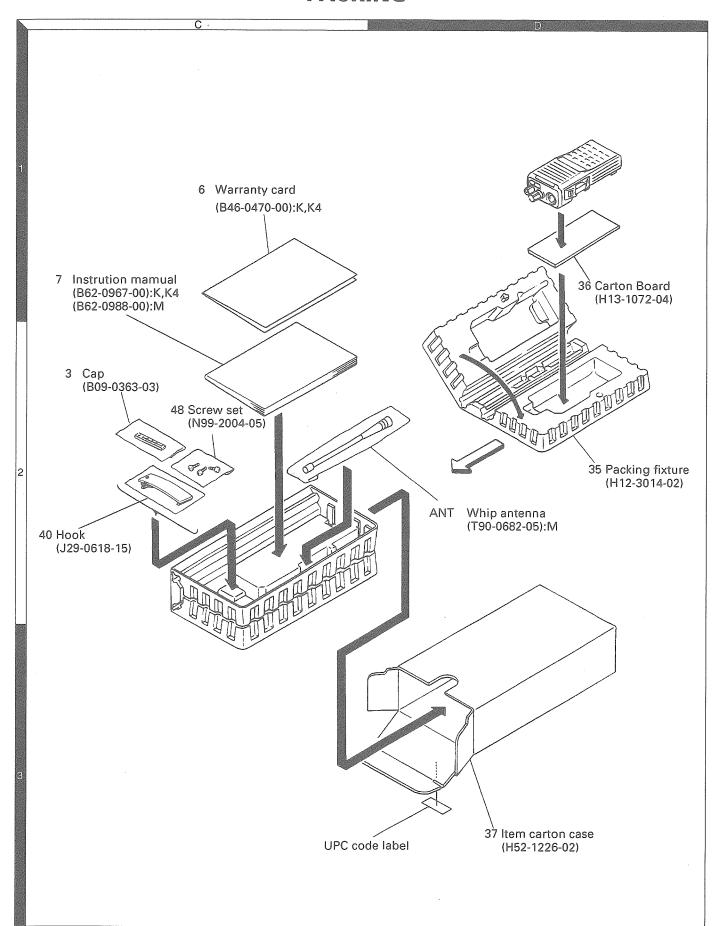
TX-RX UNIT (X57-5750-10) SUB UNIT (X58-4590-10)

Ref. No.	Address	New parts	Parts No.	Description	Destination	Ref. No.	Address	New parts	Parts No.	Description	Destination
D6 D7			MA2S111	DIODE				A1:	SUB UNIT (VCO) X58-4590-10	
7 8			DAN222 RB706F-40	DIODE		The A	1 is re	pla	ceable as a u	nit assembly so individu	al parts
) }			MA2S077	DIODE		are no					
0		*	HZU5ALL	DIODE		C50	I	· · · ·	CC73HCH1H220J	CHIP C 22PF J	T
Ē						C51		*	CC73HCH1H030B	CHIP C 3.0PF B	
12	1		HVU131	DIODE		C53			CC73HCH1H0R5B	CHIP C 0.5PF B	
14 ,15			MA2S077	DIODE		C54		*	CC73HCH1H030B	CHIP C 3.0PF B	
16			HSM88AS	DIODE		C55		*	CC73HCH1H040B	CHIP C 4.0PF B	
17 ,18		*	DA221	DIODE							
20		*	HVC372B	VARIABLE CAPACITANCE DIODE		C56		*	CC73HCH1H060B	CHIP C 6.0PF B	
22		*	HVC372B	VARIABLE CAPACITANCE DIODE		C57 C58			CK73HB1H102K	CHIP C 1000PF K	
)23			1SS373	DIODE		C59			CC73HCH1H0R5B CK73HB1H471K	CHIP C 0.5PF B CHIP C 470PF K	
24 ,25			MA2S077	DIODE		C60		*	CC73HCH1H060B	CHIP C 6.0PF B	
21			TA75W01FU	IC(OP AMP X2)		1000			0070110111110000	0.011	
C2			TC75W51FU	IC(OP AMP X2)		C61 ,62			CK73HB1H102K	CHIP C 1000PF K	
				1		C63			CK73HB1H471K	CHIP C 470PF K	
3			RN5VL42C	IC(REGULATOR)		C64			CC73HCH1H101J	CHIP C 100PF J	
24			TC75W51FU	IC(OP AMP X2)		C65			CK73HB1H102K	CHIP C 1000PF K	
C5 C6			S-81350HG-KD	IC(VOLTAGE REGULATOR)		C66			CC73HCH1H470J	CHIP C 47PF J	
36 37			NJU7201U50	IC(VOLTAGE REGULATOR)		007.00		*	0070110114120000	OUUD O O ODE 5	
,,			TC75W51FU	IC(OP AMP X2)		C67 ,68 C69		*	CC73HCH1H030B	CHIP C 3.0PF B	1
08			M62364FP	IC(D/A CONVERTER)		C69 C70		*	CC73HCH1H050B CC73HCH1H100B	CHIP C 5.0PF B CHIP C 10PF B	
9			TK11250BM	IC(VOLTAGE REGULATOR)		C71			CC73HCH1H0R5B	CHIP C 0.5PF B	
C10			TA75S01F	IC(OP AMP)		C73		*	CC73HCH1H020B	CHIP C 2.0PF B	
C11			TA75W01FU	IC(OP AMP X2)					207011011110200	2.011	1 .
C12			TA31136FN	IC(FM IF DETECTOR)		C74		*	CC73HCH1H040B	CHIP C 4.0PF B	
						TC50,51			C05-0384-05	CERAMIC TRIMMER CAP(10P/8)	
13			TC35453F	IC(AUDIO PROCESSOR)	1	CN50,51			E40-5622-05	PIN ASSY	
C14			SA7025DK	IC(PLL SYSTEM)		L50 -53			L40-3391-37	SMALL FIXED INDUCTOR(3.300UH)	
216			LC73872M	IC(DTMF RECEIVER)		L56		*	L34-4573-05	AIR-CORE COIL	
C17 C18			AT29C020-90TI GN2011(Q)	IC IC	-	1.07		*	104 4534 05	AUD CODE COU	
,10			וטאצטו ו(ען	10		L57 L58 ,59		•	L34-4574-05	AIR-CORE COIL	
C19		*	30612M4A-407GP	IC(CPU)		L56, 59 L60 ,61			L40-1085-44 L40-3391-37	SMALL FIXED INDUCTOR(100.0NH) SMALL FIXED INDUCTOR(3.300UH)	
C20			AT2408N10SI2.5	IC(8kbit SERIAL EEPROM)		L62			L92-0138-05	FERRITE CHIP	
221,22			BU4094BCFV	IC(8bit SHIFT/STORE REGISTER)		L63			L40-1875-44	SMALL FIXED INDUCTOR(18.0NH)	
C23			NJM2904V	IC(APC)		1.00			2.0 10/0 17	SINIALE TIXED INCOGRATION TO SINIA	
C24			TC7S66FU	IC(ANALOG SWITCH)		R50			RK73HB1J473J	CHIPR 47K J 1/16W	
						R51			RK73HB1J103J	CHIPR 10K J 1/16W	
11			DTC144EE	DIGITAL TRANSISTOR		R52			RK73HB1J473J	CHIPR 47K J 1/16W	
2			2SJ243	FET		R53 ,54			RK73HB1J101J	CHIPR 100 J 1/16W	
3 4			2SA1745(6,7)	TRANSISTOR		R55			RK73HB1J104J	CHIPR 100K J 1/16W	
			2SC4617(S)	TRANSISTOR		DEO			DICTOLIDA LAGAL		
5			2SJ243	FET		R56 R57			RK73HB1J181J	CHIP R 180 J 1/16W	
6			DTA144EE	DIGITAL TRANSISTOR		R58			RK73HB1J151J RK73HB1J224J	CHIP R 150 J 1/16W	
7			DTC144EE	DIGITAL TRANSISTOR		R59			RK73HB1J224J RK73HB1J101J	CHIPR 220K J 1/16W CHIPR 100 J 1/16W	
18			2SC4617(S)	TRANSISTOR		D50 -57			HVC350B	VARIABLE CAPACITANCE DIODE	
19 ,10			DTC144EE	DIGITAL TRANSISTOR						WILLIAM DEC ON NOTATIVOE DIODE	1 .
11			2SK1824	FET		D58		*	HVC351	VARIABLE CAPACITANCE DIODE	
						Q50			2SK508NV(K52)	FET	
12			2SC5108(Y)	TRANSISTOR		Q51			2SJ243	FET	
13 -15			2SK1824	FET		Q52			2SK508NV(K52)	FET	
16 -18			2SC5108(Y)	TRANSISTOR		Q53			UMC4	TRANSISTOR	
.19 20			2SC4619	TRANSISTOR		05.			000540000	TRANSPORTOR	
.U			2SC4988	TRANSISTOR		Q54			2SC5108(Y)	TRANSISTOR	
21			2SK1824	FET							
22			DTC114EE	DIGITAL TRANSISTOR							
23			2SK1824	FET							
24			3SK239A	FET							
25			DTA144EE	DIGITAL TRANSISTOR							
						9					
26			2SK1824	FET							
⊣ 1		ارا	157-302-65801	THERMISTOR							
1		*	X58-4590-10	SUB UNIT				İ			
						į					
	<u>l</u>			<u></u>							1

EXPLODED VIEW



PACKING



ADJUSTMENT

Test Equipment Required for Alignment

	Test Equipment		Major Specifications	
1.	Standard Signal Generator	Frequency Range	450 to 490MHz	
	(SSG)	Modulation	Frequency modulation and external modulation.	
		Output	-127dBm/0.1µV to greater than -47dBm/1mV	
2.	Power Meter	Input Impedance	50Ω.	
		Operation Frequency	450 to 490MHz or more.	
		Measurement Range	Vicinity of 10W	
3.	Deviation Meter	Frequency Range	450 to 490MHz.	
4.	Digital Volt Meter	Measuring Range	10mV to 10V DC	
	(DVM)	Input Impedance	High input impedance for minimum circuit loading.	
5.	Oscilloscope		DC through 30MHz.	
6.	High Sensitivity	Frequency Range	10Hz to 1000MHz.	
	Frequency Counter	Frequency Stability	0.2ppm or less.	
7.	Ammeter		5A.	
8.	AF Volt Meter	Frequency Range	50Hz to 10kHz.	
	(AF VTVM)	Voltage Range	1mV to 10V.	
9.	Audio Generator (AG)	Frequency Range	50Hz to 5kHz or more.	
		Output	0 to 1V.	
10.	Distortion Meter	Capability	3% or less at 1kHz.	
		Input Level	50mV to 10Vrms.	
11.	Spectrum Analyzer	Measuring Range	DC to 1GHz or more	
12.	Tracking Generator	Center frequency	50kHz to 500MHz	
		Output Voltage	100mV or more	
13.	16Ω Dummy Load		Approx. 16Ω, 3W.	
14.	Regulated Power Supply		5V to 10V, approx. 5A	
	·		Useful if ammeter equipped.	

■ The following parts are required for adjustment

1. Antenna connector adapter

The antenna connector of this radio uses an SMA terminal. Use an antenna connector adapter [SMA(f) - BNC(f) or SMA(f) - N(f)] for adjustment. (The adapter is not provided as an option, so buy a commercially-available one.)

Note

When the antenna connector adapter touches the knob, draw out the knob to mount the connector.

2. Universal connector

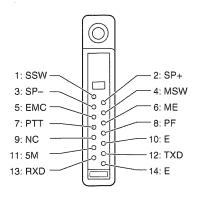
Use the interface cable (KPG-36) for PC tuning or the lead wire with plug (E30-3287-18) and screw (N08-0535-08) for panel tuning. Connct the plug to the universal connector of the radio and tighten the screw.

The lead wire with plug (E30-3287-18) and screw (N08-0535-08) terminals are as follows. Numbers are universal connector terminal numbers.

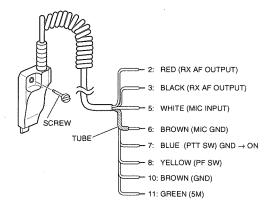
Caution

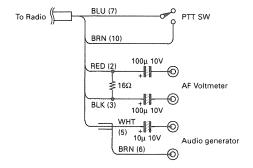
- When connecting the plug to the universal connector of the radio, a short circuit may occur. To provent this, be sure to turn the radio POWER switch off.
- Since the RX AF output is a BTL output, there is a DC component. Isolate this with a capacitor or transformer as shown in the figure.
- 3. Do not connct an instrument between red or black and GND.

Universal connector



· Panel tuning

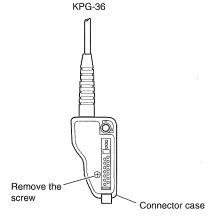


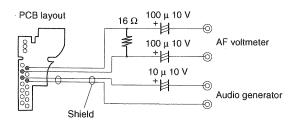


PC tuning

Connect the wires to the PCB in the connector case of interface cable.

For output the wires out of the connector case, need to process the connector case.

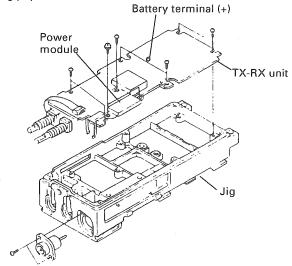




Repair Jig (Chassis)

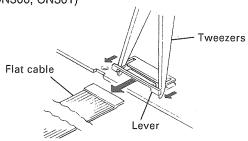
Use jig (part No.: A10-1383-14) for repairing the TK-380. Place the TX-RX unit on the jig and fit it with 7 screws.

The jig facilitates the voltage check and protects the module when the voltage on the flow side of the TX-RX unit is checked during pepairs.

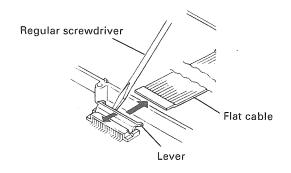


How to Remove the Flat Cable

 Gently draw out both sides of the connector lever uniformly in the direction of the arrow with tweezers. (CN300, CN301)



 Gently rise up the connector lever in the direction of the arrow with a fine regular screwdriver or tweezers. (CN1, CN3, CN304)



Test Mode

■ Test mode operating features

This transceiver has a test mode. To enter test mode, press [A] key and turn power on. Hold [A] key until test channel No. and test signalling No. appears on LCD. Test mode can be inhibited by programming. To exit test mode, switch the power on again. The following functions are available in test mode.

Controls

Controls	"FCN" appears	"FCN" not appears
[PTT]	Used when making a	Used when making
	transmission.	a transmission.
[AUX]	Unused	Unused
[MON]	Monitor ON and OFF.	Monitor ON and OFF.
[LAMP]	Lights the lamp for five	Changes wide and
	seconds.	narrow.
	Lighting is extended for	
	a further five seconds by	
	pressing any key while	
	the lamp is lit.	
[S]	Sets to the Tuning	Sets to the Tuning
	mode.	mode.
[A]	Function OFF	Function ON.
[B]	Compander function	RF power HIGH and
	ON and OFF.	LOW.
[C]	Beat shift ON and OFF	Changes signalling.
[O] to [9],	Used as the DTMF	Used as the DTMF
and [#],[*]	keypad. If a key is	keypad. If a key is
	pressed during trans-	pressed during trans-
	mission, the DTMF	mission, the DTMF
	corresponding to the	corresponding to the
	key that was pressed	key that was pressed
	is sent. (keypad model)	is sent. (keypad model)
[ENCODER]	Changes channel.	Changes channel.

Note: If a [S],[A],[B],[C] key is pressed during transmission, the DTMF corresponding to the key that was pressed is sent.

LCD indicator

"SCN" Unused

"Lights at Compander ON.
"LO" Lights at RF Power Low.

"P" Unused

"MON" Lights at moniter ON.

"SVC" Unused "□" Unused

LED indicator

Red LED Lights during transmission. Blinks at the low

battery voltage warning.

Green LED Lights when there is a carrier.

Sub LCD indicator

"FCN"

appears at Function ON.

"n"

appears at Narrow ON.

■ Frequency and signalling

The set has been adjusted for the frequencies shown in the following table. When required. re-adjust them following the adjustment procedure to obtain the frequencies you want in actual operation.

Frequency (MHz)

Channel No.	K, K4, M					
Chamerno.	RX	TX				
1	470, 05000	470. 10000				
2	470. 05000	450. 10000				
3	489. 95000	489. 90000				
4	470. 00000	470. 00000				
5	470. 20000	470. 20000				
6	470. 40000	470. 40000				
7~16						

Signalling

019110111119		
Signalling No.	RX	TX
1	None	None
2	None	100Hz square
3	LTR data	LTR data
4	QT 67.0Hz	QT 67.0Hz
5	QT 151.4Hz	QT 151.4Hz
6	QT 210.7Hz	QT 210.7Hz
7	QT 250.3Hz	QT 250.3Hz
8	DQT D023N	DQT D023N
9 -	DQT D754I	DQT D754I
10	DTMF DEC, (159D)	DTMF ENC, (159D)
11	None	DTMF tone 9
12	2 tone 321.7/928.1Hz	None
13	Single tone 1200Hz	Single tone 1200Hz
14	None	MSK
15	MSK code ,	MSK code

· Preparations for tuning the transceiver

Before attempting to tune the transceiver, connect the unit to a suitable power supply.

Whenever the transmitter is turned, the unit must be connected to a suitable dummy load (i.e. power meter).

The speaker output connector must be terminated with a 16Ω dummy load and connected to an AC voltmeter and an audio distortion meter or a SINAD measurement meter at all times during tuning.

Transceiver tuning

(To place transceiver in tuning mode)

Channel appears on LCD. Set channel according to tuning requirements.

LCD display (Test mode)

channel No. Signalling No.

Press [S], now in tuning mode. Use [◀ B] button to write tuning data through tuning modes, and channel selector knob to adjust tuning requirements (1 to 256 appears on LCD).

Use [C ▶] button to select the adjustment item through tuning modes. Use [A] button to adjust 3 or 5 point tuning, and use [LAMP] button to switch between Wide/Narrow.

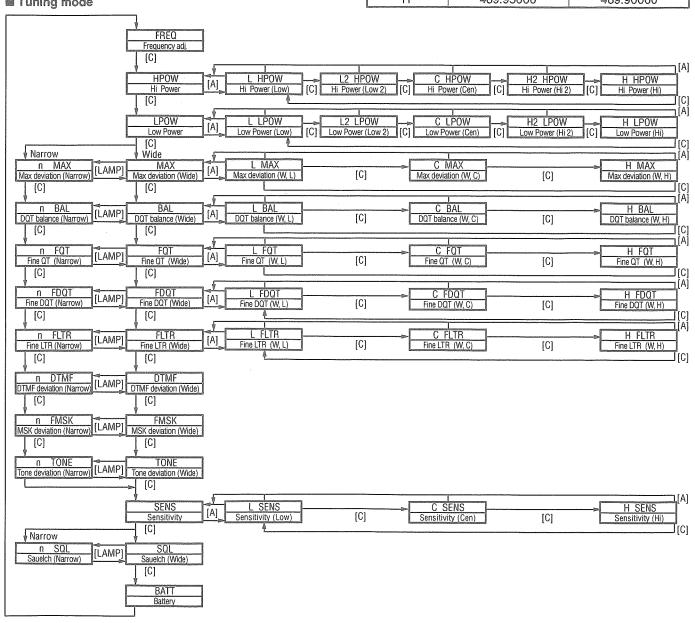
LCD display (Tuning mode)



Panel Tuning Mode

	UHF						
TEST Ch	RX frequency (MHz)	TX frequency (MHz)					
L	450.05000	450.10000					
L2	460.05000	460.10000					
С	470.05000	470.10000					
H2	480.05000	480.10000					
Н	489.95000	489.90000					

■ Tuning mode



TK-380

ADJUSTMENT

Common Section

liom	Condition		Measureme	ent	Adjustment			Specifications/
ltem	Condition	Test equipment	Unit	Terminal	Unit	Parts	Method	Remark
1. Setting	1) BATT terminal voltage:7.5V							-
	2) SSG Standard modulation							
	[Wide] MOD:1kHz, DEV:3kHz							
	[Narrow] MOD:1kHz, DEV:1.5kHz							
2. VCO lock	[Panel Test Mode]							
voltage	1) CH-Sig:2-1	Power meter	Panel	ANT			Check	0.8V or more
RX	2) CH-Sig:3-1	DVM	TX-RX	CV (CN14)				4.4V or less
TX	3) CH-Sig:2-1							0.8V or more
	PTT:ON							
	4) CH-Sig:3-1							4.4V or less
	PTT:ON							

Transmitter Section [Panel Tuning Mode except when Panel TEST Mode is specified.]

Item	Condition	N	/leasuremen	t		Adjustmer	ît	Specifications
ILCIII	Condition	Test equipment	Unit	Terminal	Unit	Parts	Method	Remark
1. Frequency	1) Adj item [FREQ]	Power meter	Panel	ANT	Panel	Encoder	Center frequ	ency
Adjust	Adjust [***]	Am meter				knob	(470.1000 M	Hz) ± 100Hz
	PTT:ON							
2. Max Power	1) Adj item [HPOW]	-					Check	4.3W or more
Check	Adjust [256]							
	2) Adj item							
	[L HPOW] → [L2 HPOW] -	→ [C HPOW] → [H2 HPOW] -	→ [H HPOW]				
	Adjust [256]	1	1					
	PTT:ON							
3. Hight Power	1) Adj item [HPOW]					Encoder	4.0W	±0.1W
Adjust	Adjust [***]					knob		2.2A or less
	2) Adj item							
	[L HPOW] → [L2 HPOW] -	→ [C HPOW] → [H2 HPOW] -	→ [H HPOW]				
	Adjust [***]							
	PTT:ON	PACIFICATION AND ADMINISTRATION				-		
4. Hight Power	[Panel Test Mode]							
Check	1) CH-Sig:1-1						Check	3.0~5.0W
	PTT:ON							2.3A or less
	2) CH-Sig:2-1				***************************************			
	PTT:ON							
	3) CH-Sig:3-1							
	PTT:ON							
5. Low Power	1) Adj item [LPOW]				Panel	Encoder	0.8W	±0.1W
Adjust	Adjust [***]					knob		1.0A or less
	2) Adj item							
	[L LPOW] → [L2 LPOW] →	[C LPOW] → [H	l2 LPOW] →	[H LPOW]				
	Adjust [***]							
	PTT:ON							
6. Low Power	[Panel Test Mode]							
Check	1) CH-Sig:1-1						Check	0.5~1.5W
	Set low power (Push [B])							1.2A or less
	PTT:ON							
	2) CH-Sig:2-1							
	PTT:ON							
	3) CH-Sig:3-1							
	PTT:ON							

[Panel Tuning Mode except when Panel TEST Mode is specified.]

			Measureme		VVIICITI		······································	speciliea.j
Item	Condition	Test equipment	7	Terminal	Unit	Adjustment Parts	Method	Specifications/ Remark
7. Max DEV	1) Adj item [MAX]	Power meter	Panel	ANT	Panel	Encoder	3.8kHz	±50Hz
Adjust	Adjust [***]	Dev meter	T distor	universal	anoi	knob	(According to	150112
[Wide]	AG:1kHz / 150mV	Oscilloscope		connector		KIIOD	the larger +,-)	
[vvide]	Dev meter filter	AG		Connector			the larger +,-)	
	LPF:15kHz	AF VTVM						
	HPF:OFF	AFVIVIVI						
	2) Adj item	A A V 1						
	[L MAX] → [C MAX] → [H M Adjust [***]	VIAN]						
	PTT:ON						75111	
[Narrow]	1) Adj item [n MAX]						1.75kHz	
HATCH THE TOTAL	Adjust [***]						(According to	
0.140	PTT:ON						the larger+,-)	
8. MIC	[Panel Test Mode]						Check	2.2~3.6kHz
Sensitivity	1) CH-Sig:1-1							
Check	AG:1kHz / 15mV							
	LPF:15kHz							
0.0075	PTT:ON			 A D I =			h.,	
9. DQT Balance	1) Adj item [BAL]	Power meter	Panel	ANT	Panel	Encoder	Make the	
Adjust	Adjust [***]	Dev meter		universal		knob	demodulation	
[Wide]	LPF:3kHz	Oscilloscope		connector			waves into	
	HPF:OFF	AG					square waves.	
NAME OF THE PROPERTY OF THE PR	2) Adj item	AF VTVM						
	[L BAL] → [C BAL] → [H BA	AL]						
	Adjust [***]							
	PTT:ON							
[Narrow]	3) Adj item [n BAL]							
	Adjust [***]							
	PTT:ON							
	1) Adj item [FQT]						Wide:0.75kHz	±50Hz
Adjust	Adjust [***]						Narrow:0.35kHz	
	LPF:3kHz							
	HPF:OFF							
[Wide]	2) Adj item							
	[L FQT] → [C FQT] → [H F	QT]						
	Adjust [***]	*						
	PTT:ON							
[Narrow]	3) Adj item [n FQT]							
	Adjust [***]							
	PTT:ON							
11.DQT	1) Adj item [FDQT]							
Devition	Adjust [***]							
Adjust	LPF:3kHz							
	HPF:OFF							
[Wide]	2) Adj item							
	[L FDQT] → [C FDQT] → [l	H FDQT]						
	Adjust [***]							
	PTT:ON							
[Narrow]	3) Adj item [n FDQT]							
	Adjust [***]							
	PTT:ON							

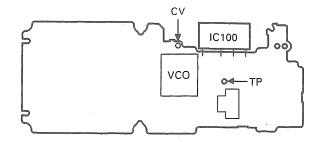
[Panel Tuning Mode except when Panel TEST Mode is specified.]

Item	Condition		Neasuremei			Adjustmen		Specifications/
		Test equipment	 	Terminal	Unit	Parts	Method	Remark
I2.LTR	1) Adj item [FLTR]	Power meter	Panel	ANT	Panel	Encoder	1.0kHz	±0.1kHz
Deviation	Adjust [***]	Dev meter		universal		knov		
Adjust	LPF:3kHz	Oscilloscope		connector				
	HPF:OFF	AG						
[Wide]	2) Adj item	AF VTVM						
	[L FLTR] → [C FLTR] → [H	FLTR]						
	Adjust [***]							
	PTT:ON							
[Narrow]	3) Adj item [n FLTR]						0.75kHz	±50Hz
	Adjust [***]							
	PTT:ON							
13.DTMF	1) Adj item [DTMF]						Wide:2.5kHz	±0.1kHz
Deviation	Adjust [***]						Narrow:1.25kHz	
Adjust	LPF:15kHz							
[Wide]	HPF:OFF							
	PTT:ON							
[Narrow]	2) Adj item [n DTMF]							
	Adjust [***]							
	PTT:ON							
14.MSK	1) Adj item [FMSK]							
Deviation	Adjust [***]							
Adjust	LPF:15kHz							
[Wide]	HPF:OFF							
	PTT:ON							
[Narrow]	2) Adj item [n FMSK]							
	Adjust [***]							
	PTT:ON							
15.TONE	1) Adj item [TONE]							
Deviation	Adjust [***]							
Adjust	LPF:15kHz							-
[Wide]	HPF:OFF							
	PTT:ON							74
[Narrow]	2) Adj item [n TONE]							
	Adjust [***]							
	PTT:ON							
16.BATT	1) Adj item [BATT]	Power meter	Panel	ANT	Panel	Encoder	After pressing	BATT terminal
Detection	Adjust [***]	DVM		BATT		knob	the PTT switch,	
Writing	PTT:ON			terminal		-	confirm that	
•				-			one predeter-	
							mined numeric	
						***	in the range 1	
						-	to 256 appears	ŀ
			,				and then press	i e
							[B] key. That	
							numeric will be	***************************************
				-			stored in memory.	The state of the s
17.BATT	[Panel Test Mode]						Check	No blinking of LE
Detection	1) CH-Sig:1-1							
Check	BATT terminal voltage:6.5V					-		
J., J.	PTT:ON					Parameter and the second and the sec		-
	2) BATT terminal voltage:5.7V							Blinking of LEC
	PTT:ON							January Of LLL

Receiver Section [Panel Tuning Mode except when Panel TEST Mode is specified.]

neceiver :	Section [Panel Tunir	,				**************************************		T
Item	Condition		Aeasuremen	T		Adjustment	,	Specifications/
		Test equipment	Unit	Terminal	Unit	Parts	Method	Remark
Sensitivity	1) Adj item [SENS]	Tracking	Panel	ANT	Panel	Encoder	Adjustment	Ta a sala salah
Adjust	Adjust [***]	generator				knob	Low-edge-f	To peak point
(BPF							Turn a knob	
characteristic)	2) Low-edge frequency	Specturm	TX-RX	TP (CN13)			and make	
	Adj item [L SENS]	analyzer		Need couple			peak point.	111
	Spe-Ana setting			capacitor				1 1 1
	Center-f : 450MHz			(1000PF)			Adjustment	
	Span : 200MHz	AEF ~10.0 d	Bm ATT 1	o de	мкв д	' 20.0 MHz,	Center-f	В
	RBW : 300kHz	10 dB/	T			5.1 dB	Turn a knob	/
	VBW : 100kHz	CAL.ON	[L SE	NSI		+	and make	
	REF level: -10dBm	MARKER Z		IC SEN	101		center level	1 1 1
	ATT : 10dB	20.0 MHZ 5.1 dB	 -	- // C SEN	101	+	5dB above	1 1 1
	Tra-G setting	5.2		[H SENS]			from Low-edge	1 1 1
,	Input level:-30dBm	HBW BOO KHZ		//		4	level.	
	3) Center frequency	NEG PK	1-1-1/4	/			Adjustment	dB
	Adj item [C SENS]	VBW 100 kHz	1 1/4				High-edge-f	
	4) High-edge frequency	1					Turn a knob	
	Adj item [H SENS]	(रहा ^न स्वा	Antinition adjug.				and make	f → L C H . 450 470 490 MHz
POGGAMA							High-edge lev	/el
		SWP 50 ms	SPAN 200	MHZ 450 47	0 490 CENTER	450 MHz	9dB above	
			Band Pass	Filter Charad	cteristic		from Low-edg	je
						<u> </u>	level.	I
2. Sensitivity	[Panel Test Mode]	SSG	Panel	ANT				
Check	1) CH-Sig:1-1	AF VTVM		Universal			Check	12dB SINAD or
	SSG OUT	Oscilloscope		connector				more
	Wide:-118dBm							
	(MOD:1kHz / ±3kHz)							
	Narrow:-117dBm							
	(MOD:1kHz / ±1.5kHz)							
3. Squelch	1) Adj item [SQL]					Encoder	Adjust to point	
Adjust	Adjust [***]					knob	of opening	
	SSG OUT:						squelch.	
	12dB SINAD level							
	2) Adj item [n SQL]							
	Adjust [***]							
	12dB SINAD level						ļ	
4. Squelch	[Panel Test Mode]						Check	Squelch must
Check	1) CH-Sig:1-1							be opened.
	SSG OUT:							
	12dB SINAD level							
	2) SSG OUT:OFF							Squelch must
								be closed.

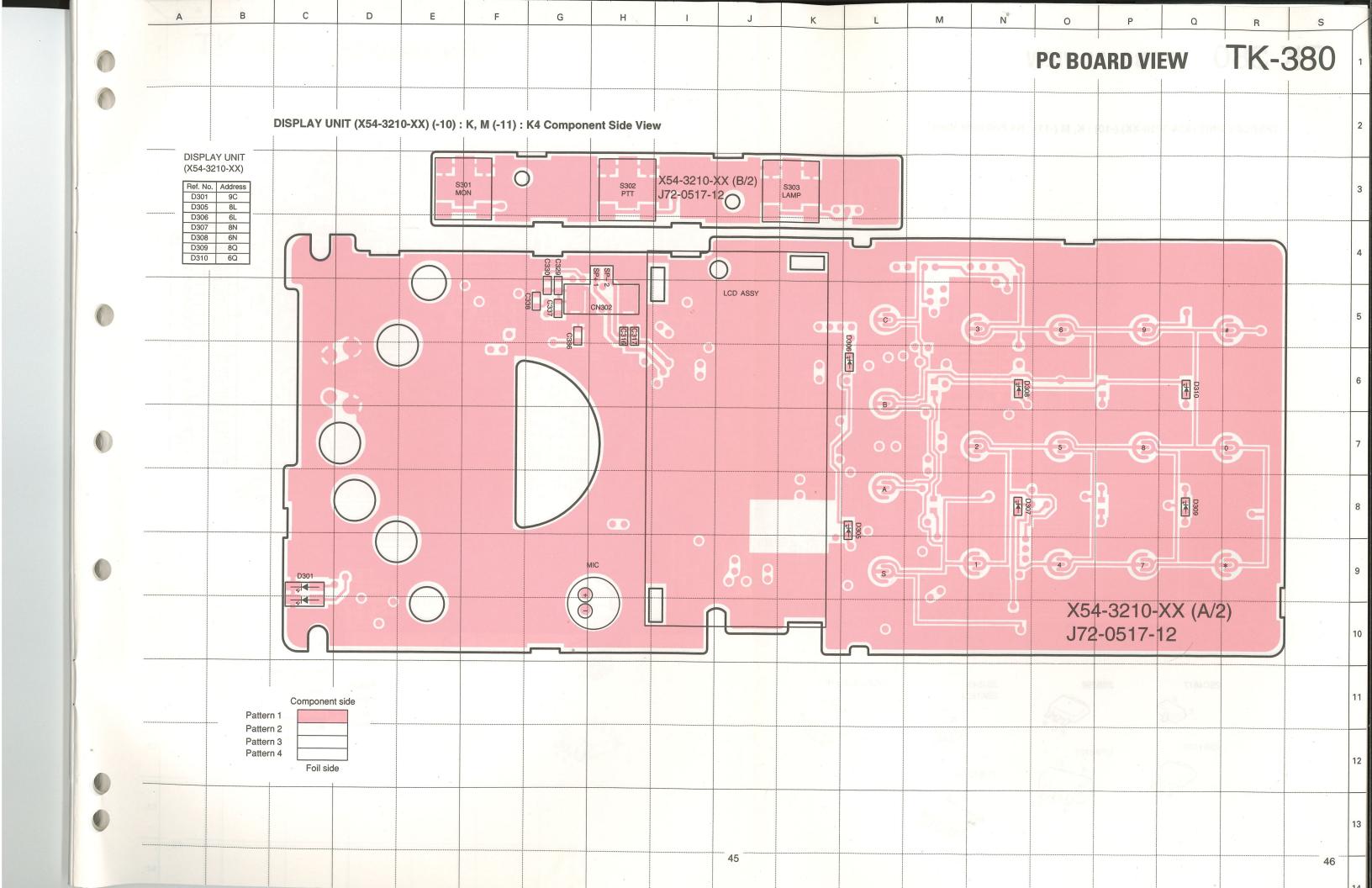
Adjustment points TX-RX unit (X57-5750) component side view

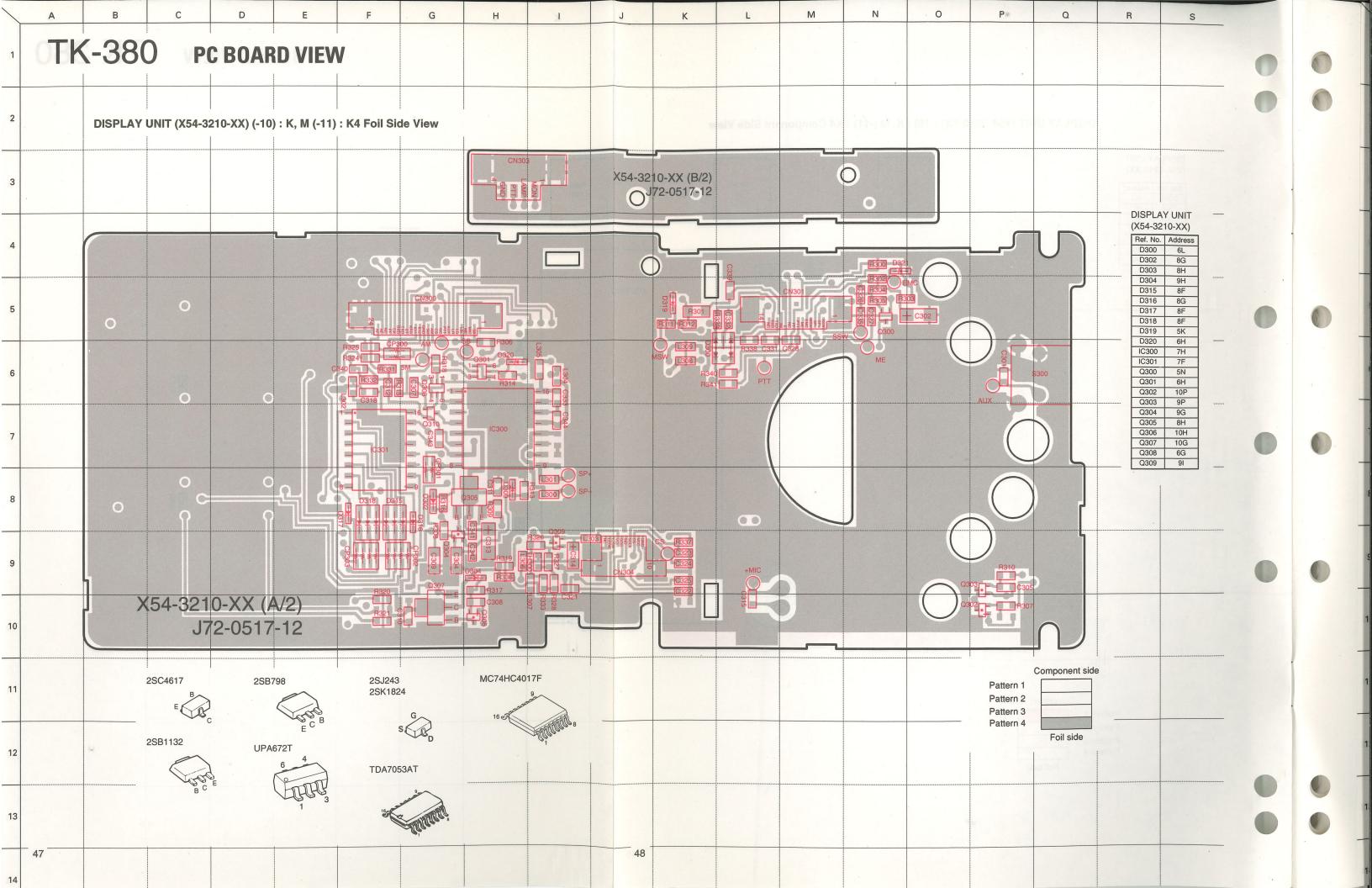


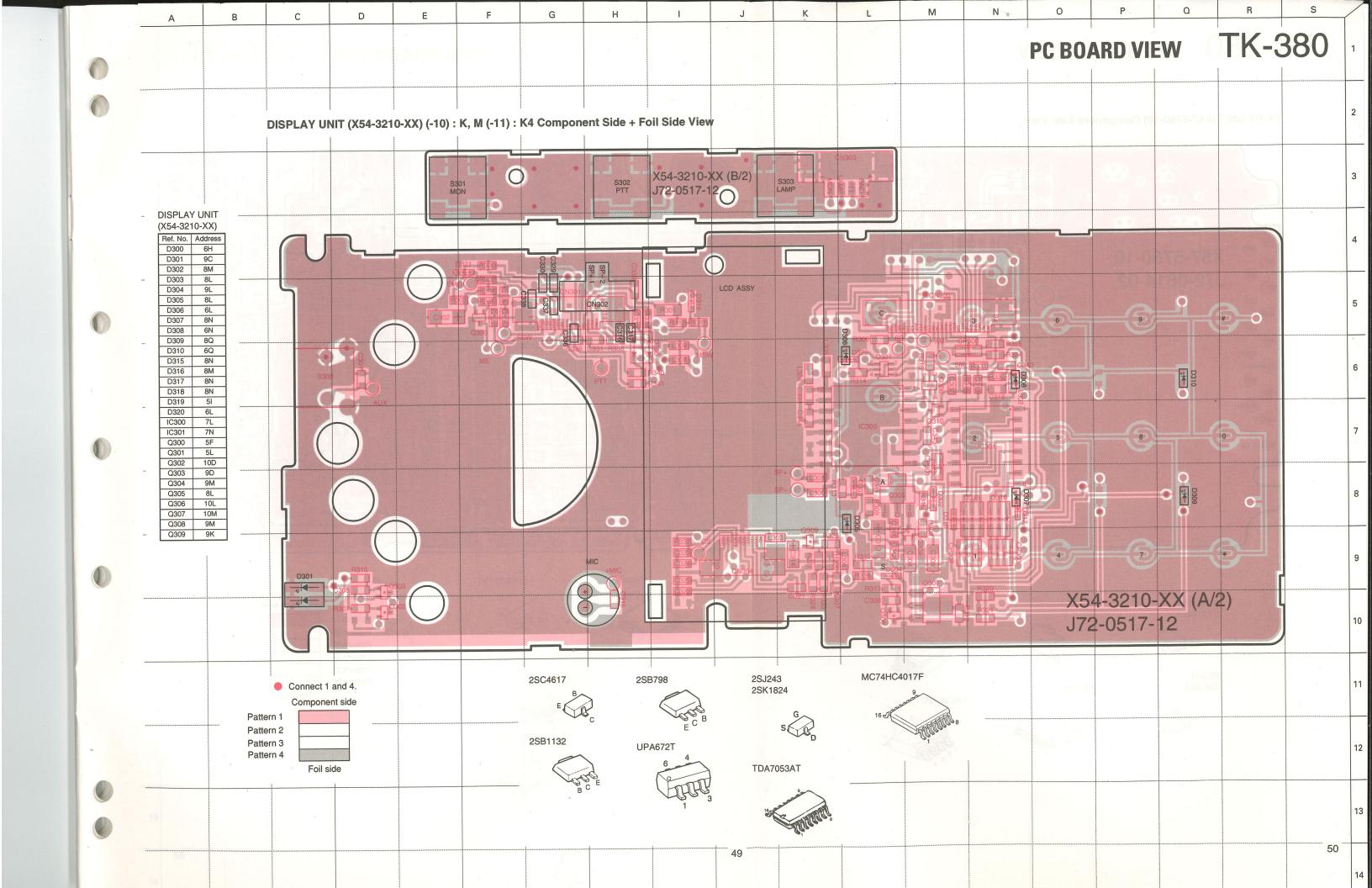
TERMINAL FUNCTION

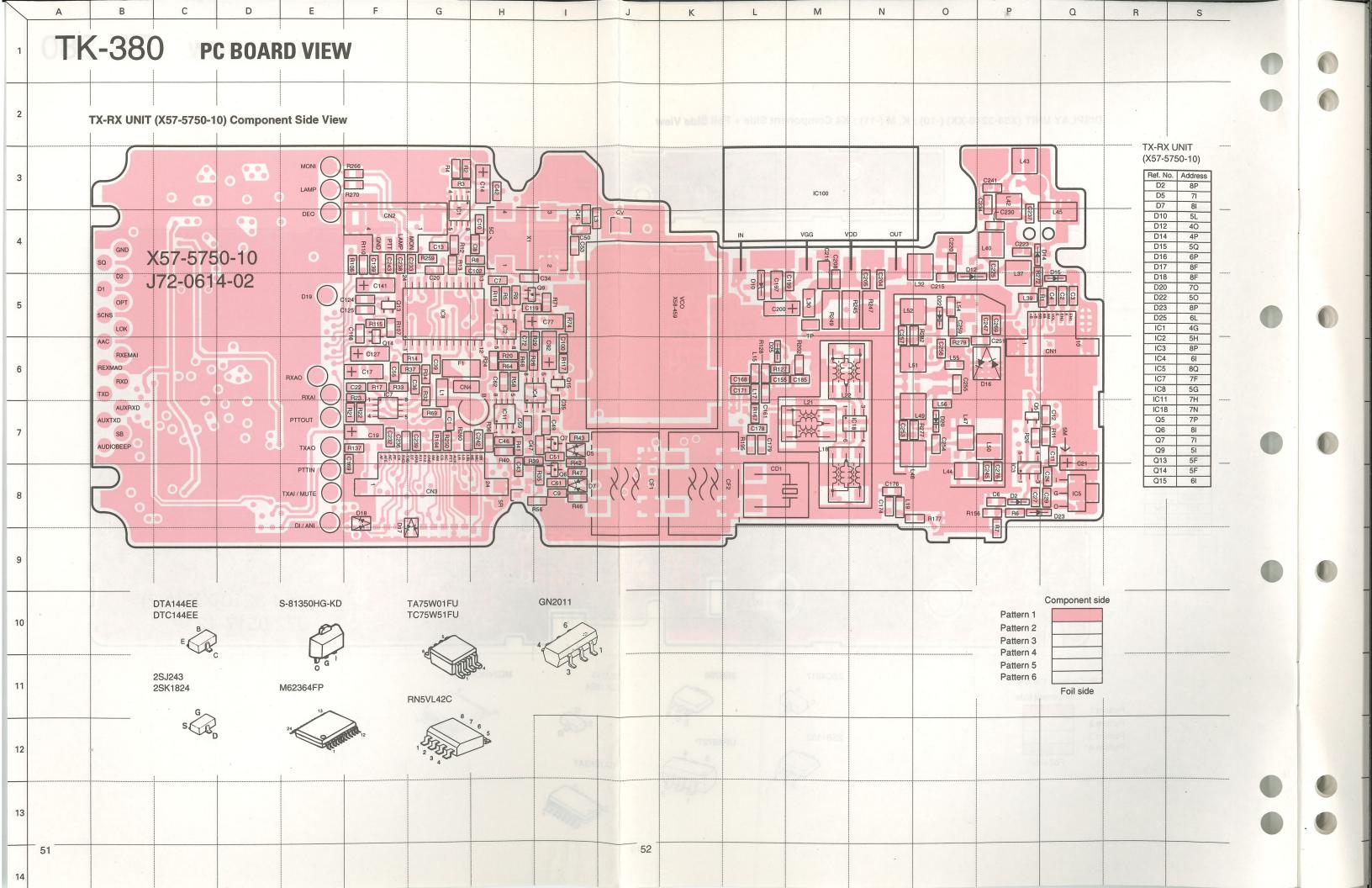
CN No.	Pin No.		1/0	Function
	TX-RX	UNIT	(X57	-5750-XX): TX-RX section
CN1	1	В	0	Power input after passing through
				the fuse.
	2	В	0	Power input after passing through
				the fuse.
	3	SB	1	Power output after power switch.
	4	SB	1	Power output after power switch.
	5	5M	0	5V.
	6	VOL	ı	Volume level input for audic
				control.
	7	E	-	GND
	8	EN2	1	Encoder pulse input.
	9	E	_	GND
	10	EN1		Encoder pulse input.
CN2	1	MON		Normally; 5V. MON when
for X54-	·			connected GND.
SW	2	LAMP		Normally; 5V. LAMP when
section	_			connected GND.
	3	PTT		Normally; 5V. transmit when
		• • •		connected GND.
	4	GND	_	GND
CN3	1	AF	0	Audio output.
2.10	2	AFE	_	Audio GND.
for	3	NC	_	Not use.
X54-	4	PF	1	External PF signal input.
Display	5	CK	o	Clock data output.
unit	6	RXD	Ĭ	Serial control signal input.
anne	7	TXD	o	Serial control signal output.
	8	DT	0	Data output for LCD driver/decade
	0	01		counter.
	9	KRS	0	Key scan IC reset output.
	10	KI1	ĭ	KEY input
	11	KI2		KEY input
	12	GND		GND
	13	5M	0	5V.
	14	AM	0	Audio mute signal output.
	1→	CIAI		Mute: "L". Unmute: "H"
	15	cs	0	LCD driver chip select output.
	16	NC	\ \	· ·
	17		-	Not use.
		PTT	1	PTT signal input.
	18	AUX	1	AUX key input.
	19	LR	0	TX LED control. Normally: 0V
	00	1.0		lighting: 5V.
	20	LG	0	RX LED control. Normally: 0V
	6.4			lighting: 5V.
	21	LBL	0	Backlight LED control.
				Normally: 0V, lighting: 5V.
	22	ME	-	MIC GND.
	23	MIC		MIC signal input.
FBC 1	24	SB	0	Power output after power switch.
				210-XX A/2) : DISPLAY section
CN300	1	SB		Power input after power switch.
	2	MIC	0	MIC signal output.
for	3	ME	-	MIC GND.
X57-	4	LBL	1	Backlight LED control.
TX-RX	_			Normally: 0V, lighting: 5V.
	5	LG	1	RX LED control. Normally: 0V, lighting: 5V
unit	1	!		
unit	6 7	LR AUX	0	TX LED control. Normally: 0V, lighting: 5V AUX key output.

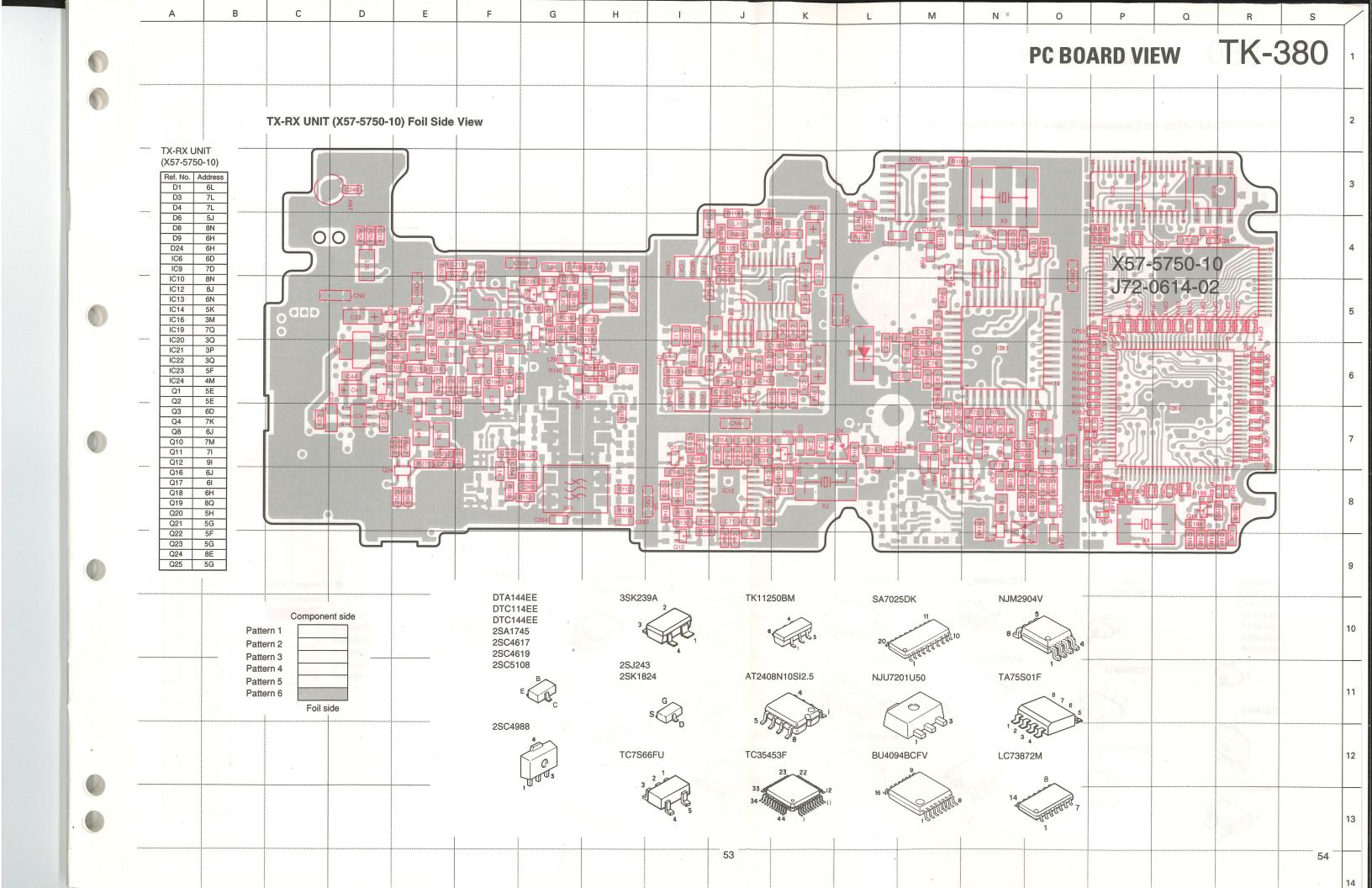
CN No.	Pin No.	Name	I/O	Function
	8	PTT	0	PTT signal output.
	9	NC	-	Not use.
	10	cs	1	LCD driver chip select input.
	11	AM	ı	Audio mute signal input.
				Mute: "L", Unmute: "H"
	12	5M	1	5V.
	13	GND	-	GND
	14	KI2	0	KEY output
	15	KI1	0	KEY output
	16	KRS	1	Key scan IC reset input
	17	DT .	1	Data input for LCD driver/decade counter.
	18	TXD	1	Serial control signal input.
	19	RXD	0	Serial control signal output.
	20	CK		Clock data input.
	21	PF	0	External PF signal output.
	22	NC	-	Not use.
	23	AFE	-	Audio GND.
	24	AF	1	Audio input.
CN301	1	SSW	_	EXT/INT speaker switch input.
	2	SP+	0	BTL output + for external speaker.
	3	SP-	0	BTL output - for external speaker.
	4	MSW	ı	EXT/INT MIC switch input.
	5	EMC		External microphone input.
	6	ME	-	External microphone ground.
	7	PTT	l	External PTT input.
	8	PF		Programmable function key input.
	9	NC	-	Not use.
	10	E	-	GND
dia di dia dia dia dia dia dia dia dia d	11	5M	0	5V output
	12	TXD	0	Serial data output.
	13	RXD	1	Serial data input.
011000	14	NC (E)	-	Not use (GND)
CN302	1	SP	0	Output for internal speaker.
CN304	2	E NC	-	GND
UN304		LEDK	-	Not use.
	2 3	LEDA	0	Backlight LED control. Backlight LED control.
	4	VCI	0	LCD power supply.
	5	SOD	0	Serial data output for LCD driver.
	6	SID	ı	Serial data input for LCD driver.
	7	SCLK	0	Clock data output for LCD driver.
Withdeal	8	CS	0	LCD driver chip select output.
	9	Vcc	0	5V
	10	GND	_	GND
		AY UNIT	(X5	4-3210-XX B/2) : SW section
CN303	1	MON	0	Normally; 5V.,MON when connected GND.
For X57-	2	LAMP	0	Normally; 5V, LAMP when connected GND.
TX-RX	3	PTT	0	Normally; 5V, transmit when connected GND.
unit	4	GND	-	GND

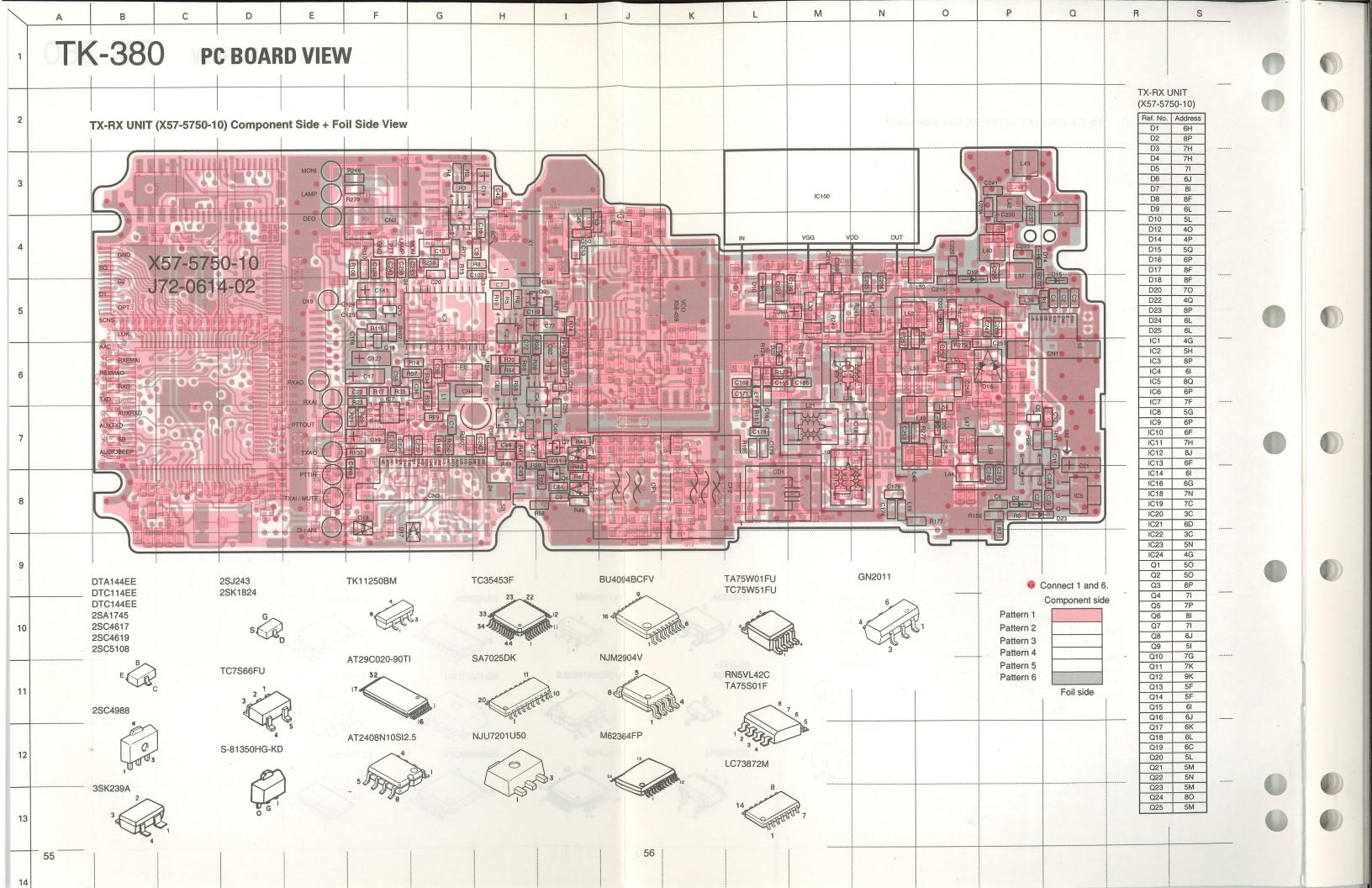


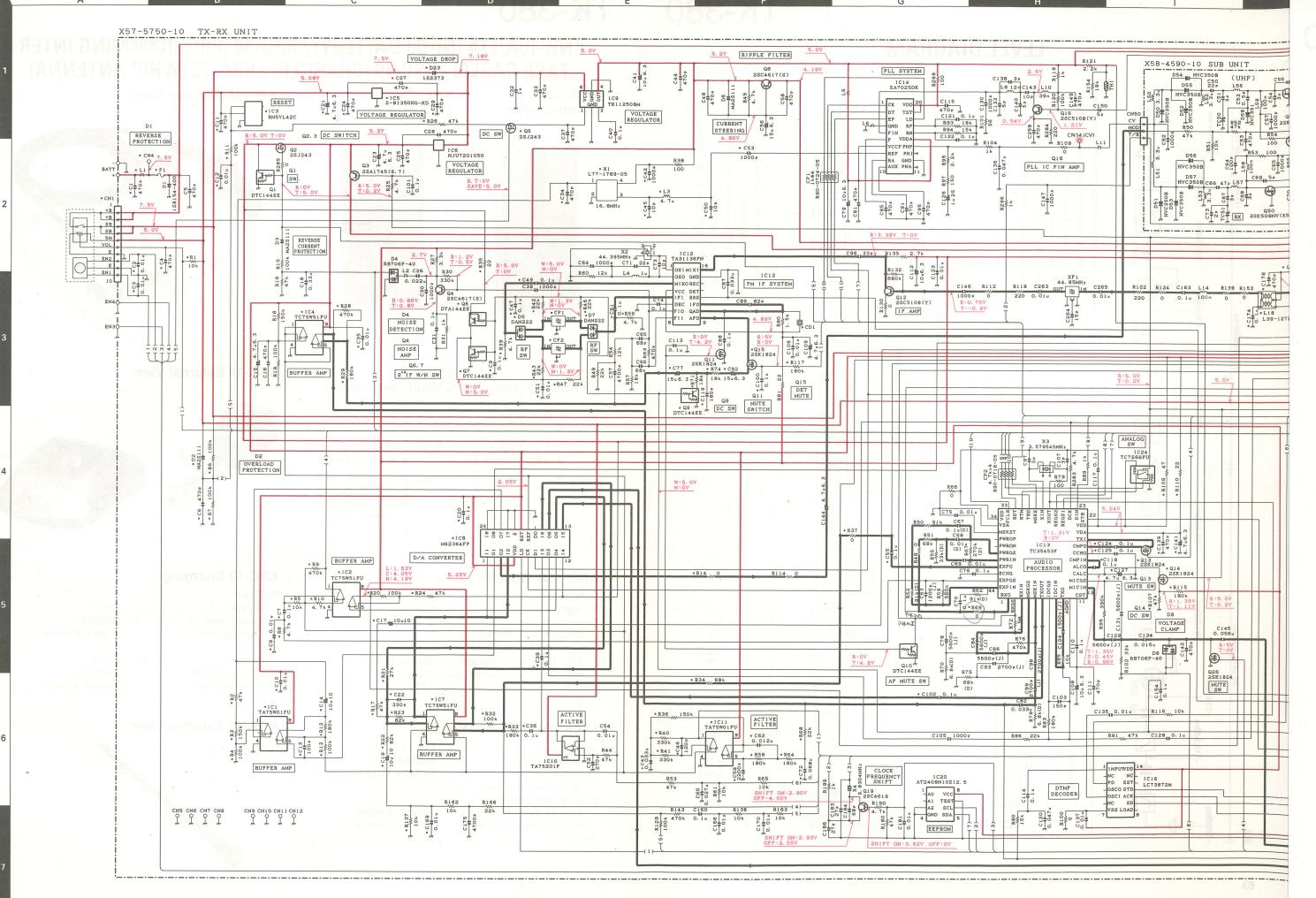


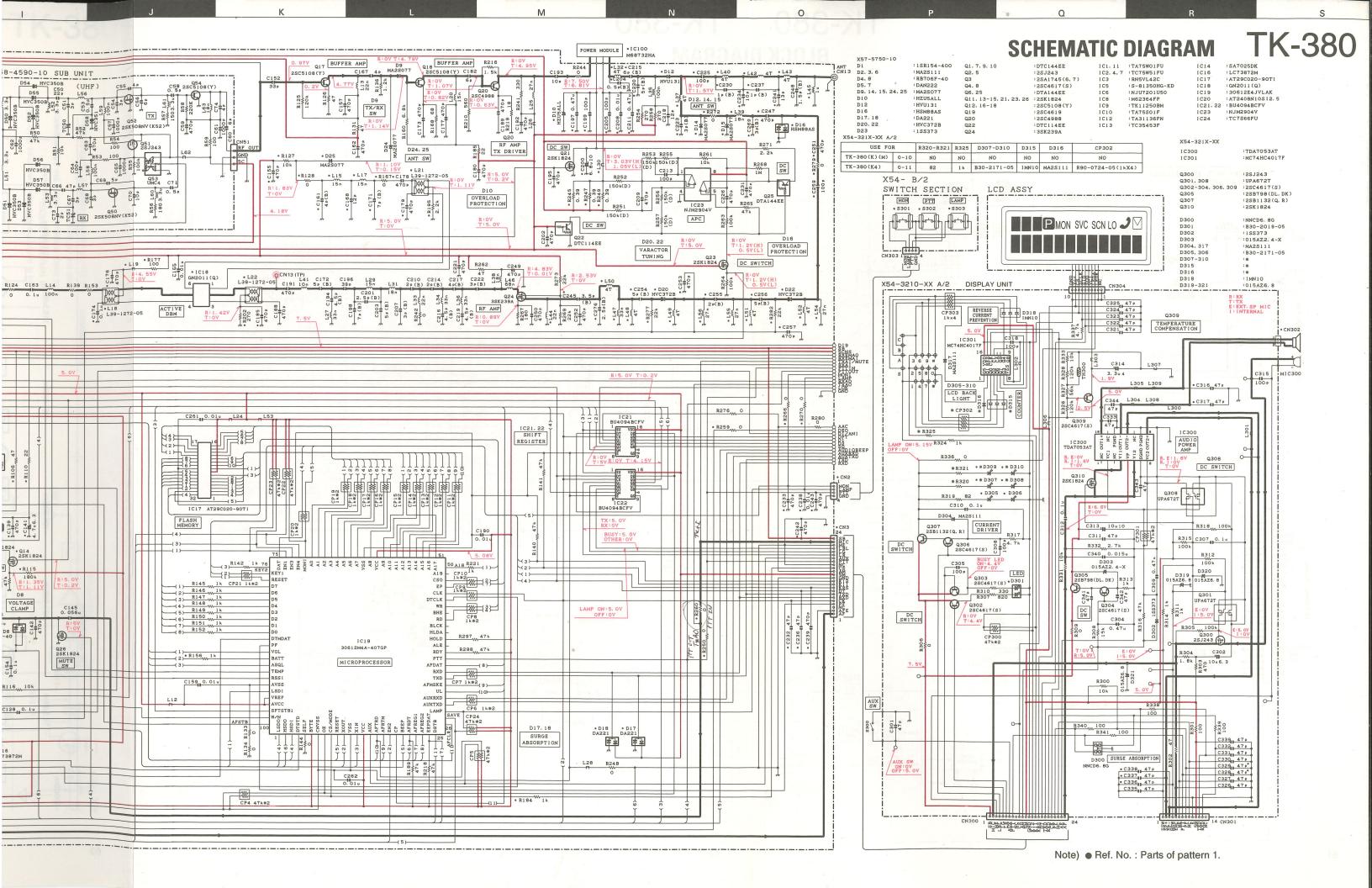






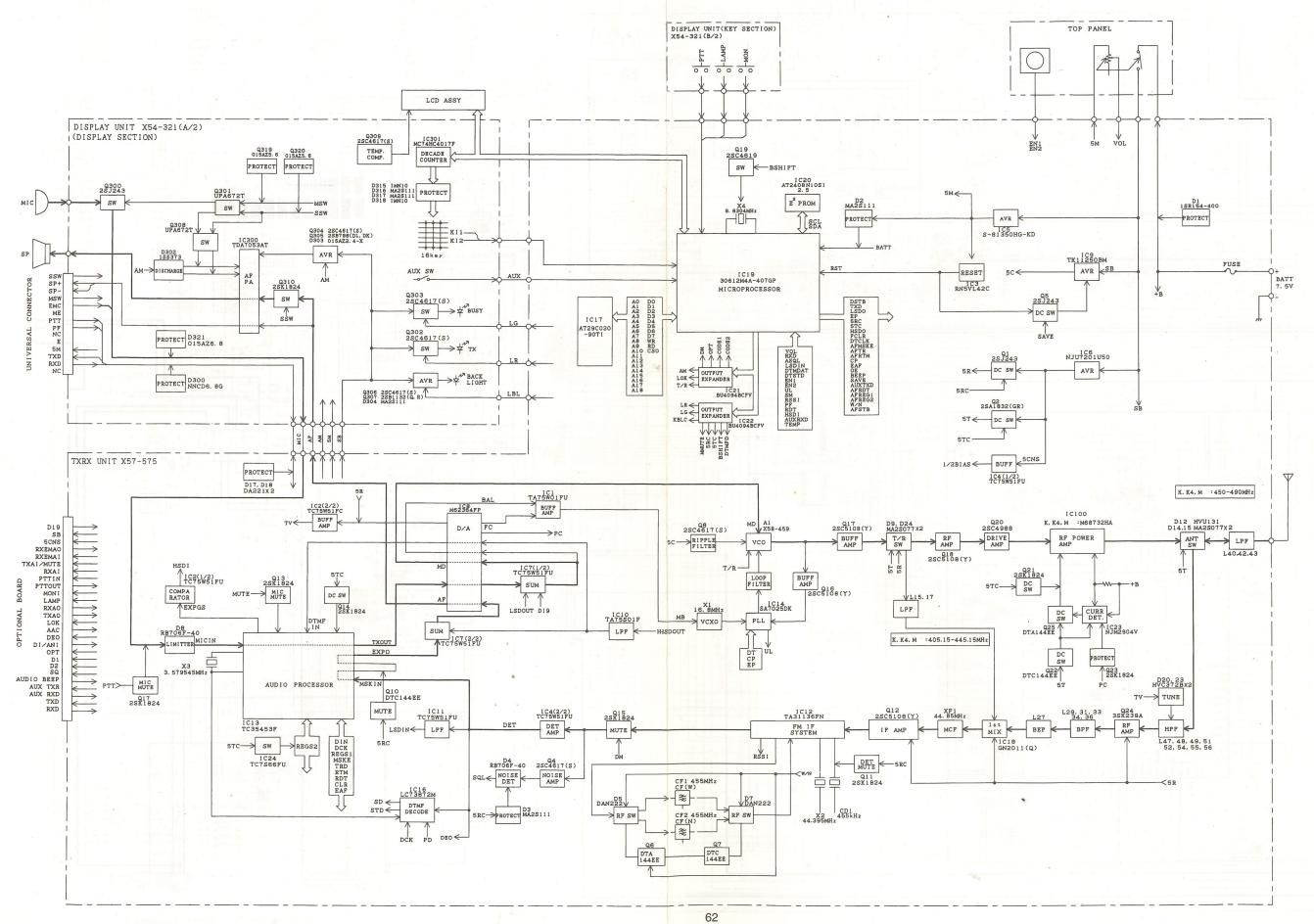




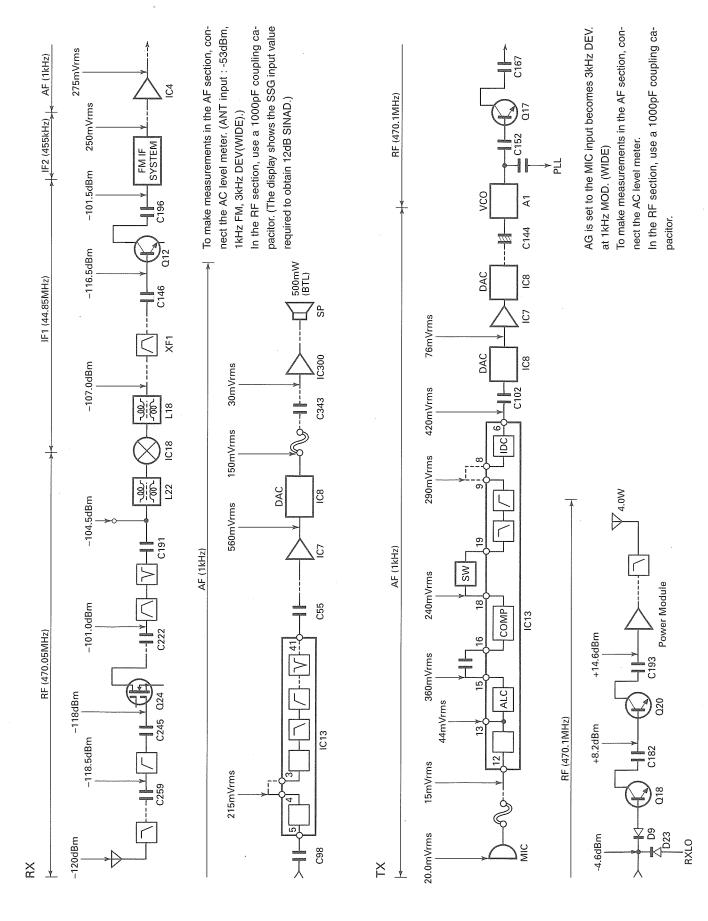


TK-380 TK-380

BLOCK DIAGRAM



LEVEL DIAGRAM



KNB-16A/17A (Ni-Cd BATTERY) / KPG-36 (PROGRAMMING INTER-FACE CABLE) / KSC-19 (CHARGER) / KRA-15 (WHIP ANTENNA)

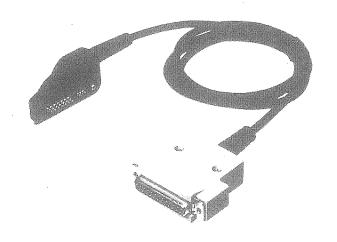
KNB-16A External View

KNB-17A External View









KNB-16A Circuit Diagram

Discharge pin side

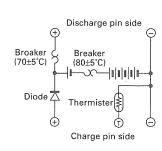
Broaker (70±5°C)

Diode

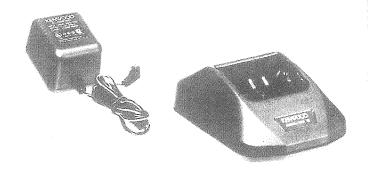
Thermister

Charge pin side

KNB-17A Circuit Diagram



KSC-19 External View



KNB-16A Specifications

Voltage	7.2V (1.2V x 6)
Charging current	
Dimensions (mm)	58 W x 110.8 H x 17.2 D
(Projections included)	
Charger and charging time	
KSC-19 (Normal Charger)	Approx. 8 hours
KSC-20 (Rapid Charger)	Approx. 1 hour
Weight	180g

KNB-17A Specifications

Voltage	7.2V (1.2V x 6)
Charging current	1500mAh
Dimensions (mm)	58.0 W x 110.8 H x 20.0 D
(Projections included)	
Charger and charging time	
KSC-19 (Normal Charger)	Approx. 8 hours
KSC-20 (Rapid Charger)	Approx. 1.3 hour
Weight	220g

KSC-19 Charging

9 9	
KNB-16A	
Voltage	7.2V
Battery capacity	1100mAh
Charging time	Approx. 8 hours
KNB-17A	
Voltage	7.2V
Battery capacity	1500mAh
Charging time	Approx. 8 hours

KRA-15 External View



Externa

Spec Microp Imp Ser Speak Imp

> Ma Dimel Weigl

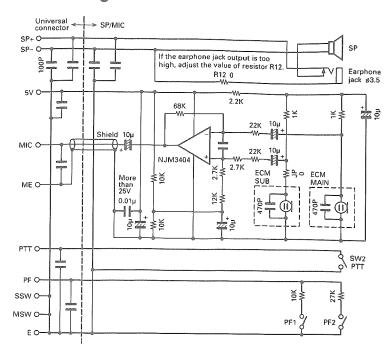
ER-

KMC-25 (SPEAKER MICROHONE)

External View



Circuit Diagram



Specifications

Microphone	
Impedance	2kΩ
Sensitivity	
Speaker	
Impedance	16Ω
Input	
Maximum input	
Dimensions	
Weight (With plug cord)	Approx. 0.17kg

SPECIFICATION

General

Receiver (Measurements made per EIA-RS 316B)

Transmitter (Measurements made per EIA-RS 316B)

 RF Power Output
 4W

 Low
 1W

 RF Output Impedance
 50Ω

 Spurious
 -70dB

 Modulation (Wide/Narrow)
 16K0F3E/11K0F3E

 FM Noise (Wide/Narrow)
 -45dB/-40dB

 Audio Distortion (Wide/Narrow)
 Less then 3%/5%

 Frequency Stability
 ±0.00025% (-30°C to +60°C)

 Channel Spread
 40MHz

KENWOOD CORPORATION

14-6, Dogenzaka 1-chome, Shibuya-ku, Tokyo 150-8501, Japan

KENWOOD SERVICE CORPORATION

P.O. BOX 22745, 2201 East Dominguez Street, Long Beach, CA 90801-5745, U.S.A.

KENWOOD ELECTRONICS LATIN AMERICA S.A.

P.O. BOX 55-2791 Piso 6 Plaza Chase Cl, 47 y Aquilino de la Guardio Panama, Republic of Panama

KENWOOD ELECTRONICS CANADA INC.

6070 Kestrel Road, Mississauga, Ontario, Canada L5T 1S8

KENWOOD ELECTRONICS DEUTSCHLAND GMBH Rembrücker Str. 15, 63150 Heusenstamm, Germany

KENWOOD ELECTRONICS BELGIUM N.V.

Mechelsesteenweg 418 B-1930 Zaventem, Belgium

KENWOOD ELECTRONICS FRANCE S.A.

13, Boulevard Ney, 75018 Paris, France

KENWOOD ELECTRONICS U.K. LIMITED

KENWOOD House, Dwight Road, Watford, Herst., WD1 8EB United Kingdom

KENWOOD ELECTRONICS EUROPE B.V.

Amsterdamseweg 37, 1422 AC Uithoorn, The Netherlands

KENWOOD ELECTRONICS ITALIA S.p.A.

Via G. Sirtori, 7/9 20129 Milano, Italy

KENWOOD IBERICA S.A.

Bolivia, 239-08020 Barcelona, Spain

KENWOOD ELECTRONICS AUSTRALIA PTY. LTD.

(A.C.N. 001 499 074)

P.O. Box 504, 8 Figtree Drive, Australia Centre, Homebush, N.S.W. 2140, Australia

KENWOOD & LEE ELECTRONICS, LTD.

Unit 3712-3724, Level 37, Tower one Metroplaza, 223 Hing Fong Road, Kwai Fong, N.T., Hong Kong