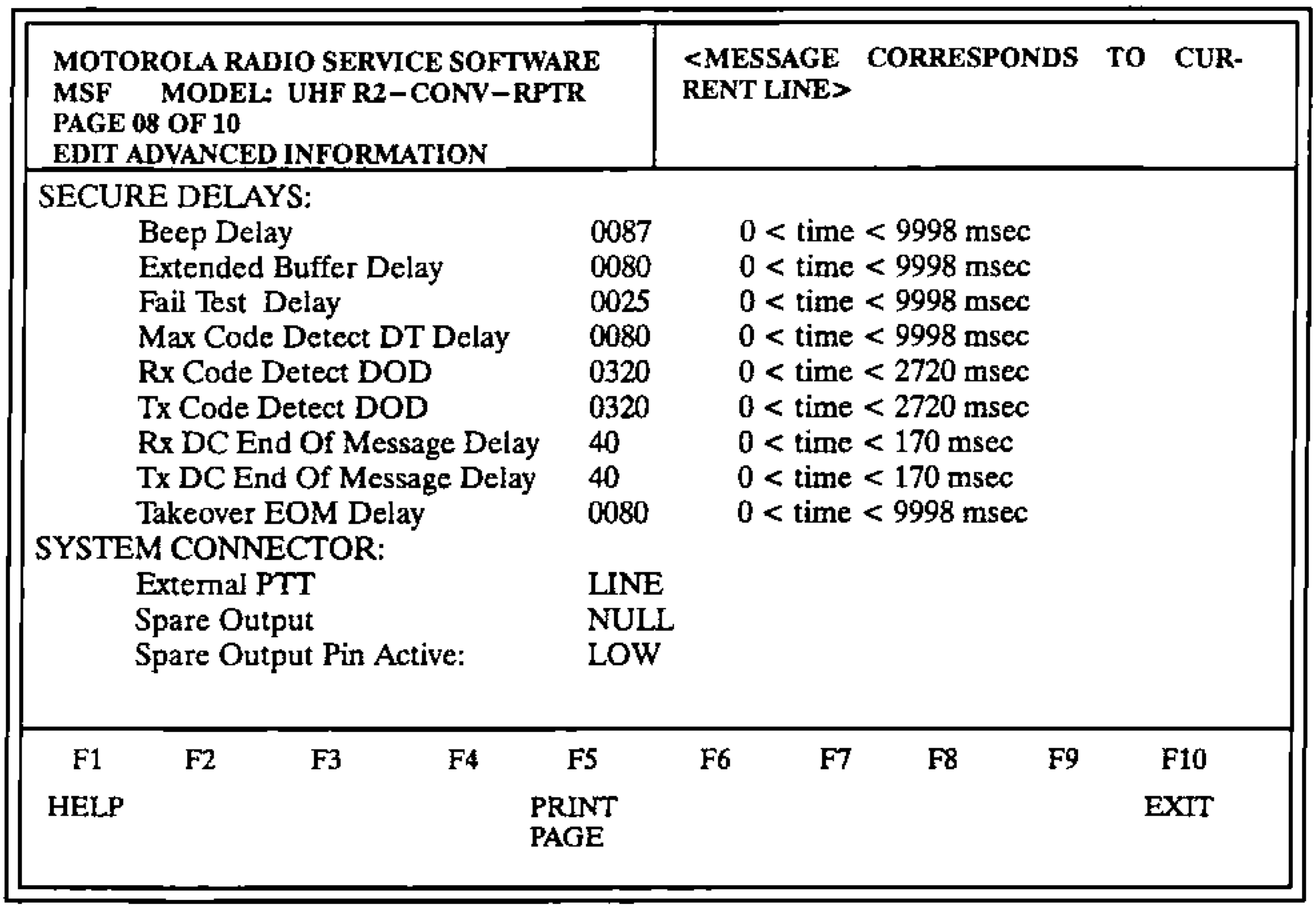

Figure 5.21: Advanced Information Screen #8

5.5.16. System Connector

External PTT

The External PTT field indicates which bit on the MUXbus will be activated when the External PTT input to the station is activated. The External PTT input is pin 12 of the System Connector (J2 on the Junction Box), and is active low. To set a MUXbus bit in response to the External PTT Input, enter MUX, followed by A (indicating the address), followed by the MUXbus address (0–F), followed by B (indicating the bit), followed by the bit number to set (0–3). For example, MUXA2B3 sets bit 3 of MUXbus address 2 (TX PL DS) when the External PTT Input is active, and clears the bit when the input is inactive. Also, the following inputs are valid: LINE (sets bit 2 of MUXbus address 2), TRNK (sets the Trunking PTT bit on the High Speed Ring), and NULL (sets nothing). Only one command may be entered via the RSS. Some SP stations may use more than one command, in order to set multiple bits on the MUXbus in response to the External PTT Input. When reading a codeplug that contains more than one command, the External PTT field will show MULTIPLE and will be non–editable. The default for this field is TRNK for trunking stations and LINE for all others.

Spare Output

The Spare Output field indicates which bit on the MUXbus or High–Speed Ring (HSR) will be used to activate the Spare Output Pin on the station's Junction Box. The Spare Output is pin 9 of the System Connector (J2 on the Junction Box); see Appendix J for Spare Output Active Polarity. To activate the Spare Output in response to a MUXbus bit being active, enter MUX, followed by A (indicating the address), followed by the MUXbus address (0–F), followed by B (indicating the bit), followed by the bit number to read (0–3). For example, MUXA2B3 activates the Spare Output when bit 3 of MUXbus address 2 (TX PL DS) is active, and clears the Spare Output when it is inactive. To activate the Spare Output in response to a High Speed Ring (HSR) bit being active, enter HSR, followed by A (indicating the address), followed by the HSR address (0–4), followed by B (indicating the bit), followed by the bit number to read (0–7). For example, HSRA0B5 activates the Spare Output when bit 5 of HSR address 0 (TSTAT) is active, and clears the Spare Output when it is inactive. Also, NULL is a valid input, and it leaves the Spare Output always inactive. Only one command may be entered via the RSS. Some SP station may use more than one command, in order to set the Spare Output when a combination of MUXbus and/or HSR bits are active. When reading a codeplug that contains more than one command, the Spare Output field will show MULTIPLE and will be non–editable. The default for this field is NULL.

See the *MSF 5000* User Manual for a complete description of the MUXbus and High Speed Ring.

Spare Output Pin Active

The Spare Output Pin Level field indicates the "active" polarity level of the spare output signal sent to the Junction Box connector at J2 pin 9. This signal can also be tapped at the TTRC board at J2900 pin 9. The active polarity can be toggled either active HIGH or LOW by means of the UP/DOWN arrow keys. The Spare Output Pin Level field defaults to active LOW.